



01N30

Power MOSFET

0.1A, 300V N-CHANNEL POWER MOSFET

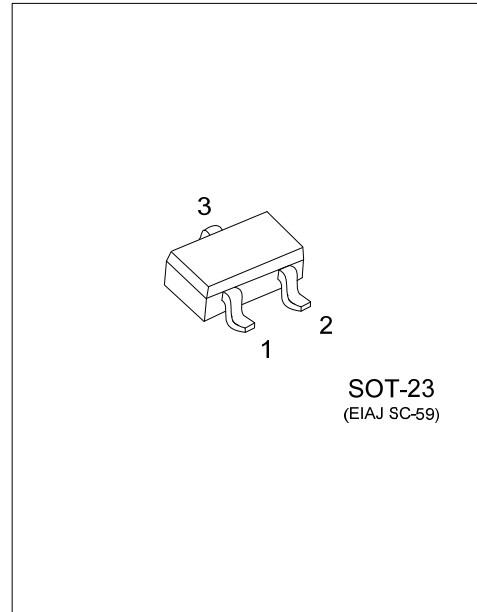
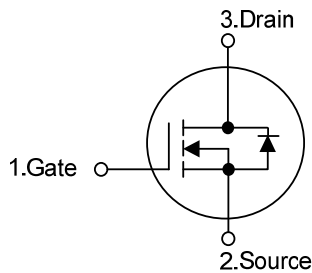
DESCRIPTION

The UTC **01N30** is a planar power MOSFET using UTC's advanced technology to provide customers with a minimum on-state resistance, low gate charge and superior switching performance.

FEATURES

- * $R_{DS(ON)} \leq 7.5 \Omega$ @ $V_{GS}=10V, I_D=50mA$
- * High switching speed
- * 100% avalanche tested

SYMBOL



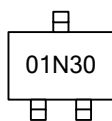
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
01N30L-AE3-R	01N30G-AE3-R	SOT-23	G	S	D	Tape Reel

Note: Pin Assignment: G: Gate S: Source D: Drain

<p>01N30G-AE3-R</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Green Package</p>	<p>(1) R: Tape Reel</p> <p>(2) AE3: SOT-23</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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MARKING



■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V_{DSS}	300	V
Gate-Source Voltage	V_{GSS}	± 30	V
Drain Current	Continuous	0.1	A
	Pulsed (Note 2)	0.2	A
Peak Diode Recovery dv/dt (Note 3)	dv/dt	3.7	V/ns
Power Dissipation	P_D	0.3	W
Junction Temperature	T_J	+150	$^{\circ}\text{C}$
Storage Temperature	T_{STG}	-55 ~ +150	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $I_{SD} \leq 0.1\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^{\circ}\text{C}$

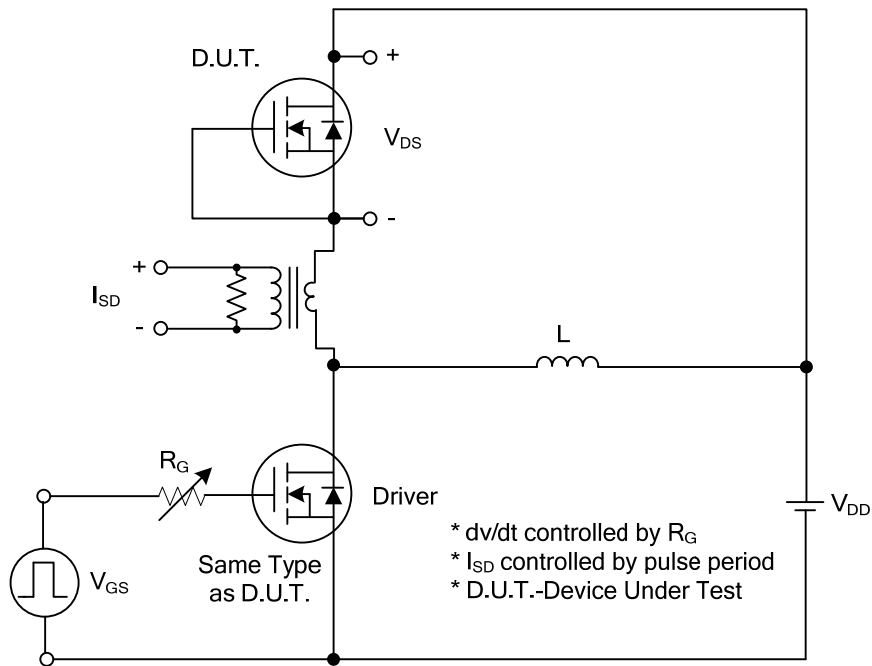
■ ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D = 250\mu\text{A}$, $V_{DS} = 0\text{V}$	300			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS} = 300\text{V}$			10	μA
Gate-Source Leakage Current	I_{GSS}	Forward			± 100	nA
		Reverse			± 100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS} = V_{GS}$, $I_D = 250\mu\text{A}$	1.0		3.0	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS} = 10\text{V}$, $I_D = 50\text{mA}$			7.5	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{GS} = 0\text{V}$, $V_{DS} = 25\text{V}$, $f = 1\text{MHz}$		70		pF
Output Capacitance	C_{OSS}			15		pF
Reverse Transfer Capacitance	C_{RSS}			3.5		pF
SWITCHING PARAMETERS						
Total Gate Charge	Q_G	$V_{DS} = 240\text{V}$, $V_{GS} = 10\text{V}$, $I_D = 0.1\text{A}$ (Note 1, 2)		9.5		nC
Gate to Source Charge	Q_{GS}			0.7		nC
Gate to Drain Charge	Q_{GD}			0.3		nC
Turn-ON Delay Time	$t_{D(ON)}$	$V_{DS} = 100\text{V}$, $V_{GS} = 10\text{V}$, $I_D = 0.1\text{A}$, $R_G = 25\Omega$ (Note 1, 2)		2.5		ns
Rise Time	t_R			17		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			28		ns
Fall-Time	t_F			150		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				0.1	A
Maximum Body-Diode Pulsed Current	I_{SM}				0.2	A
Drain-Source Diode Forward Voltage	V_{SD}	$I_S = 0.1\text{A}$			1.4	V
Reverse Recovery Time	t_{rr}	$V_{GS} = 0\text{V}$, $I_S = 0.1\text{A}$,		56		ns
Reverse Recovery Charge	Q_{rr}	$di_F/dt = 100\text{A}/\mu\text{s}$		30		nC

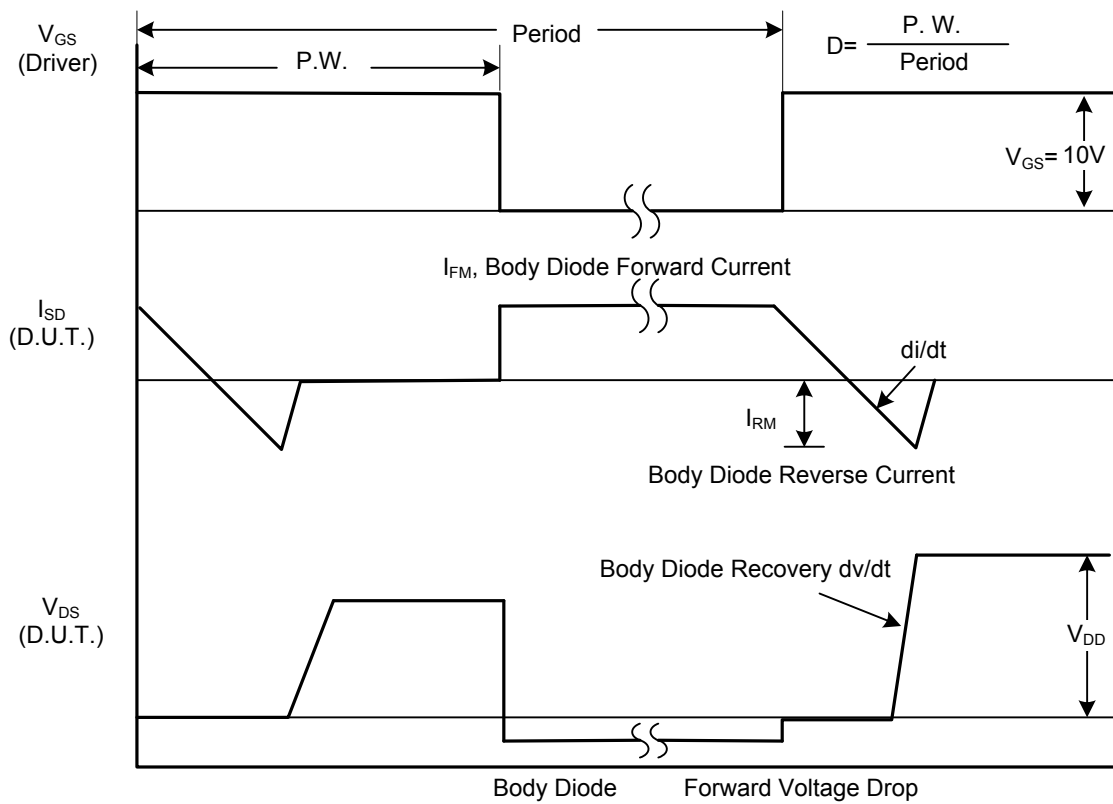
Notes: 1. Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

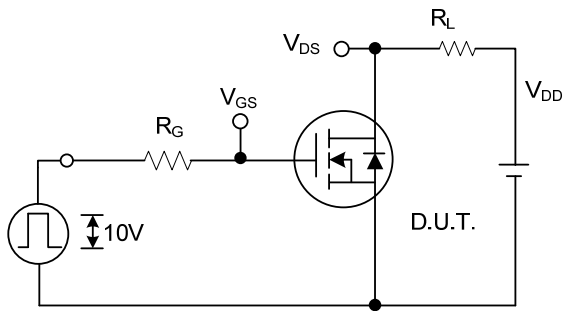


Peak Diode Recovery dv/dt Test Circuit

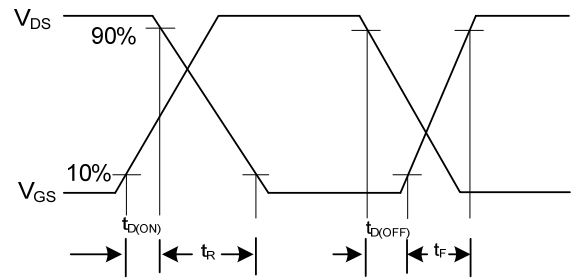


Peak Diode Recovery dv/dt Waveforms

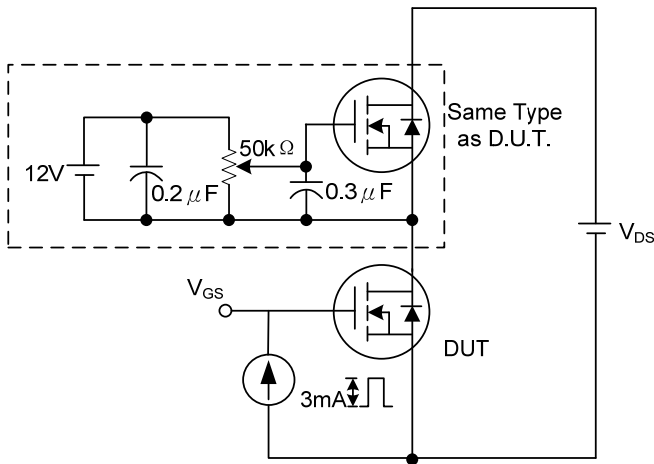
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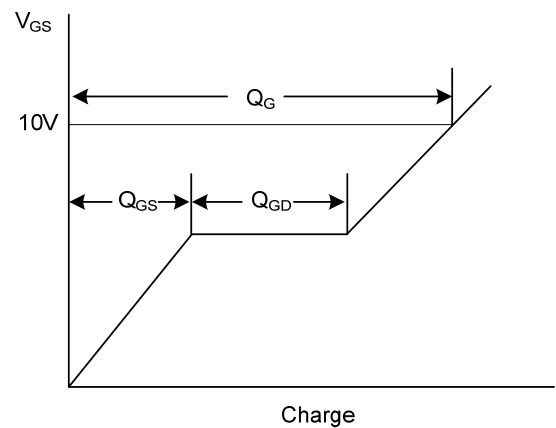
Switching Test Circuit



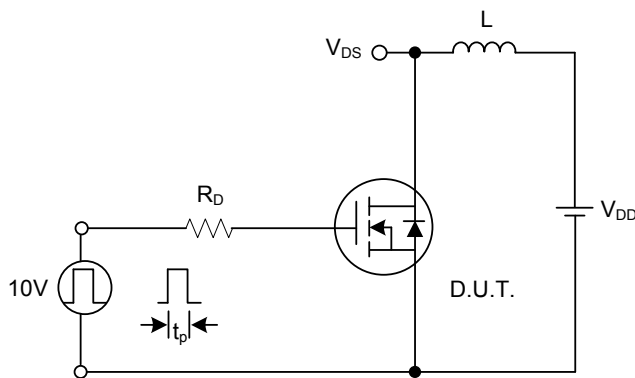
Switching Waveforms



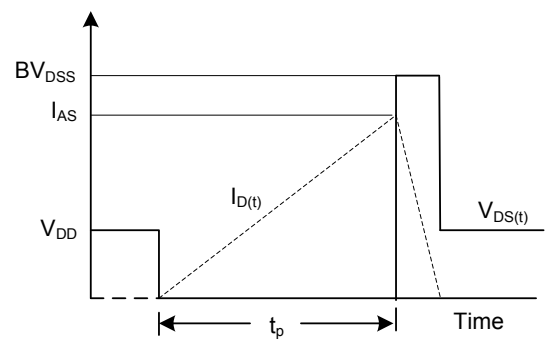
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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