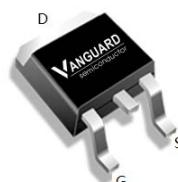


Features

- N-Channel, 5V Logic Level Control
- Enhancement mode
- Very low on-resistance $R_{DS(on)}$ @ $V_{GS}=4.5$ V
- Fast Switching
- 100% Avalanche test
- Pb-free lead plating; RoHS compliant


Halogen-Free

V_{DS}	30	V
$R_{DS(on),TYP}$ @ $V_{GS}=10$ V	4.3	$m\Omega$
$R_{DS(on),TYP}$ @ $V_{GS}=4.5$ V	6	$m\Omega$
I_D	85	A

TO-252

Drain Pin 2

Source Pin 3

Part ID	Package Type	Marking	Tape and reel information
VS3610AD	TO-252	3610AD	2500pcs/Reel

Maximum ratings, at $T_A = 25^\circ C$, unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage	30	V
I_S	Diode continuous forward current	$T_c = 25^\circ C$	A
I_D	Continuous drain current@ $V_{GS}=10V$	$T_c = 25^\circ C$	A
		$T_c = 100^\circ C$	A
I_{DM}	Pulse drain current tested ①	$T_c = 25^\circ C$	A
EAS	Avalanche energy, single pulsed ②	56	mJ
P_D	Maximum power dissipation	$T_c = 25^\circ C$	W
V_{GS}	Gate-Source voltage	± 20	V
$T_{STG} T_J$	Storage and operating temperature range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	2.9	°C/W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	100	°C/W

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ $T_j=25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	30	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$	--	--	1	μA
	Zero Gate Voltage Drain Current($T_j=125^\circ\text{C}$)	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$	--	--	100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS}(\text{TH})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_D=250\mu\text{A}$	1.0	1.8	2.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ^③	$V_{\text{GS}}=10\text{V}$, $I_D=20\text{A}$	--	4.3	5	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ^③	$V_{\text{GS}}=4.5\text{V}$, $I_D=16\text{A}$	--	6	8	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (unless otherwise stated)						
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	--	1930	--	pF
C_{oss}	Output Capacitance		--	310	--	pF
C_{rss}	Reverse Transfer Capacitance		--	260	--	pF
R_g	Gate Resistance	$f=1\text{MHz}$	--	0.85	--	Ω
Q_g	Total Gate Charge	$V_{\text{DS}}=15\text{V}$, $I_D=20\text{A}$, $V_{\text{GS}}=10\text{V}$	--	38	--	nC
Q_{gs}	Gate-Source Charge		--	5.1	--	nC
Q_{gd}	Gate-Drain Charge		--	12	--	nC
Switching Characteristics						
$t_{d(\text{on})}$	Turn-on Delay Time	$V_{\text{DD}}=15\text{V}$, $I_D=20\text{A}$, $R_G=3\Omega$, $V_{\text{GS}}=10\text{V}$	--	8.5	--	nS
t_r	Turn-on Rise Time		--	9	--	nS
$t_{d(\text{off})}$	Turn-Off Delay Time		--	31	--	nS
t_f	Turn-Off Fall Time		--	9	--	nS
Source- Drain Diode Characteristics@ $T_j = 25^\circ\text{C}$ (unless otherwise stated)						
V_{SD}	Forward on voltage	$I_{\text{SD}}=20\text{A}$, $V_{\text{GS}}=0\text{V}$	--	0.8	1.2	V
t_{rr}	Reverse Recovery Time	$T_j=25^\circ\text{C}$, $I_{\text{SD}}=20\text{A}$, $V_{\text{GS}}=0\text{V}$ $dI/dt=500\text{A}/\mu\text{s}$	--	16	--	nS
Q_{rr}	Reverse Recovery Charge		--	42	--	nC

NOTE:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Limited by $T_{j\text{max}}$, starting $T_j = 25^\circ\text{C}$, $L = 0.5\text{mH}$, $R_G = 25\Omega$, $I_{AS} = 15\text{A}$, $V_{GS} = 10\text{V}$. Part not recommended for use above this value
- ③ Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.



Vanguard
Semiconductor

VS3610AD

30V/85A N-Channel Advanced Power MOSFET

Typical Characteristics

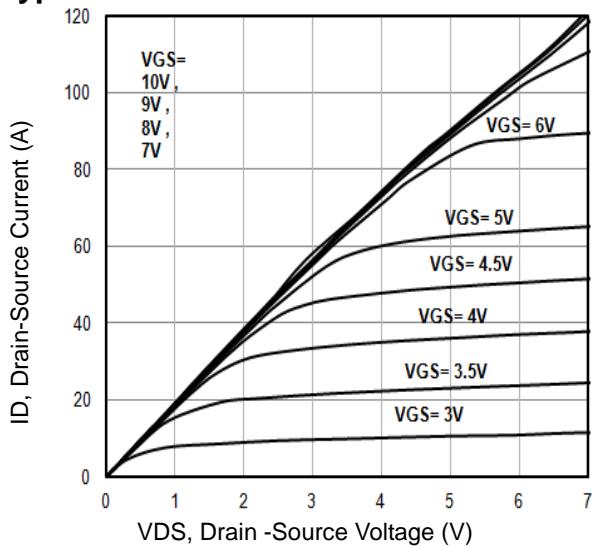


Fig1. Typical Output Characteristics

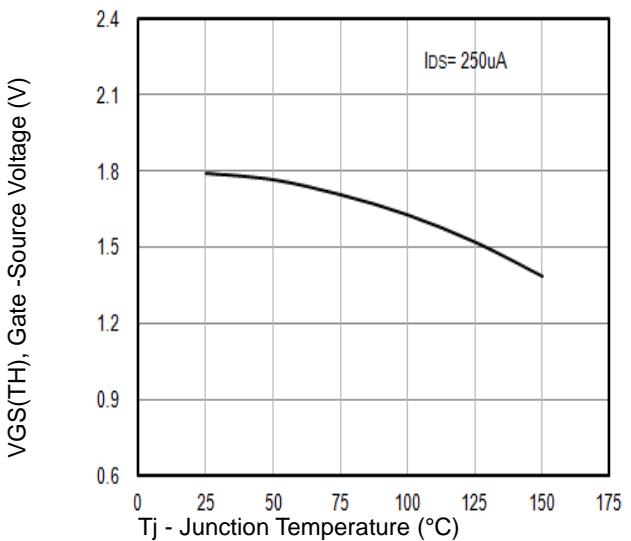


Fig2. $V_{GS(TH)}$ Gate -Source Voltage Vs. T_j

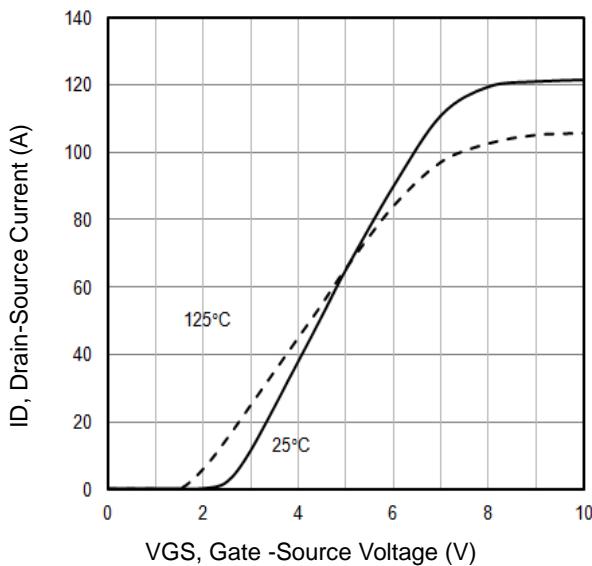


Fig3. Typical Transfer Characteristics

Fig4. Normalized On-Resistance Vs. T_j

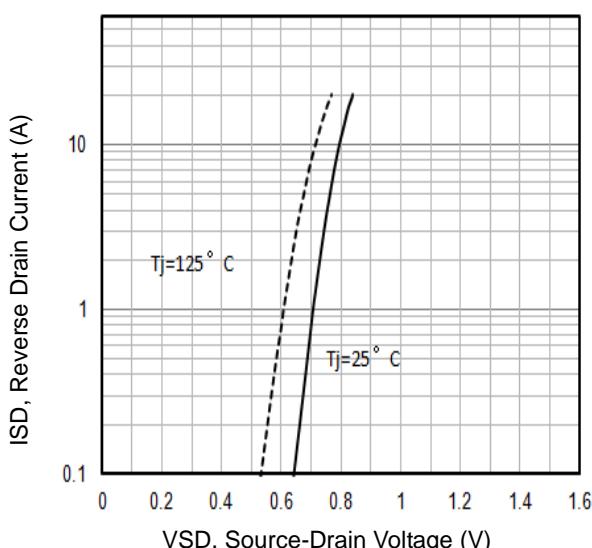


Fig5. Typical Source-Drain Diode Forward Voltage

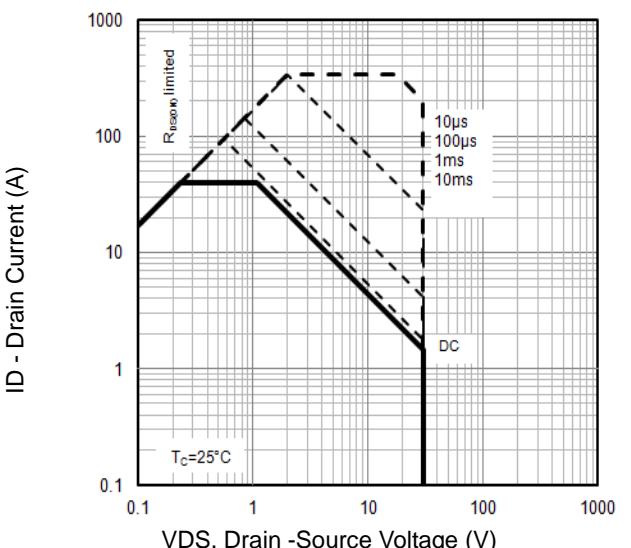


Fig6. Maximum Safe Operating Area



Typical Characteristics

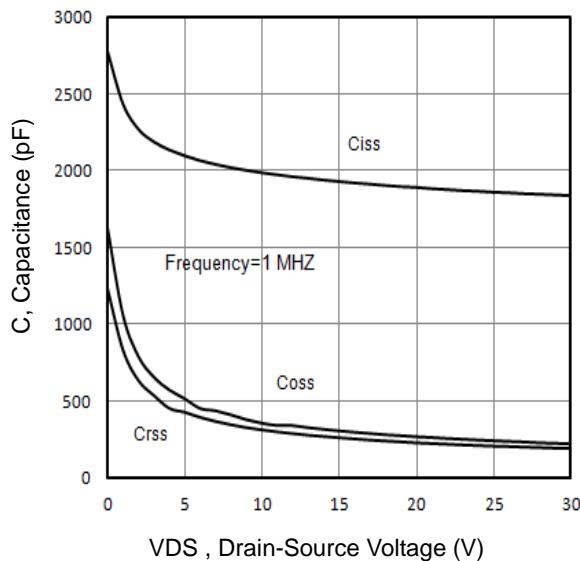


Fig7. Typical Capacitance Vs.Drain-Source Voltage

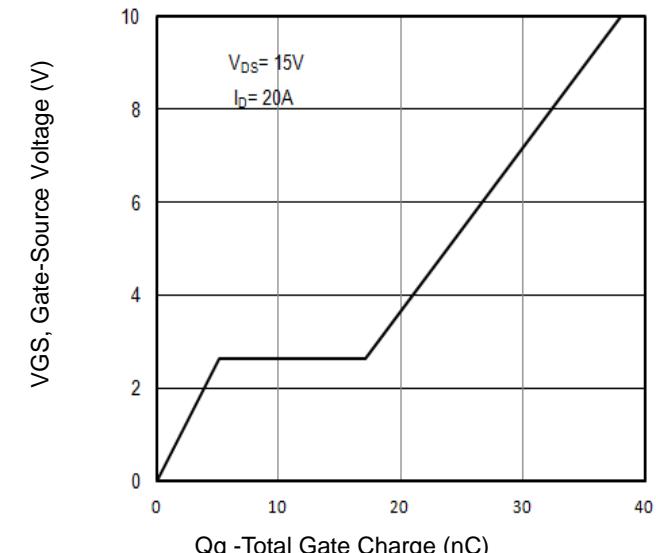


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

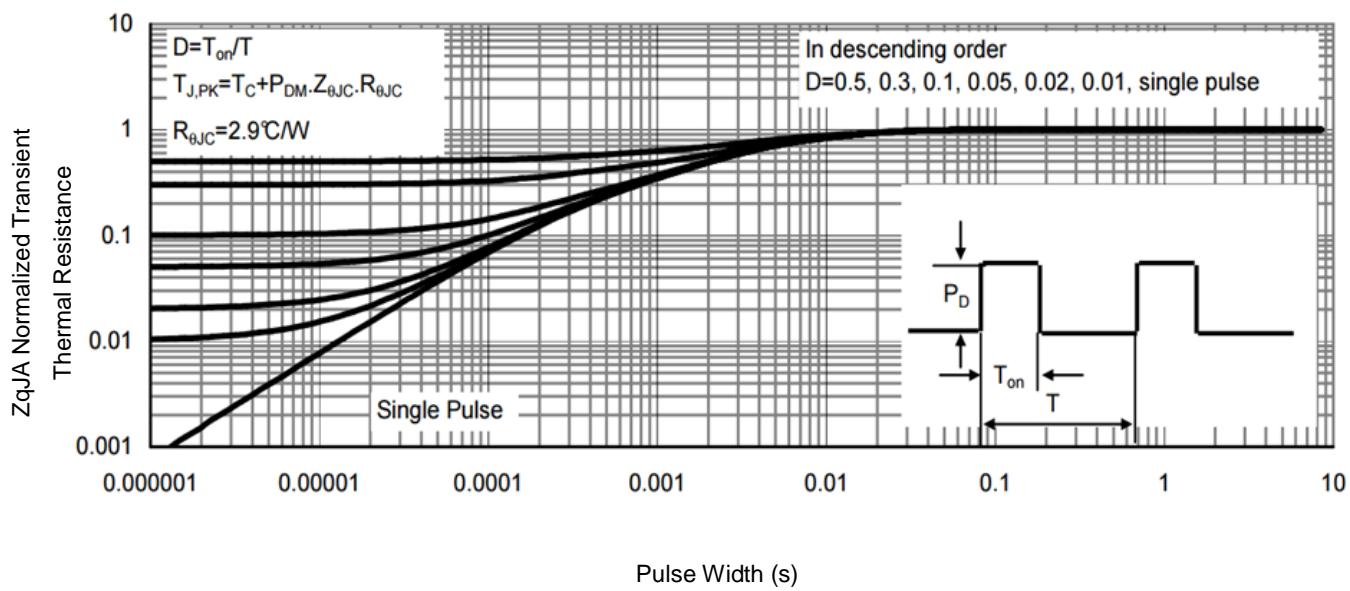


Fig9. Normalized Maximum Transient Thermal Impedance

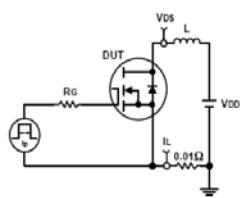


Fig10. Unclamped Inductive Test Circuit and waveforms

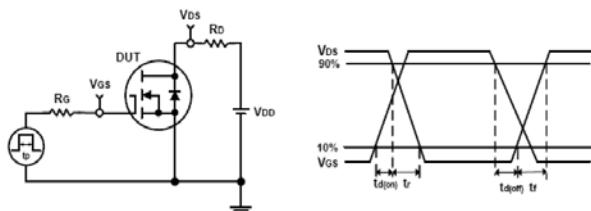
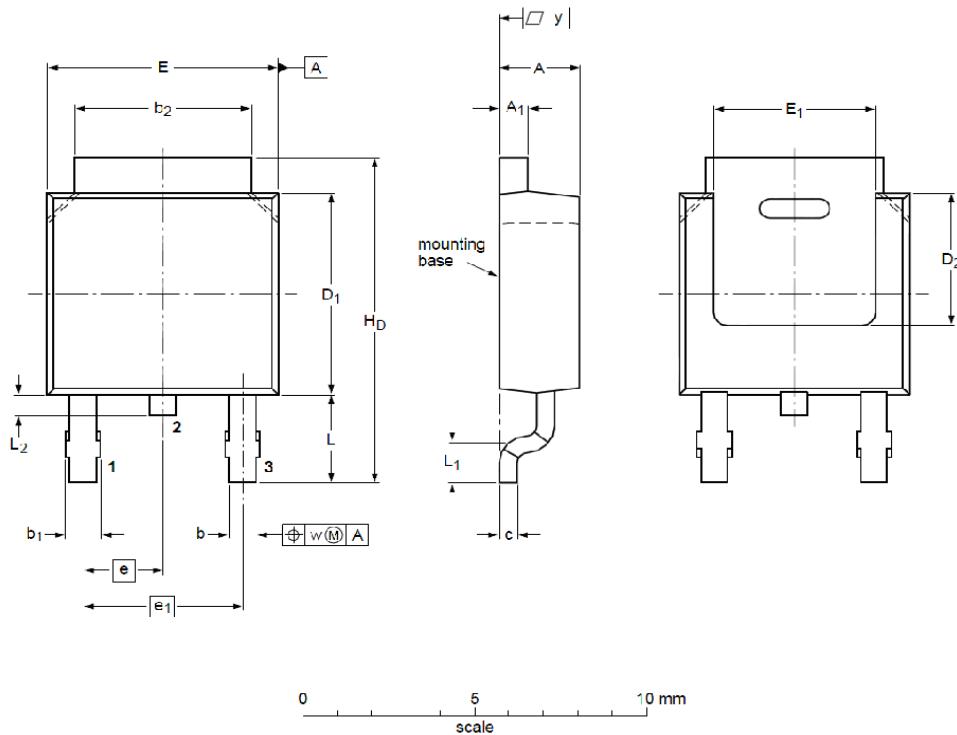


Fig11. Switching Time Test Circuit and waveforms

TO-252 Package Outline Data



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	2.22	2.30	2.38	A ₁	0.46	0.58	0.93
b	0.71	0.79	0.89	b ₁	0.90	0.98	1.10
b ₂	5.00	5.30	5.46	c	0.20	0.40	0.56
D ₁	5.98	6.05	6.22	D ₂	--	4.00	--
E	6.47	6.60	6.73	E ₁	5.10	5.28	5.45
e	--	2.28	--	e ₁	--	4.57	--
H _D	9.60	10.08	10.40	L	2.75	2.95	3.05
L ₁	--	0.50	--	L ₂	0.80	0.90	1.10
w	--	0.20	--	y	0.20	--	--

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