

# P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$		Q <sub>g</sub> (Typ.)		
- 30	0.056at V <sub>GS</sub> = - 10 V	- 20	19 nC		
- 30	0.072 at V <sub>GS</sub> = -4.5 V	- 15	19110		

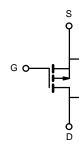
#### **FEATURES**

- Halogen-free
- TrenchFET® Power MOSFET
- 100 % R<sub>g</sub> Tested
- 100 % UIS Tested



#### **APPLICATIONS**

- Load Switch
- · Notebook Adaptor Switch



P-Channel MOSFET

TO-251	
G D S	Drain Connected to Drain-Tab
Top View	

ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted					
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	- 30	V		
Gate-Source Voltage		$V_{GS}$	± 20	V	
	T <sub>C</sub> = 25 °C		- 20		
Continuous Drain Current (T <sub>,I</sub> = 150 °C)	T <sub>C</sub> = 70 °C	] <sub>I-</sub>	- 15		
Continuous Diain Curient (1) = 130 C)	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	-7.9 <sup>a, b</sup>		
	T <sub>A</sub> = 70 °C	]	- 5.6 <sup>a, b</sup>	A	
Pulsed Drain Current	Pulsed Drain Current			^	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	I.	- 20		
Continuous Source-Diam blode Current	T <sub>A</sub> = 25 °C	- I <sub>S</sub>	- 7.9 <sup>a, b</sup>		
Avalanche Current		I <sub>AS</sub>	- 20		
Single-Pulse Avalanche Energy L = 0.1 ml		E <sub>AS</sub>	20	mJ	
	T <sub>C</sub> = 25 °C	P <sub>D</sub>	20		
Maximum Power Dissipation	T <sub>C</sub> = 70 °C		15	w	
Maximum Fower Dissipation	T <sub>A</sub> = 25 °C	' Б	2.7 <sup>a, b</sup>	v	
	T <sub>A</sub> = 70 °C		1.7 <sup>a, b</sup>		
Operating Junction and Storage Temperature Range	$T_J$ , $T_{stg}$	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a, c</sup> t ≤ 10 s		R <sub>thJA</sub>	38	46	°C/W
Maximum Junction-to-Foot	Steady State	R <sub>thJF</sub>	20	25	C/VV

#### Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- c. Maximum under Steady State conditions is 85 °C/W. d. Based on  $T_C$  = 25 °C.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 30			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = - 250 μA		- 34		mV/
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	η <sub>D</sub> = - 250 μΑ		5.3		°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	- 1.4		- 2.5	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 25 \text{ V}$			± 100	nA
Zara Cata Valtaga Drain Current	I	V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C		- 1	μА	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			- 5		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge -10 \text{ V}, V_{GS} = -10 \text{ V}$	- 20			Α
	D	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 6 A		0.056		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 4 A		0.072		Ω
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 6 A		28		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>			1150		
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		205		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			140		1 '
	Q <sub>g</sub> -	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 6 A		27	43	1
Total Gate Charge				19	25	
Gate-Source Charge		$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -6 \text{ A}$		6		nC
Gate-Drain Charge	Q <sub>qd</sub>			12		
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.5	2.2	4.4	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			13	25	
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_{L} = 1.5 \Omega$		12	24	
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong -10 \text{ A}, V_{GEN} = -10 \text{ V}, R_q = 1 \Omega$		40	70	1
Fall Time	t <sub>f</sub>			9	18	
Turn-On Delay Time	t <sub>d(on)</sub>			48	80	ns
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_{L} = 1.5 \Omega$		92	160	
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong -6$ A, $V_{GEN} = -4.5$ V, $R_a = 1$ $\Omega$		34	60	
Fall Time				19	35	
Drain-Source Body Diode Characteris	stics					
Continous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 4.1	
Pulse Diode Forward Current I <sub>S</sub>					- 60	A
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 3 A, V <sub>GS</sub> = 0 V		- 0.75	- 1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	5 55		27	45	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	1		16	27	nC
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = -6 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 \text{ °C}$		12		
Reverse Recovery Rise Time	t <sub>b</sub>	1		15		ns

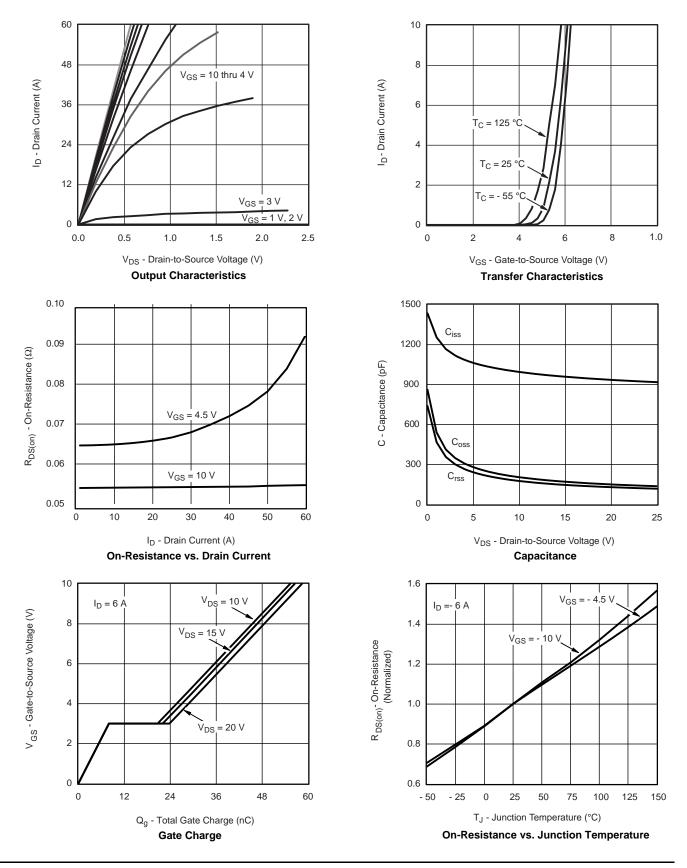
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

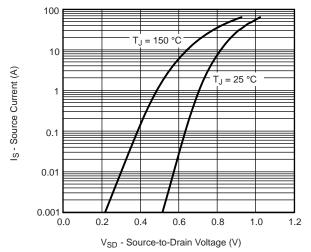


#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

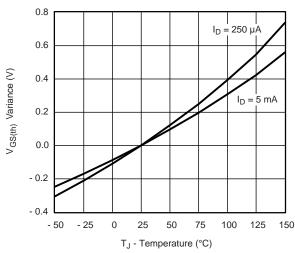




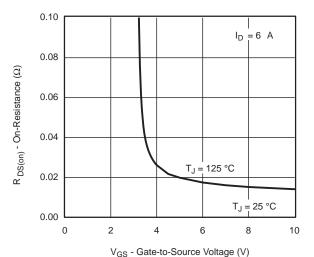
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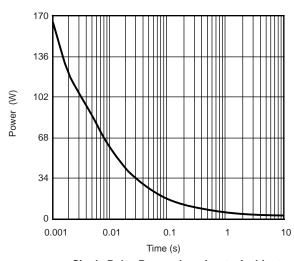
#### Source-Drain Diode Forward Voltage



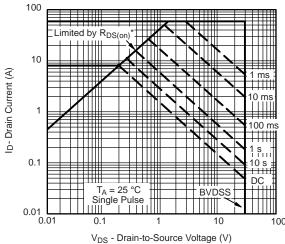
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient

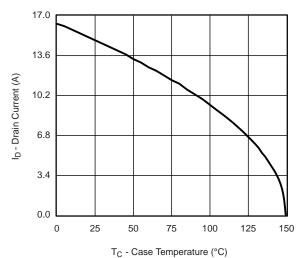


V<sub>DS</sub> - Drain-to-Source voltage (V)
V<sub>GS</sub> > minimum V<sub>GS</sub> at which R<sub>DS(on)</sub> is specified

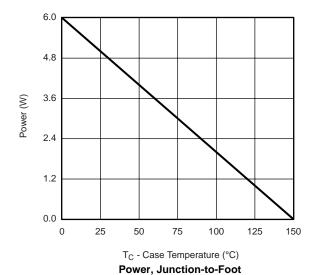
Safe Operating Area

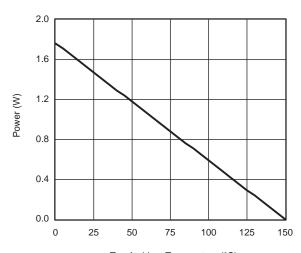


## MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



#### **Current Derating\***



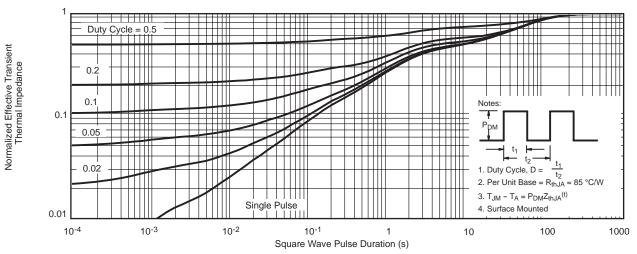


T<sub>A</sub> - Ambient Temperature (°C) Power Derating, Junction-to-Ambient

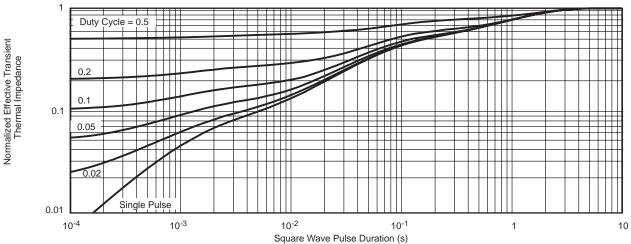
<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package



#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



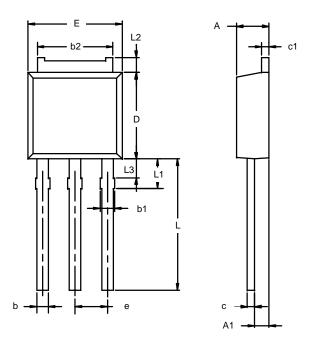
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



### TO-251AA



Note: Dimension L3 is for reference or	ηly.
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	MILLIMETERS		INC	HES	
Dim	Min	Max	Min	Max	
Α	2.21	2.38	0.087	0.094	
A1	0.89	1.14	0.035	0.045	
b	0.71	0.89	0.028	0.035	
b1	0.76	1.14	0.030	0.045	
b2	5.23	5.43	0.206	0.214	
С	0.46	0.58	0.018	0.023	
с1	0.46	0.58	0.018	0.023	
D	5.97	6.22	0.235	0.245	
E	6.48	6.73	0.255	0.265	
е	2.28	BSC 0.090 BSC			
L	3.89	9.53	0.153	0.375	
L1	1.91	2.28	0.075	0.090	
L2	0.89	1.27	0.035	0.050	
L3	1.15	1.52	0.045	0.060	



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