

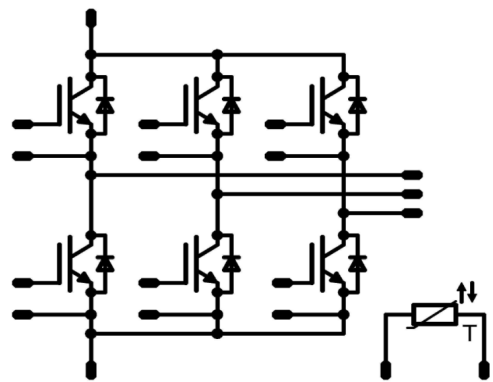




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<i>flow</i> PACK 1 3rd gen	1200 V / 50 A
<div style="background-color: #eee; padding: 2px; margin-bottom: 5px;">Features</div> <ul style="list-style-type: none"> Compact <i>flow</i> 1 housing Trench Fieldstop IGBT4 technology Compact and low inductance design AlN substrate for improved performance Built-in NTC 	<div style="background-color: #eee; padding: 2px; margin-bottom: 5px;">flow 1 12mm housing</div> <div style="display: flex; justify-content: space-around; align-items: center;"> <div style="text-align: center;">  <p>solder pins</p> </div> <div style="text-align: center;">  <p>Press-fit pins</p> </div> </div>
<div style="background-color: #eee; padding: 2px; margin-bottom: 5px;">Target applications</div> <ul style="list-style-type: none"> Motor Drive Power generation UPS 	<div style="background-color: #eee; padding: 2px; margin-bottom: 5px;">Schematic</div> 
<div style="background-color: #eee; padding: 2px; margin-bottom: 5px;">Types</div> <ul style="list-style-type: none"> V23990-P829-F08-PM V23990-P829-F08Y-PM 	

Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Inverter Switch				
Collector-emitter voltage	V_{CES}		1200	V
Collector current	I_C	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	71	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}	150	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	216	W
Gate-emitter voltage	V_{GES}		±20	V
Short circuit ratings	t_{sc}	$T_j \leq 150\text{ °C}$	10	µs
	V_{CC}	$V_{GE} = 15V$	800	V
Maximum Junction Temperature	T_{jmax}		175	°C



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Parameter	Symbol	Conditions	Value	Unit
Inverter Diode				
Peak Repetitive Reverse Voltage	V_{RRM}		1200	V
Continuous (direct) forward current	I_F	$T_j = T_{jmax}$ $T_s = 80^\circ\text{C}$	65	A
Repetitive peak forward current	I_{FRM}		100	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80^\circ\text{C}$	146	W
Maximum Junction Temperature	T_{jmax}		175	$^\circ\text{C}$

Parameter	Symbol	Conditions	Value	Unit
Module Properties				
Thermal Properties				
Storage temperature	T_{stg}		-40...+125	$^\circ\text{C}$
Operation temperature under switching condition	T_{jop}		-40...+($T_{jmax} - 25$)	$^\circ\text{C}$

Isolation Properties				
Isolation voltage	V_{isol}	DC Voltage $t_p=2s$	4000	V
Creepage distance			min. 12,5	mm
Clearance		solder pins / Press-fit pins	7,81 / 7,9	mm
Comparative Tracking Index	CTI		> 200	



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Characteristic Values

Inverter Switch

Parameter	Symbol	Conditions					Value			Unit
		V_{GE} [V]	V_{CE} [V]	I_C [A]	T_j [°C]	Min	Typ	Max		

Static

Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{GE}=V_{CE}$			0,0017	25 125	5,3	5,8	6,3	V
Collector-emitter saturation voltage	V_{CEsat}		15		50	25 125 150	1,58	1,88	2,07	V
Collector-emitter cut-off current	I_{CES}		0	1200		25 125			1	μA
Gate-emitter leakage current	I_{GES}		20	0		25 125			120	nA
Internal gate resistance	r_g							4		Ω
Input capacitance	C_{ies}	f=1MHz	0	25		25		2800		pF
Reverse transfer capacitance	C_{res}							100		

Thermal

Thermal resistance junction to sink	$R_{th(j-s)}$	phase-change material $\lambda=3,4W/mK$						0,44		K/W
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IGBT Switching

Turn-on delay time	$t_{d(on)}$	$R_{goff} = 8 \Omega$ $R_{gon} = 8 \Omega$	±15	600	50	25		96		ns
Rise time	t_r					150		101		
Turn-off delay time	$t_{d(off)}$					25		17		
Fall time	t_f					150		24		
Turn-on energy (per pulse)	E_{on}	$Q_{rFWD} = 4,8 \mu C$ $Q_{rFWD} = 9,7 \mu C$				25		214		mWs
Turn-off energy (per pulse)	E_{off}					150		281		
						25		87		
						150		122		



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Inverter Diode

Parameter	Symbol	Conditions					Value			Unit
				V_r [V]	I_F [A]	T_j [°C]	Min	Typ	Max	
Static										
Forward voltage	V_F				50	25 125 150		1,73 1,70 1,68	2,05	V
Reverse leakage current	I_r			1200		25 150			10	μA

Thermal

Thermal resistance junction to sink	$R_{th(j-s)}$	phase-change material $\lambda = 3,4$ W/mK						0,65		K/W
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FWD Switching

Peak recovery current	I_{RRM}	$di/dt = 3866$ A/μs $di/dt = 2820$ A/μs	±15	600	50	25 150		81 85		A
Reverse recovery time	t_{rr}					25 150		139 316		ns
Recovered charge	Q_r					25 150		4,797 9,708		μC
Reverse recovered energy	E_{rec}					25 150		1,790 3,972		mWs
Peak rate of fall of recovery current	$(di_{rr}/dt)_{max}$					25 150		4803 1209		A/μs

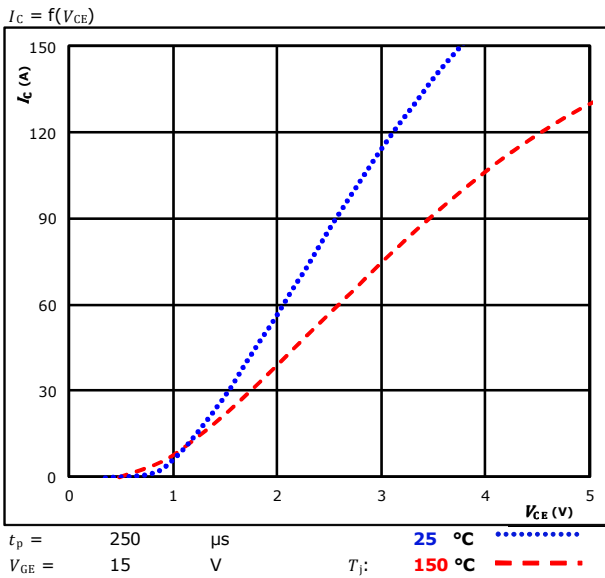
Thermistor

Parameter	Symbol	Conditions					Value			Unit
			V_{GE} [V]	V_{CE} [V]	I_C [A]	T_j [°C]	Min	Typ	Max	
Rated resistance	R					25		4,7		kΩ
Deviation of R100	$\Delta_{R/R}$	R100=401 Ω				100	-12,4		12,4	%
Power dissipation	P					25		210		mW
Power dissipation constant						25		3,5		mW/K
B-value	$B_{(25/50)}$					25		3590		K
B-value	$B_{(25/100)}$					25		3650		K
Vincotech NTC Reference									D	

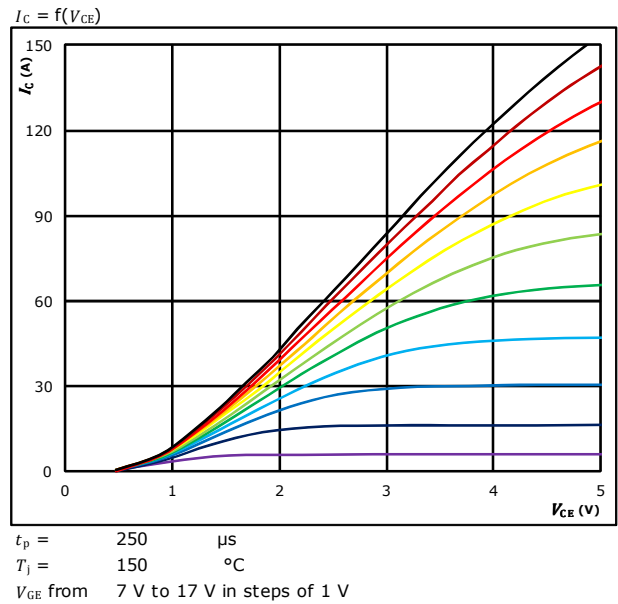


Inverter Switch Characteristics

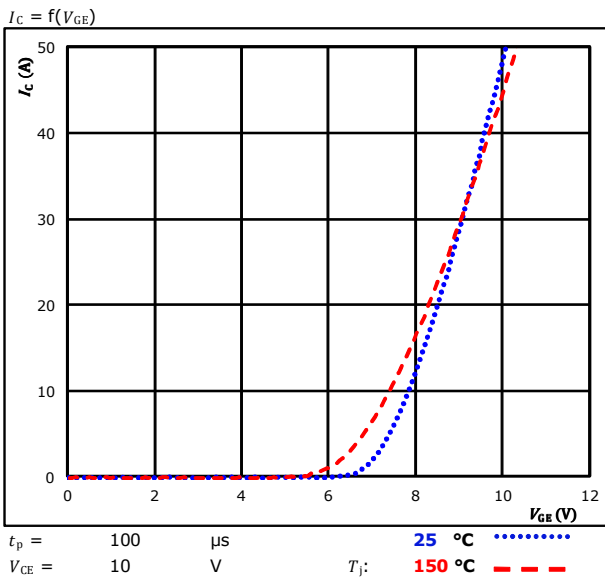
Typical output characteristics IGBT



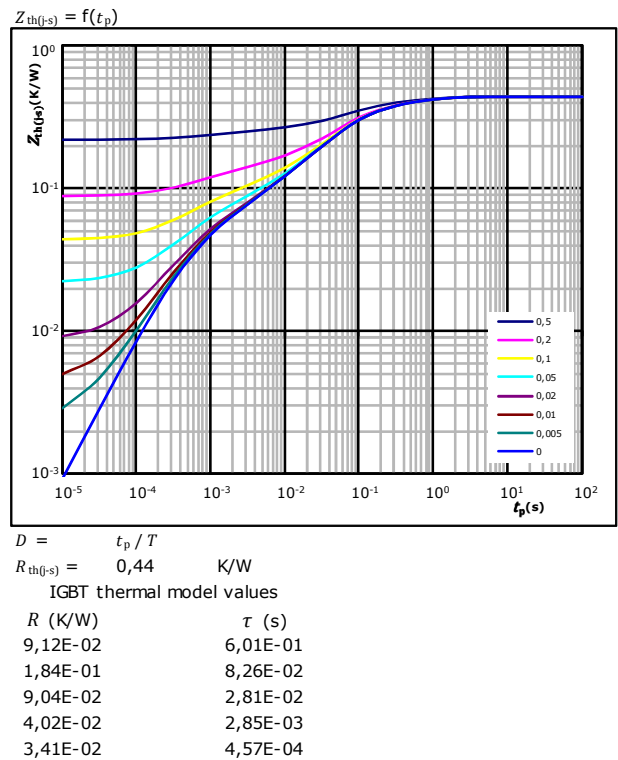
Typical output characteristics IGBT



Typical transfer characteristics IGBT



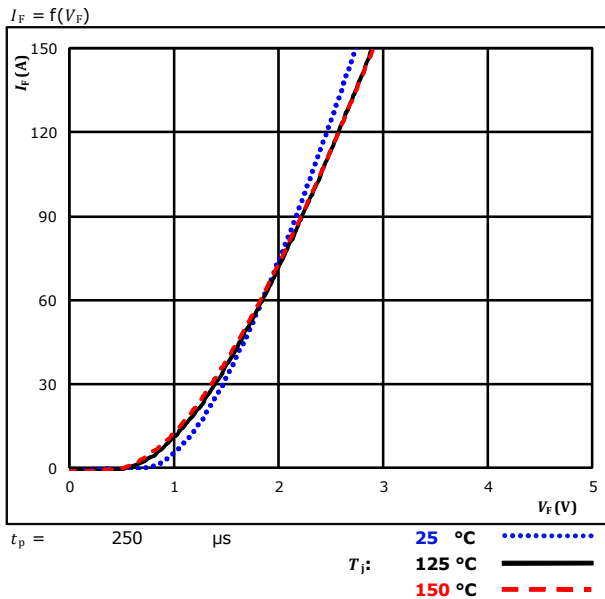
Transient Thermal Impedance as function of Pulse duration IGBT



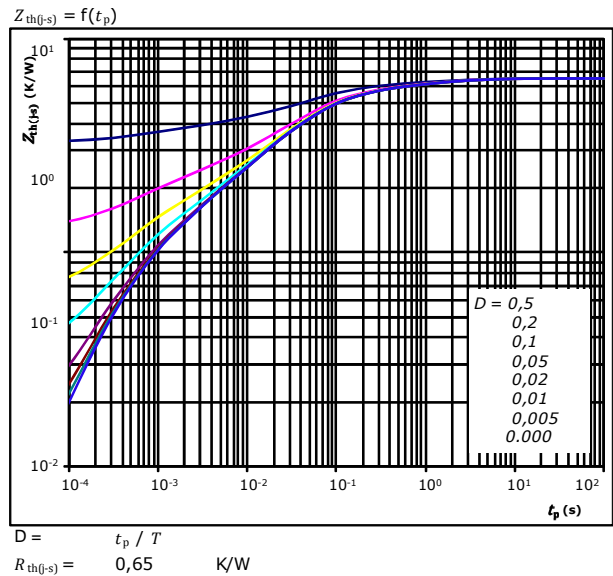


Inverter Diode Characteristics

Typical forward characteristics FWD



Transient thermal impedance as a function of pulse width FWD



FWD thermal model values

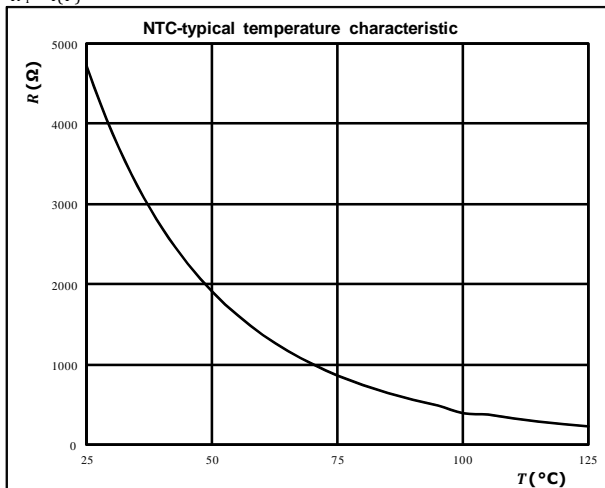
R (K/W)	τ (s)
2,1630E-02	5,6310E+00
7,4790E-02	7,7120E-01
1,3890E-01	1,1610E-01
2,2410E-01	3,1460E-02
7,3190E-02	6,5550E-03
5,9880E-02	1,5650E-03
5,8730E-02	3,6760E-04

Thermistor

Thermistor typical temperature characteristic

Typical NTC characteristic
as a function of temperature

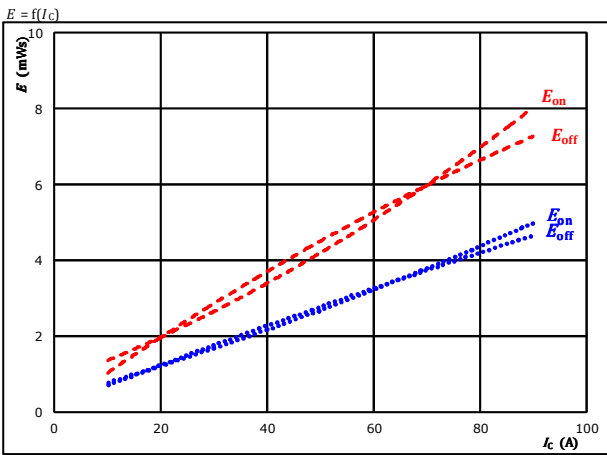
$R_T = f(T)$





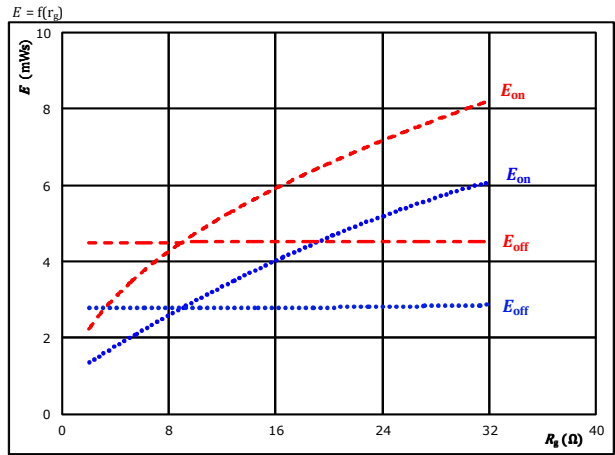
Inverter Switching Characteristics

Figure 1. IGBT
Typical switching energy losses as a function of collector current



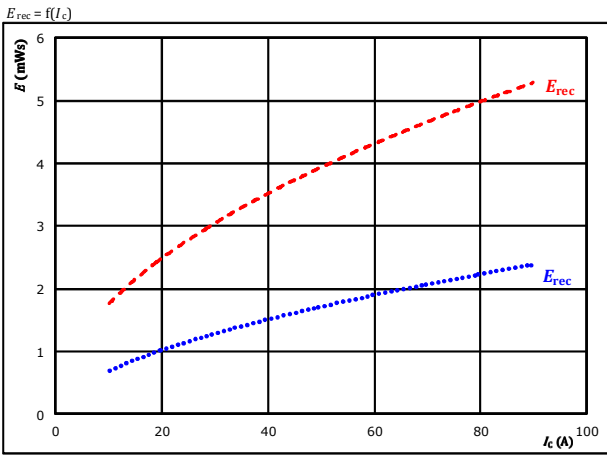
With an inductive load at
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 8$ Ω
 $R_{goff} = 8$ Ω
 T_j : 25 °C (dotted), 150 °C (dashed)

Figure 2. IGBT
Typical switching energy losses as a function of gate resistor



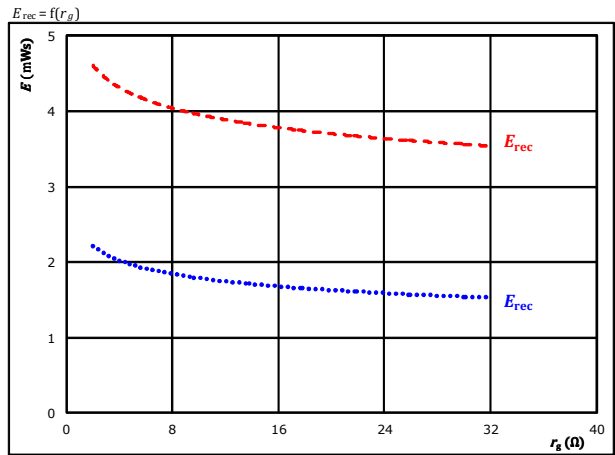
With an inductive load at
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $I_C = 50$ A
 T_j : 25 °C (dotted), 150 °C (dashed)

Figure 3. FWD
Typical reverse recovered energy loss as a function of collector current



With an inductive load at
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 8$ Ω
 T_j : 25 °C (dotted), 150 °C (dashed)

Figure 4. FWD
Typical reverse recovered energy loss as a function of gate resistor

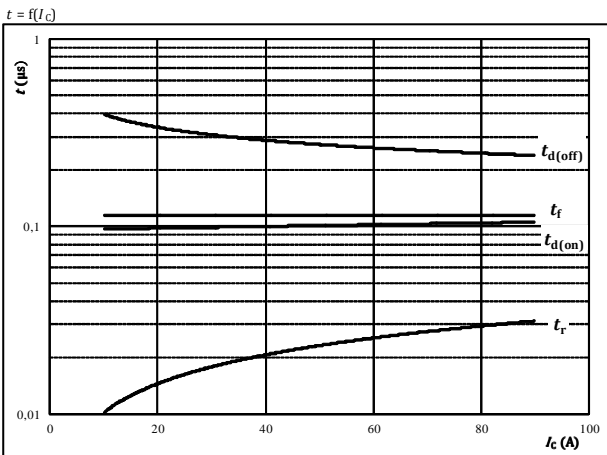


With an inductive load at
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $I_C = 50$ A
 T_j : 25 °C (dotted), 150 °C (dashed)



Inverter Switching Characteristics

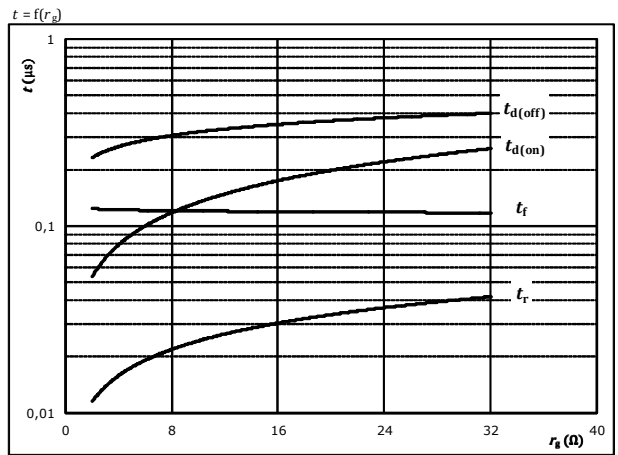
Figure 5. IGBT
Typical switching times as a function of collector current



With an inductive load at

$T_j =$	150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$R_{gon} =$	8	Ω
$R_{goff} =$	8	Ω

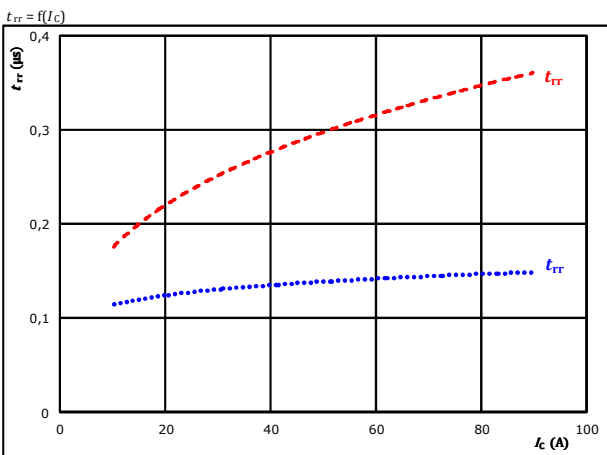
Figure 6. IGBT
Typical switching times as a function of gate resistor



With an inductive load at

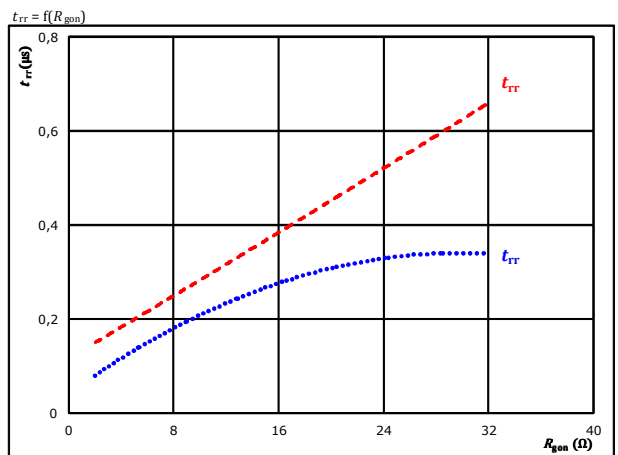
$T_j =$	150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$I_c =$	50	A

Figure 7. FWD
Typical reverse recovery time as a function of collector current



At	$V_{CE} =$	600	V	$T_j:$	25 °C
	$V_{GE} =$	±15	V		150 °C	-----
	$R_{gon} =$	8	Ω			

Figure 8. FWD
Typical reverse recovery time as a function of IGBT turn on gate resistor



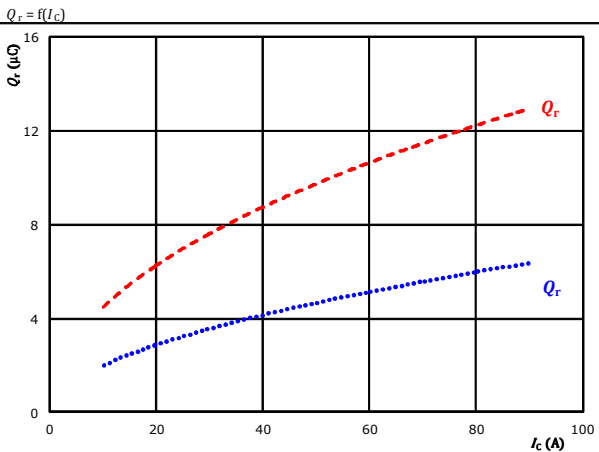
At	$V_{CE} =$	600	V	$T_j:$	25 °C
	$V_{GE} =$	±15	V		150 °C	-----
	$I_c =$	50	A			



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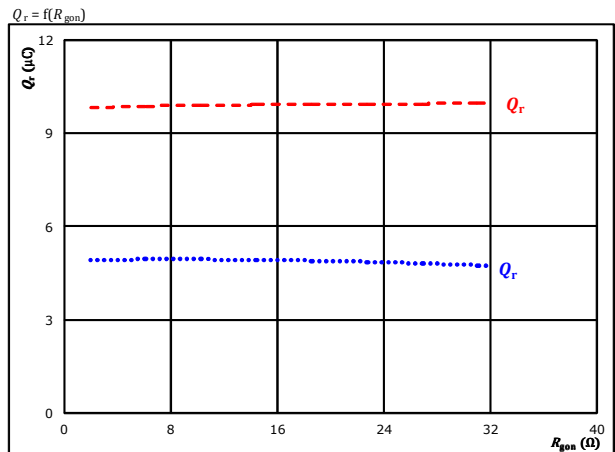
Inverter Switching Characteristics

Figure 9. FWD
Typical recovered charge as a function of collector current



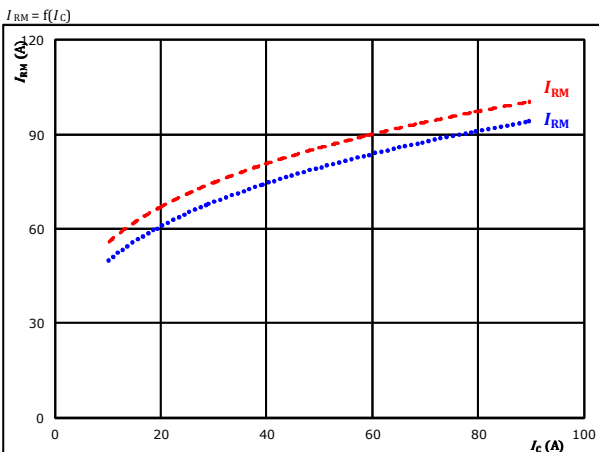
At $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 8$ Ω
 T_j : 25 °C (dotted line)
 150 °C (dashed line)

Figure 10. FWD
Typical recovered charge as a function of IGBT turn on gate resistor



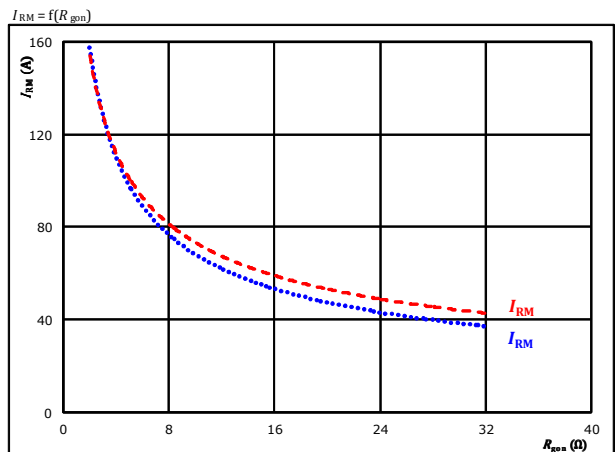
At $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $I_c = 50$ A
 T_j : 25 °C (dotted line)
 150 °C (dashed line)

Figure 11. FWD
Typical peak reverse recovery current as a function of collector current



At $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 8$ Ω
 T_j : 25 °C (dotted line)
 150 °C (dashed line)

Figure 12. FWD
Typical peak reverse recovery current as a function of IGBT turn on gate resistor



At $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $I_c = 50$ A
 T_j : 25 °C (dotted line)
 150 °C (dashed line)

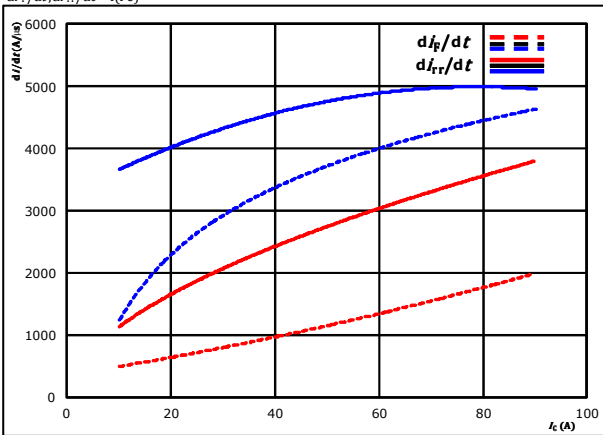


Inverter Switching Characteristics

Figure 13. FWD

Typical rate of fall of forward and reverse recovery current as a function of collector current

$$di_F/dt, di_{rr}/dt = f(I_C)$$

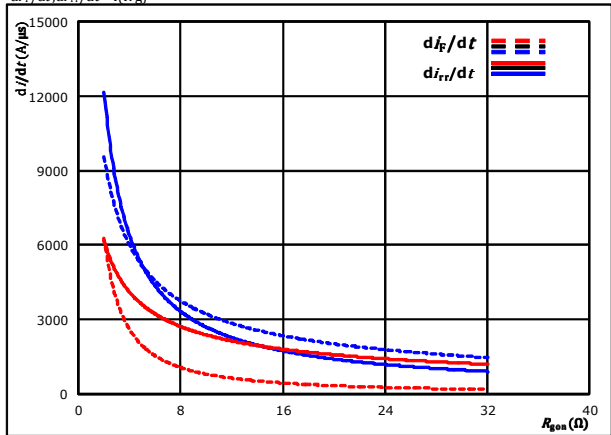


At $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 8$ Ω
 $T_j = 25$ °C (dotted line)
 $T_j = 150$ °C (dashed line)

Figure 14. FWD

Typical rate of fall of forward and reverse recovery current as a function of IGBT turn on gate resistor

$$di_F/dt, di_{rr}/dt = f(R_g)$$

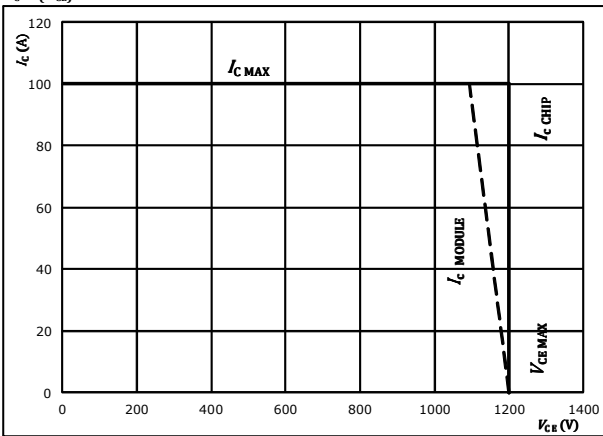


At $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $I_C = 50$ A
 $T_j = 25$ °C (dotted line)
 $T_j = 150$ °C (dashed line)

Figure 15. IGBT

Reverse bias safe operating area

$$I_C = f(V_{CE})$$



At $T_j = 175$ °C
 $R_{gon} = 8$ Ω
 $R_{goff} = 8$ Ω



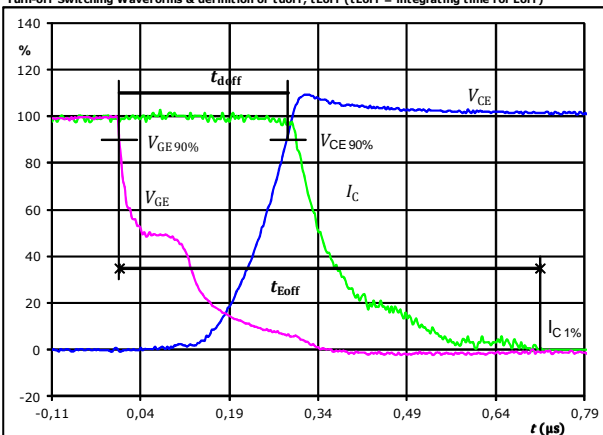
Inverter Switching Definitions

General conditions

T_j	=	150 °C
R_{gon}	=	8 Ω
R_{goff}	=	8 Ω

Figure 1. IGBT

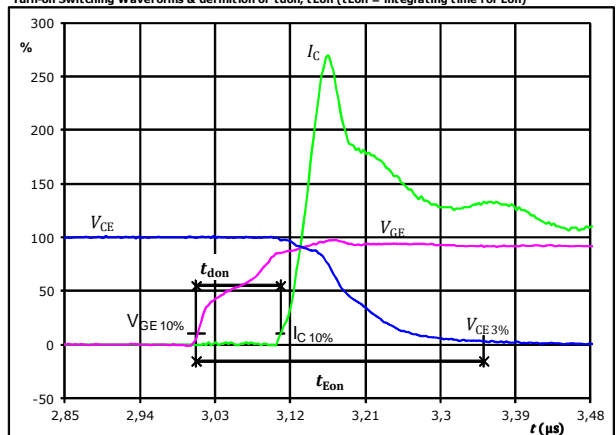
Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff} (t_{Eoff} = integrating time for Eoff)



$V_{GE}(0\%) =$	-15	V
$V_{GE}(100\%) =$	15	V
$V_C(100\%) =$	600	V
$I_C(100\%) =$	50	A
$t_{doff} =$	0,281	μs
$t_{Eoff} =$	0,710	μs

Figure 2. IGBT

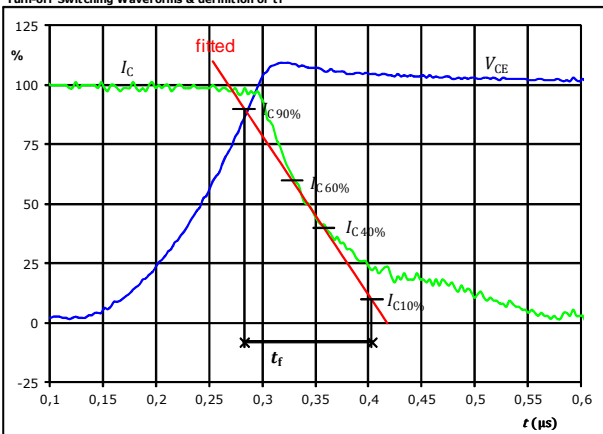
Turn-on Switching Waveforms & definition of t_{don} , t_{Eon} (t_{Eon} = integrating time for Eon)



$V_{GE}(0\%) =$	-15	V
$V_{GE}(100\%) =$	15	V
$V_C(100\%) =$	600	V
$I_C(100\%) =$	50	A
$t_{don} =$	0,101	μs
$t_{Eon} =$	0,345	μs

Figure 3. IGBT

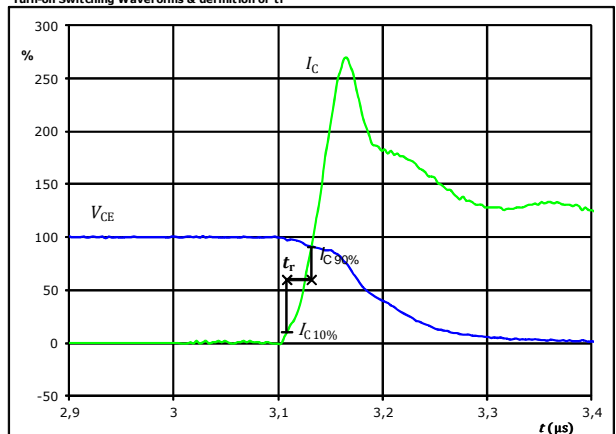
Turn-off Switching Waveforms & definition of t_f



$V_C(100\%) =$	600	V
$I_C(100\%) =$	50	A
$t_f =$	0,122	μs

Figure 4. IGBT

Turn-on Switching Waveforms & definition of t_r

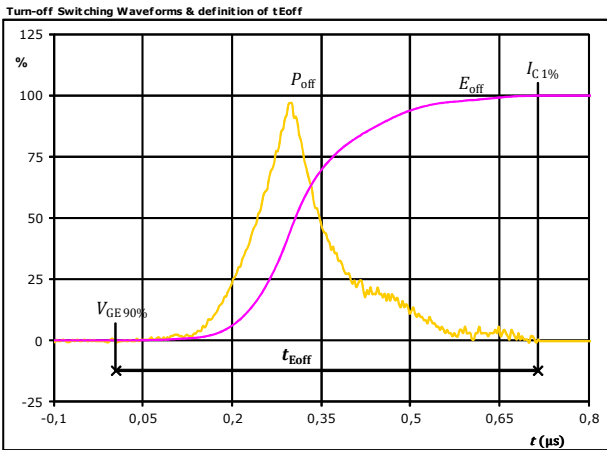


$V_C(100\%) =$	600	V
$I_C(100\%) =$	50	A
$t_r =$	0,024	μs



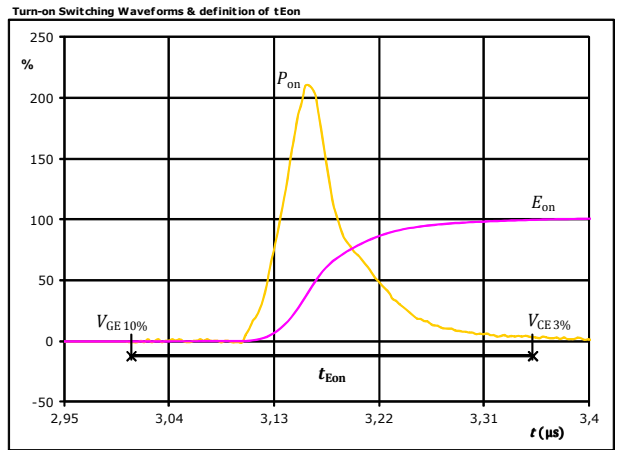
Inverter Switching Definitions

Figure 5. IGBT



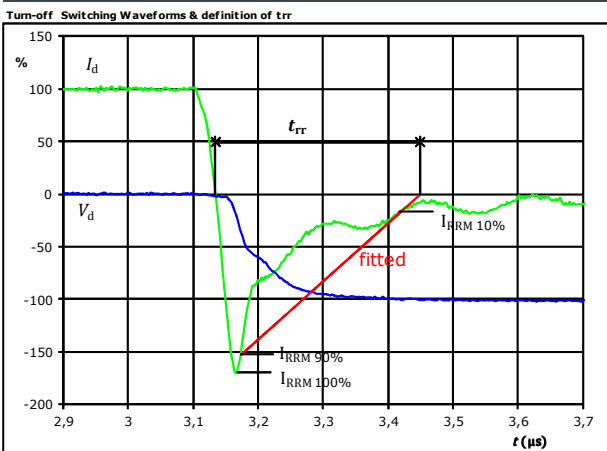
$P_{off}(100\%) =$	30,10	kW
$E_{off}(100\%) =$	4,53	mJ
$t_{Eoff} =$	0,71	μs

Figure 6. IGBT



$P_{on}(100\%) =$	30,10	kW
$E_{on}(100\%) =$	4,21	mJ
$t_{Eon} =$	0,345	μs

Figure 7. FWD

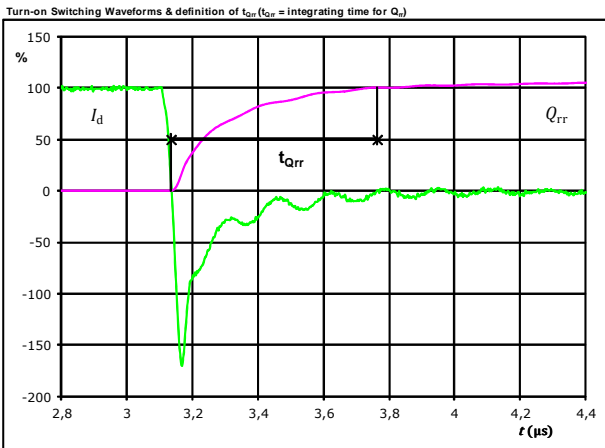


$V_d(100\%) =$	600	V
$I_d(100\%) =$	50	A
$I_{RRM}(100\%) =$	-85	A
$t_{tr} =$	0,316	μs



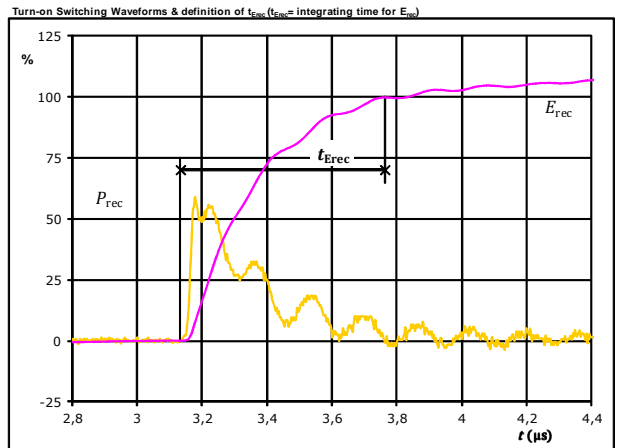
Inverter Switching Definitions

Figure 8. FWD



$I_d(100\%) =$	50	A
$Q_{rr}(100\%) =$	9,71	μC
$t_{Qrr} =$	0,63	μs

Figure 9. FWD



$P_{rec}(100\%) =$	30,10	kW
$E_{rec}(100\%) =$	3,97	mJ
$t_{Erec} =$	0,63	μs



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Ordering Code & Marking							
Version				Ordering Code			
Without thermal paste 12 mm housing solder pins				V23990-P829-F08-PM			
With thermal paste 12 mm housing solder pins				V23990-P829-F08-/-3/-PM			
Without thermal paste 12 mm housing Press-fit pins				V23990-P829-F08Y-PM			
With thermal paste 12 mm housing Press-fit pins				V23990-P829-F08Y-/-3/-PM			
	Text	Vinco	Date code	Name&Ver	UL	Lot	Serial
		Vinco	WWYY	TTTTTTTVV	UL	LLLL	SSSS
	Datamatrix	Type&Ver	Lot number	Serial	Date code		
		TTTTTTTVV	LLLL	SSSS	WWYY		

Outline							
Pin table [mm]				Pin table [mm]			
Pin	X	Y	Function	Pin	X	Y	Function
1	52,6	0	DC-	30	52,6	14,65	DC+
2	49,9	0	DC-	31	49,9	14,65	DC+
3	42,65	0	G6				
4	39,65	0	S6				
5	35,15	0	NTC1				
6	28,4	0	NTC2				
7	24	0	G4				
8	21	0	S4				
9	12,2	0	G2				
10	9,2	0	S2				
11	2,7	0	DC-				
12	0	0	DC-				
13	0	14,65	DC+				
14	2,7	14,65	DC+				
15	0	28,6	U				
16	2,7	28,6	U				
17	5,4	28,6	U				
18	9,6	28,6	S1				
19	12,6	28,6	G1				
20	19,6	28,6	V				
21	22,3	28,6	V				
22	25	28,6	V				
23	29,7	28,6	S3				
24	32,7	28,6	G3				
25	39,7	28,6	S5				
26	42,7	28,6	G5				
27	47,2	28,6	W				
28	49,9	28,6	W				
29	52,6	28,6	W				

center of press-fit pinhead
for connection parameter see the handling instruction

ø0,9 ±0,1
ø2 ±0,5

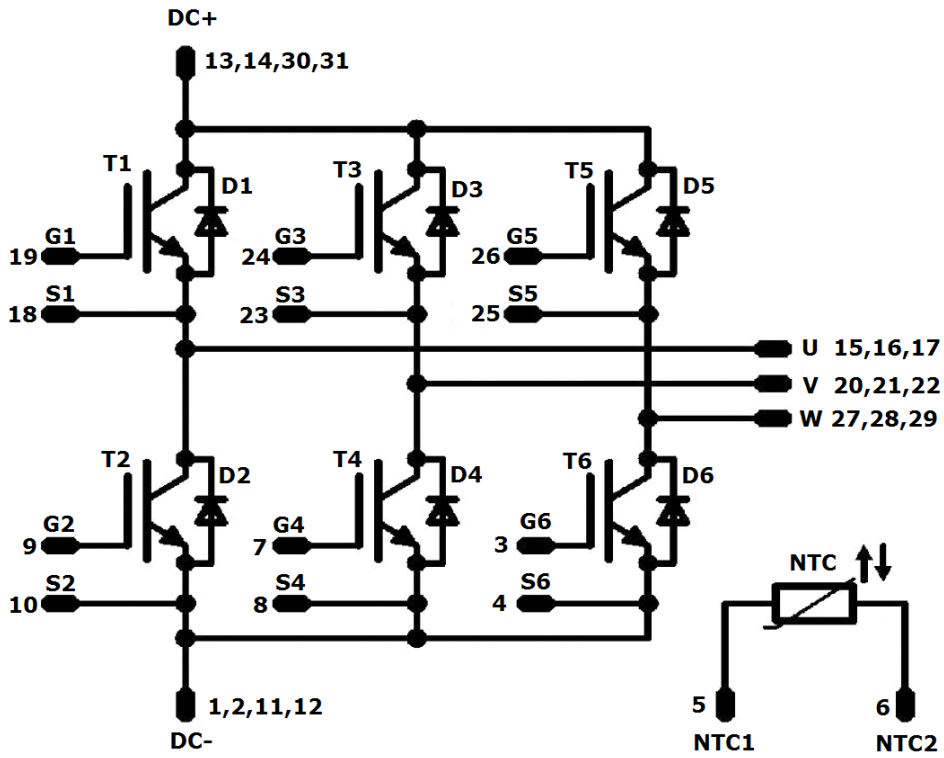
14,3
26,3

Tolerance of pinpositions: ±0,5mm at the end of pins
Dimension of coordinate axis is only offset without tolerance



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Pinout



Identification

ID	Component	Voltage	Current	Function	Comment
T1,T2,T3,T4,T5,T6	IGBT	1200 V	50 A	Inverter Switch	
D1,D2,D3,D4,D5,D6	FWD	1200 V	50 A	Inverter Diode	
NTC	NTC	-	-	Thermistor	



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Packaging instruction					
Standard packaging quantity (SPQ)	100	>SPQ	Standard	<SPQ	Sample

Handling instruction	
Handling instructions for <i>flow</i> 1 packages see vincotech.com website.	

Package data	
Package data for <i>flow</i> 1 packages see vincotech.com website.	

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