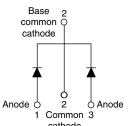


Vishay Semiconductors

## Schottky Rectifier, 2 x 15 A





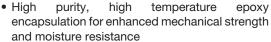
**TO-220AB** 

	2
Anode 6 2	2 O Anode amon 3

PRODUCT SUMMARY					
Package	TO-220AB				
I <sub>F(AV)</sub>	2 x 15 A				
$V_R$	25 V, 40 V, 45 V				
V <sub>F</sub> at I <sub>F</sub>	0.50 V				
I <sub>RM</sub> max.	70 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
Diode variation	Common cathode				
E <sub>AS</sub>	20 mJ				

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation





- · Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

### **DESCRIPTION**

The VS-25CTQ... center tap Schottky rectifier series has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Rectangular waveform	30	А			
V <sub>RRM</sub>	Range	35 to 45	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	990	А			
V <sub>F</sub>	15 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.50	V			
T <sub>J</sub>	Range	- 55 to 150	°C			

VOLTAGE RATINGS										
PARAMETER	SYMBOL	VS- 25CTQ035PbF	VS- 25CTQ035-N3	VS- 25CTQ040PbF	VS- 25CTQ040-N3	VS- 25CTQ045PbF	VS- 25CTQ045-N3	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>									
Maximum working peak reverse voltage	V <sub>RWM</sub>	35	35	40	40	45	45	V		

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS \					
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 102 °C	30	А				
Maximum peak one cycle non-repetitive surge current per leg	I	5 μs sine or 3 μs rect. pulse  Following any rated load condition and with rated V <sub>RRM</sub> applied		990	А			
See fig. 7	I <sub>FSM</sub>			250	A			
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 3.0 A, L = 4.40 mH		20	mJ			
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		3	А			



# VS-25CTQ...PbF Series, VS-25CTQ...-N3 Series

www.vishay.com

# Vishay Semiconductors

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS	
		15 A	T <sub>.1</sub> = 25 °C	0.56	V	
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	30 A	11=23 0	0.71		
	VFM (1)	15 A	T 105 °C	0.50		
		30 A	T <sub>J</sub> = 125 °C	0.64		
Maximum reverse leakage current per leg	I <sub>RM</sub> (1)	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	1.75	- mA	
See fig. 2		T <sub>J</sub> = 125 °C	VR = nateu VR	70		
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz) 25 °C		900	pF	
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs	

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C		
Maximum thermal resistance, junction to case per leg		D	DC operation See fig. 4	3.25			
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	1.63	°C/W		
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased				
Approximate weight				2.0	g		
Approximate weight				0.07	OZ.		
	minimum			6 (5)	kgf · cm		
Mounting torque maximum				12 (10)	(lbf · in)		
				25CTQ035			
Marking device			Case style TO-220AB	25CT	Q040		
				25CT	Q045		



### Vishay Semiconductors

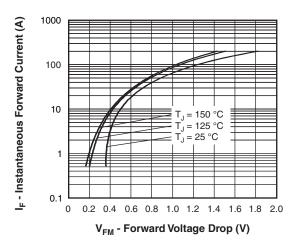


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

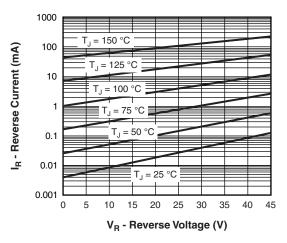


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

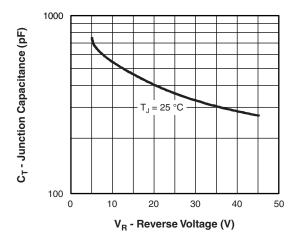


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

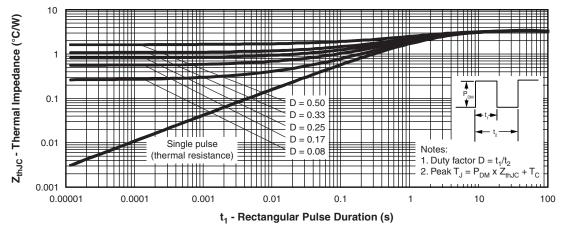


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

## Vishay Semiconductors

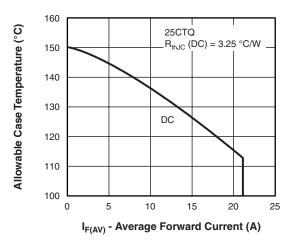


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

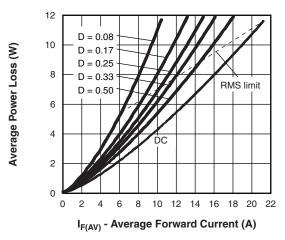


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

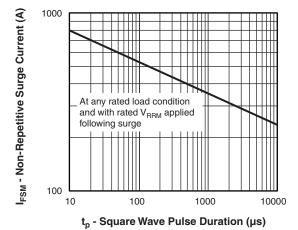


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

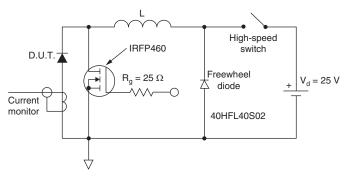


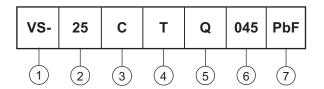
Fig. 8 - Unclamped Inductive Test Circuit



### Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

**Device code** 



Vishay Semiconductors product

2 - Current rating (25 = 25 A)

3 - Circuit configuration:

C = Common cathode

4 - Package:

T = TO-220

5 - Schottky "Q" series

035 = 35 V 040 = 40 V

Voltage ratings

045 = 45 V

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-25CTQ035PbF	50	1000	Antistatic plastic tube				
VS-25CTQ035-N3	50	1000	Antistatic plastic tube				
VS-25CTQ040PbF	50	1000	Antistatic plastic tube				
VS-25CTQ040-N3	50	1000	Antistatic plastic tube				
VS-25CTQ045PbF	50	1000	Antistatic plastic tube				
VS-25CTQ045-N3	50	1000	Antistatic plastic tube				

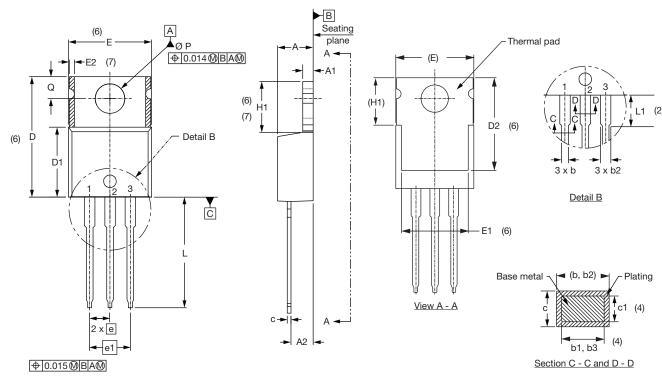
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95222</u>				
Dout moulding information	TO-220AB PbF	www.vishay.com/doc?95225		
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028		
SPICE model		www.vishay.com/doc?95285		



## Vishay Semiconductors

### **TO-220AB**

### **DIMENSIONS** in millimeters and inches



### Lead assignments

### <u>Diodes</u>

- 1. Anode/open
- 2. Cathode
- 3. Anode

### Conforms to JEDEC outline TO-220AB

SYMBOL	MILLIN	MILLIMETERS		INCHES		
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.25	4.65	0.167	0.183		
A1	1.14	1.40	0.045	0.055		
A2	2.56	2.92	0.101	0.115		
b	0.69	1.01	0.027	0.040		
b1	0.38	0.97	0.015	0.038	4	
b2	1.20	1.73	0.047	0.068		
b3	1.14	1.73	0.045	0.068	4	
С	0.36	0.61	0.014	0.024		
c1	0.36	0.56	0.014	0.022	4	
D	14.85	15.25	0.585	0.600	3	
D1	8.38	9.02	0.330	0.355		
D2	11.68	12.88	0.460	0.507	6	

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° to 93°		

#### **Notes**

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- $^{(7)}$  Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline

Lead tip



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