



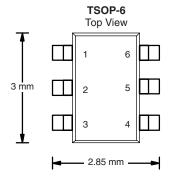
# P-Channel 1.8-V (G-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)		
- 8	0.042 at V <sub>GS</sub> = - 4.5 V	± 5.6		
	0.060 at V <sub>GS</sub> = - 2.5 V	± 4.7		
	0.080 at V <sub>GS</sub> = - 1.8 V	± 2.9		

#### **FEATURES**

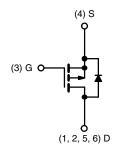
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFETs
- 1.8 V Rated
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si3445DV-T1-E3 (Lead (Pb)-free)

Si3445DV-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> T <sub>A</sub> = 25 °C, unless otherwise noted					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 8	V	
Gate-Source Voltage		V <sub>GS</sub>	± 8	7 v	
Continuous Drain Current (T, = 150 °C) <sup>a, b</sup>	T <sub>A</sub> = 25 °C	I <sub>D</sub>	± 5.6		
Continuous Drain Current (1 <sub>J</sub> = 150 °C) <sup>3, 2</sup>	T <sub>A</sub> = 70 °C		± 4.5		
Pulsed Drain Current		I <sub>DM</sub>	± 20	Α	
Continuous Source Current (Diode Conduction) <sup>a, b</sup>		I <sub>S</sub>	- 1.7		
M. D. D. J. J. ah	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.0	W	
Maximum Power Dissipation <sup>a, b</sup>	T <sub>A</sub> = 70 °C	'D	1.3		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 5 s	$R_{thJA}$		62.5	°C/W	
	Steady State		106		C/VV	

Notes:

a. Surface Mounted on FR4 board.

b.  $t \le 5$  s.

# Vishay Siliconix



<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static								
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = -250 \mu A$	- 0.45		- 1.0	V		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -8 \text{ V}, V_{GS} = 0 \text{ V}$	- 1					
		$V_{DS} = -8 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			- 5	μΑ		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 15			Α		
		$V_{GS} = -4.5 \text{ V}, I_D = -5.6 \text{ A}$		0.034	0.042	Ω		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -2.5 \text{ V}, I_D = -4.7 \text{ A}$		0.048	0.060			
		$V_{GS} = -1.8 \text{ V}, I_D = -2.0 \text{ A}$		0.062	0.080	ı		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 5.6 A		15		S		
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 1.7 A, V <sub>GS</sub> = 0 V		- 0.7	- 1.2	V		
Dynamic <sup>b</sup>								
Total Gate Charge	$Q_g$			15	25			
Gate-Source Charge	$Q_{gs}$	$V_{DS} = -4 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -5.6 \text{ A}$	3	3		nC		
Gate-Drain Charge	$Q_{gd}$			2				
Turn-On Delay Time	t <sub>d(on)</sub>			20	40			
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 4 V, $R_L$ = 4 $\Omega$		50	100			
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ - 1 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 6 $\Omega$		110	220	ns		
Fall Time	t <sub>f</sub>			60	120			
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.7 A, dI/dt = 100 A/μs		60	100			

#### Notes:

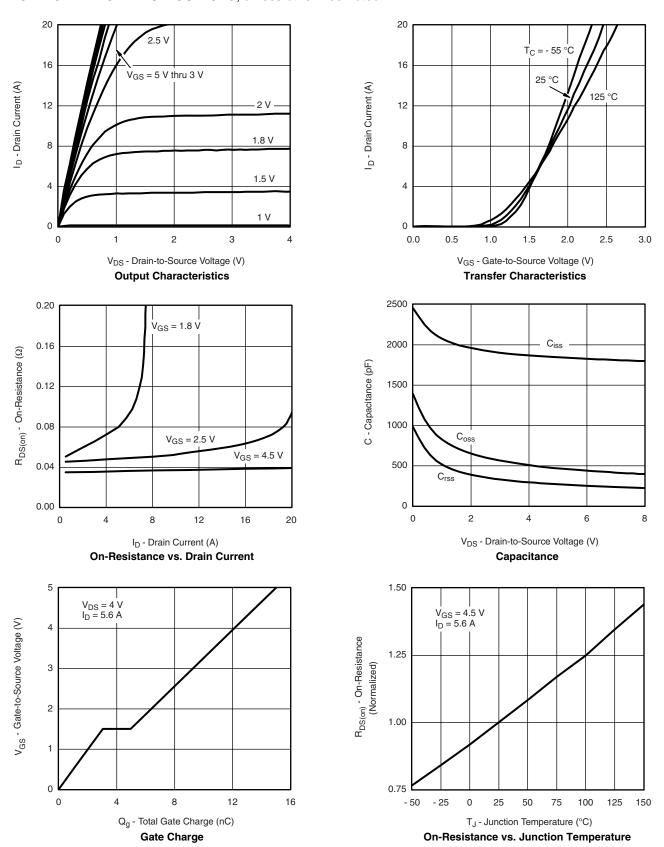
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.



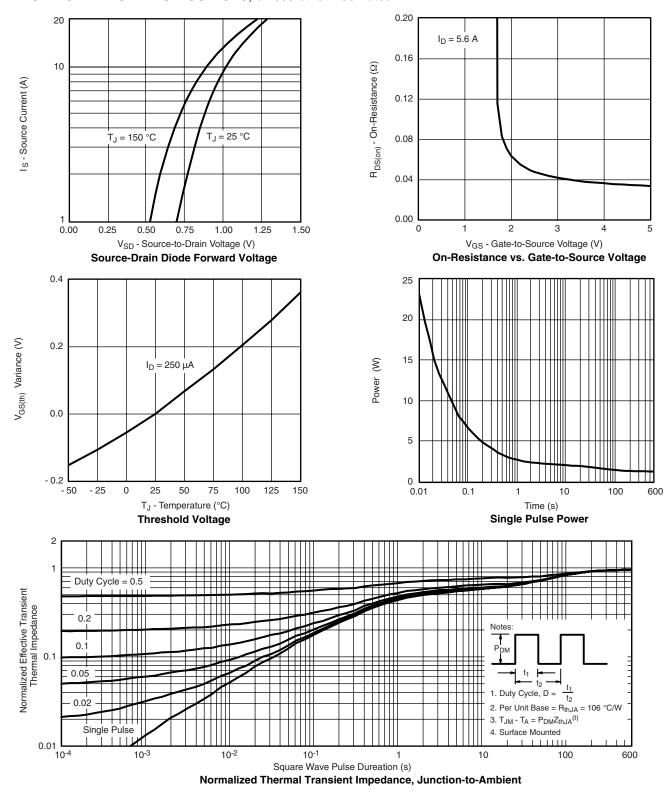
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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