

High Performance Schottky Rectifier, 240 A





HALF-PAK (D-67)

PRIMARY CHARACTERISTICS				
I _{F(AV)} 240 A				
V_{R}	150 V			
Package	HALF-PAK (D-67)			
Circuit configuration	Single diode			

FEATURES

- 175 °C T_J operation
- Low forward voltage drop



- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION

The VS-249NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	240	А			
V_{RRM}		150	V			
I _{FSM}	t _p = 5 μs sine	20 000	Α			
V _F	240 A _{pk} , T _J = 125 °C	0.78	V			
T _J	Range	-55 to +175	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-249NQ150PbF	UNITS		
Maximum DC reverse voltage	V _R	150	V		
Maximum working peak reverse voltage	V_{RWM}	150			

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 121 °C, rectangular waveform		240	
Maximum peak one cycle non-repetitive surge current	l	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	20 000	А
See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse		2300	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 5.5 \text{A}, L = 1 \text{mH}$		15	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	А



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	240 A	T _J = 25 °C	1.21	- V
		480 A		1.65	
		240 A	- T _J = 125 °C	0.78	
		480 A		0.94	
Maximum reverse leakage current per leg See fig. 2	I _{RM}	T _J = 25 °C	V _B = Rated V _B	6	- mA
		T _J = 125 °C	v _R = nateu v _R	85	
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		6000	pF
Typical series inductance	L _S	From top of terminal hole to mounting plane		5.0	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width $<300~\mu s,$ duty cycle <2~%

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage tem	perature range	T _J , T _{Stg}		-55 to 175	°C	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	0.19	°C/W	
Typical thermal resistance, case to	heatsink	R _{thCS}	Mounting surface, smooth and greased	0.05		
Approximate weight				30	g	
				1.06	oz.	
Mounting torque	minimum			3 (26.5)		
	maximum		Non-lubricated threads	4 (35.4)	$\begin{array}{c} N \cdot m \\ \text{(lbf} \cdot \text{in)} \end{array}$	
Terminal torque	minimum			3.4 (30)		
	maximum			5 (44.2)		
Case style				HALF-PAK module		

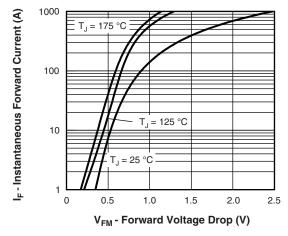


Fig. 1 - Maximum Forward Voltage Drop Characteristics

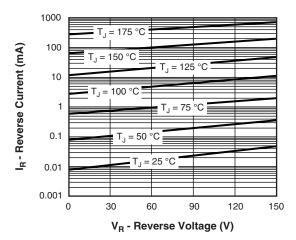


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



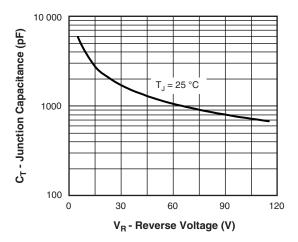


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

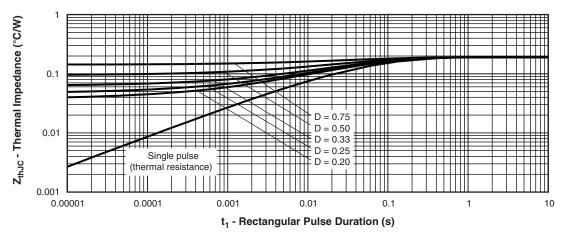


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

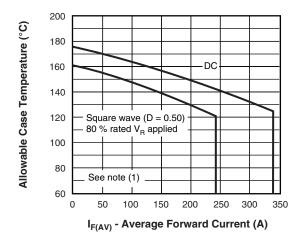
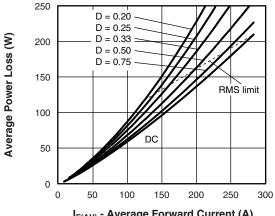
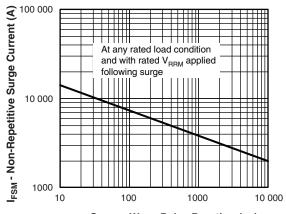


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



I_{F(AV)} - Average Forward Current (A)

Fig. 6 - Forward Power Loss Characteristics



 t_p - Square Wave Pulse Duration (μ s)

Fig. 7 - Maximum Non-Repetitive Surge Current

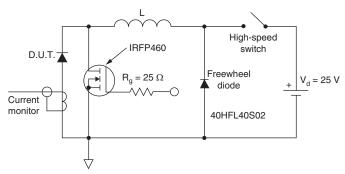


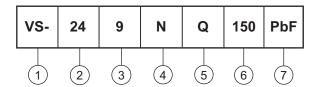
Fig. 8 - Unclamped Inductive Test Circuit

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = rated V_R

ORDERING INFORMATION TABLE

Device code



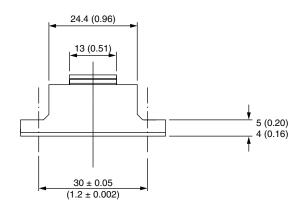
- 1 Vishay Semiconductors product
- 2 Average current rating (x 10)
- Product silicon identification
- 4 N = not isolated
- 5 Q = Schottky rectifier diode
- 6 Voltage rating (150 = 150 V)
- 7 Lead (Pb)-free

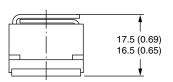
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95020			

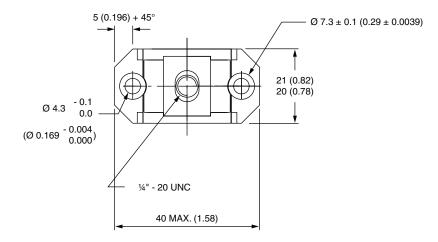


D-67 HALF-PAK

DIMENSIONS in millimeters (inches)









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