



High Voltage (up to 0.5 kV) Thick Film Chip Resistors



FEATURES

- High operating voltage (up to 500 V)
- Pure tin solder contacts on Ni barrier layer provides compatibility with lead (Pb)-free and lead containing soldering processes



- Metal glaze on high quality ceramic
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

STANDARD ELECTRICAL SPECIFICATIONS								
MODEL	CASE SIZE INCH	CASE SIZE METRIC	POWER RATING P ₇₀ W	LIMITINGELEMENT VOLTAGE U _{MAX.} AC _{RMS} /DC V	TEMPERATURE COEFFICIENT ± ppm/K	TOLERANCE ± %	$\begin{array}{c} \text{RESISTANCE} \\ \text{RANGE} \\ \Omega \end{array}$	SERIES
RCV0805 e3	0805	05 RR 2012M 0.1	0.125	400	100	1	100K to 10M	E24; E96
HCV0003 e3	0803	NN 2012W			200	5	TOOK TO TOW	E24
DCV/1006 o2	1206	RR 3216M	0.25	500	100	1	100K to 10M	E24; E96
RCV1206 e3	1206	nn 32101VI	0.25	500	200	5	1000 10 10101	E24

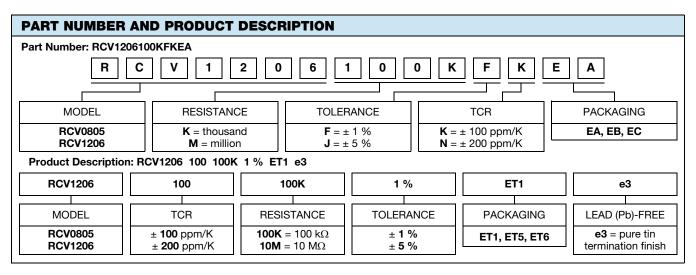
Notes

- These resistors do not feature a lifetime limitation when operated within the limits of rated dissipation, permissible operating voltage and
 permissible film temperature. However, the resistance typically increases due to the resistor's film temperature over operating time, generally
 known as drift. The drift may exceed the stability requirements of an individual application circuit and thereby limits the functional lifetime
- · No marking
- · Power rating depends on the max. temperature at the solder point, the component placement density and the substrate material

TECHNICAL SPECIFICATIONS						
PARAMETER	UNIT	RCV0805	RCV1206			
Rated dissipation P_{70} ⁽¹⁾	W	0.125	0.25			
Limiting element voltage U _{max.} AC _{RMS} /DC	V	400	500			
Insulation voltage U _{ins.} (1 min)	V	> 5	00			
Voltage coefficient of resistance chart	ppm/V	25	5			
Insulation resistance	Ω	> 1	> 10 ⁹			
Operating temperature range	°C	-55 to	+155			
Weight	mg	5.5	10			

Note

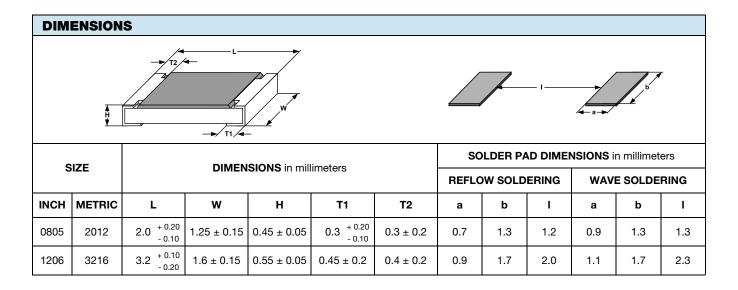
⁽¹⁾ The power dissipation on the resistors generates a temperature rise against the local ambient, depending on the heat flow support of the printed-circuit board (thermal resistance). The rated dissipation applies only if the permitted film temperature of 155 °C is not exceeded



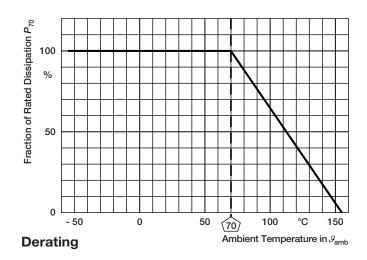


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PACKAGING							
MODEL	CODE	QUANTITY	CARRIER TAPE	WIDTH	PITCH	REEL DIAMETER	
	EA = ET1	5000		8 mm	4 mm	180 mm/7"	
RCV0805	EB = ET5	10 000				285 mm/11.25"	
	EC = ET6	20 000	Paper tape acc. to IEC 60068-3 Type I			330 mm/13"	
	EA = ET1	5000				180 mm/7"	
RCV1206	EB = ET5	10 000	7.			285 mm/11.25"	
	EC = ET6	20 000				330 mm/13"	



FUNCTIONAL PERFORMANCE





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EN 00445 4	IEO 00000 0		PROCE	DURE	REQUIREMENTS PERMISSIBLE CHANGE (ΔR)		
EN 60115-1 CLAUSE	TEST METHOD	TEST	Stability for pro	oduct types:			
			RCV e3		100 kΩ to 10 MΩ		
4.5	-	Resistance	-		± 1 %	± 5 %	
		Short time overload	$U = 2.5 \times \sqrt{P_{70} \times R}$ $\leq 2 \times U_{\text{max.}};$				
4.13	-		Style	Duration	± (0.25 % R + 0.05 Ω)	± (0.5 % R + 0.05 Ω)	
			RR2012M	1 s	-		
			RR3216M	2 s			
		Solderability	Solder bath method; Sn60Pb40 non-activated flux; (235 ± 5) °C (2 ± 0.2) s		Good tinning (≥ 95 % covered); no visible damage		
4.17.2 58 (Td)		Solderability	Solder bath method; Sn96.5Ag3Cu0.5 non-activated flux; (245 ± 5) °C (3 ± 0.3) s		Good tinning (≥ 95 % covered); no visible damage		
4.8.4.2	-	Temperature coefficient	(20 / -55 / 20) °C and (20 / 155 / 20) °C		± 100 ppm/K	± 200 ppm/K	
4.32	21 (Uu ₃)	Shear (adhesion)	17.7 N		No visible damage		
4.33	21 (Uu ₁)	Uu₁) Substrate bending		2 mm; nes	No visible damage, no open circuit in bent position $\pm (0.25~\%~R + 0.05~\Omega)$		
4.19	14 (Na)	Rapid change of temperature	30 min. at -55 °C; 30 min. at 125 °C				
			5 cycles		± (0.25 % R + 0.05 Ω)	$\pm (0.5 \% R + 0.05 \Omega)$	
			1000 cycles		± (1 % R + 0.05 Ω)	± (1 % R + 0.05 Ω)	
4.23	-	Climatic sequence:	-				
4.23.2	2 (Ba)	Dry heat	125 °C	; 16 h			
4.23.3	30 (Db)	Damp heat, cyclic	55 °C; ≥ 90 % RH 24 h; 1 cycle				
4.23.4	1 (Aa)	Cold	-55 °C	C; 2 h	± (1 % R + 0.05 Ω)	$\pm (2 \% R + 0.1 \Omega)$	
4.23.5	13 (M)	Low air pressure	1 kPa; (25 ±	10) °C; 1 h			
4.23.6	30 (Db)	Damp heat, cyclic	55 °C; . 9 24 h; 5				
4.23.7	-	DC load	$U = \sqrt{P_{70} \times R}$				
4.25.1	-	Endurance	$U = \sqrt{P_{70} \times R} \le U_{\text{max.}};$ 1.5 h on; 0.5 h off; 70 °C; 1000 h				
		at 70 °C			± (1 % R + 0.05 Ω)	± (2 % R + 0.1 Ω)	
			70 °C; 8000 h		± (2 % R + 0.1 Ω)	± (4 % R + 0.1 Ω)	
4.18.2	58 (Td)	Resistance to soldering heat	Solder bath method (260 ± 5) °C; (10 ± 1) s		± (0.25 % R + 0.05 Ω)	± (0.5 % R + 0.05 Ω)	
4.24	78 (Cab)	Damp heat, steady state	(40 ± 3) (93 ± 3) 56 d	% RH;	± (1 % R + 0.05 Ω)	± (2 % R + 0.05 Ω)	



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TEST PROCEDURES AND REQUIREMENTS						
EN 60115-1	IEC 60068-2		PROCEDURE	REQUIREMENTS PERMISSIBLE CHANGE (△R)		
CLAUSE	TEST METHOD	TEST	Stability for product types:	100 k Ω to 10 M Ω		
			RCV e3			
4.25.3	-	Endurance at upper category temperature	155 °C; 1000 h	± (1 % R + 0.05 Ω)	± (2 % R + 0.05 Ω)	
4.40	-	Electrostatic discharge (human body model)	IEC 61340-3-1; 3 pos. + 3 neg. discharges; ESD voltage acc. to style	± (1 % R + 0.05 Ω)		
4.29	45 (XA)	Component solvent resistance	Isopropyl alcohol; 50 °C; method 2	No visible damage		
4.30	45 (XA)	Solvent resistance of marking	Isopropyl alcohol; 50 °C; method 1, toothbrush	Marking legible, no visible damage		
4.22	6 (Fc)	Vibration, endurance by sweeping	f = 10 Hz to 2000 Hz; x, y, z ≤ 1.5 mm; A ≤ 200 m/s²; 10 sweeps per axis	± (0.25 % R + 0.05 Ω)	$\pm (0.5 \% R + 0.05 \Omega)$	
4.37	-	Periodic electric overload	$U = \sqrt{15 \times P_{70} \times R}$ $\leq 2 \times U_{\text{max.}};$ 0.1 s on; 2.5 s off; 1000 cycles	± (1 % <i>R</i> + 0.05 Ω)		
4.27	-	Single pulse high voltage overload, 10 µs/700 µs	$\hat{U} = 10 \times \sqrt{P_{70} \times R}$ $\leq 2 \times U_{\text{max}};$ 10 pulses	± (1 % R + 0.05 Ω)		

All tests are carried out in accordance with the following specifications:

- EN 60115-1, generic specification
- EN 140400, sectional specification
- EN 140401-802, detail specification
- IEC 60068-2-x, environmental test procedures



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