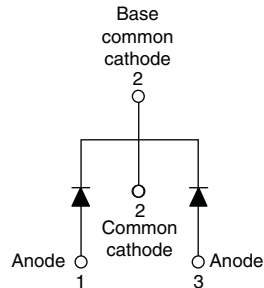


**Ultrafast Rectifier, 2 x 8 A FRED Pt<sup>®</sup>**


TO-220AB


**FEATURES**

- Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)


**DESCRIPTION/APPLICATIONS**

VS-MUR1620CTPbF is the state of the art ultrafast recovery rectifier specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

PRODUCT SUMMARY	
Package	TO-220AB
$I_{F(AV)}$	2 x 8 A
$V_R$	200 V
$V_F$ at $I_F$	0.975 V
$t_{rr}$ typ.	See Recovery table
$T_J$ max.	175 °C
Diode variation	Common cathode

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		200	V
Average rectified forward current	$I_{F(AV)}$	per leg	8.0	A
		total device	Rated $V_R$ , $T_C = 150$ °C	
Non-repetitive peak surge current per leg	$I_{FSM}$		100	
Peak repetitive forward current per leg	$I_{FM}$	Rated $V_R$ , square wave, 20 kHz, $T_C = 150$ °C	16	
Operating junction and storage temperatures	$T_J, T_{Stg}$		- 65 to 175	°C

ELECTRICAL SPECIFICATIONS ( $T_J = 25$ °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_R$	$I_R = 100$ $\mu$ A	200	-	-	V
Forward voltage	$V_F$	$I_F = 8$ A	-	-	0.975	
		$I_F = 8$ A, $T_J = 150$ °C	-	-	0.895	
Reverse leakage current	$I_R$	$V_R = V_R$ rated	-	-	5	$\mu$ A
		$T_J = 150$ °C, $V_R = V_R$ rated	-	-	250	
Junction capacitance	$C_T$	$V_R = 200$ V	-	25	-	pF
Series inductance	$L_S$	Measured lead to lead 5 mm from package body	-	8.0	-	nH



DYNAMIC RECOVERY CHARACTERISTICS (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 1.0 A, dI <sub>F</sub> /dt = 50 A/μs, V <sub>R</sub> = 30 V	-	-	35	ns	
		I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1.0 A, I <sub>REC</sub> = 0.25 A	-	-	25		
		T <sub>J</sub> = 25 °C	-	20	-		
		T <sub>J</sub> = 125 °C	-	34	-		
Peak recovery current	I <sub>RPM</sub>	I <sub>F</sub> = 8 A dI <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 160 V	T <sub>J</sub> = 25 °C	-	1.7	-	A
			T <sub>J</sub> = 125 °C	-	4.2	-	
Reverse recovery charge	Q <sub>rr</sub>		T <sub>J</sub> = 25 °C	-	23	-	nC
			T <sub>J</sub> = 125 °C	-	75	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	-	3.0	°C/W
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>		-	-	50	
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2.0	-	g
			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220AB	MUR1620CT			

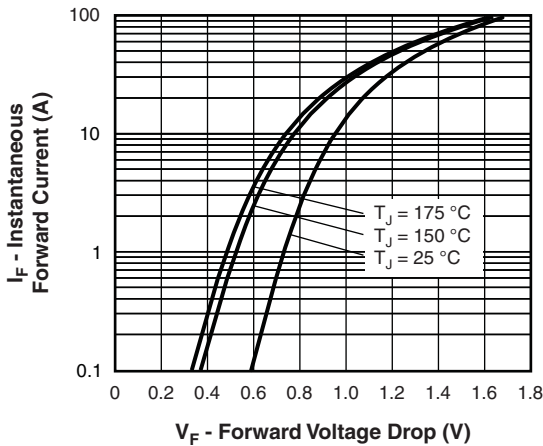


Fig. 1 - Typical Forward Voltage Drop Characteristics

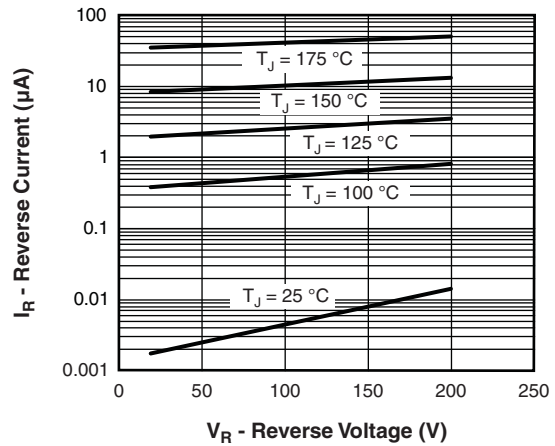


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

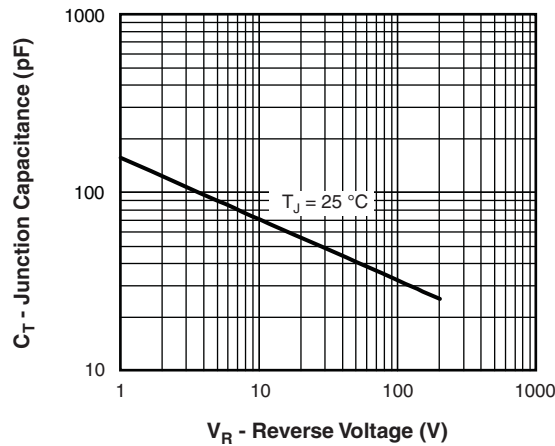


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

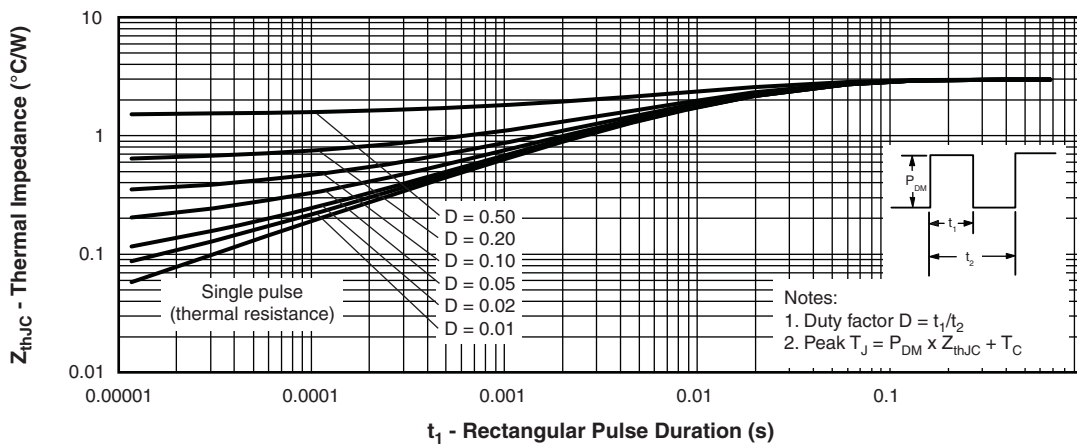


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

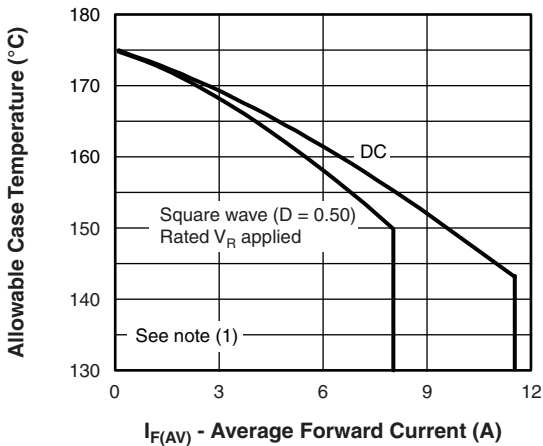


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

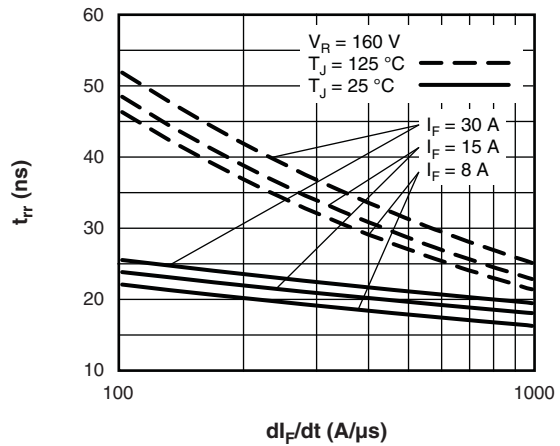


Fig. 7 - Typical Reverse Recovery Time vs.  $di_F/dt$

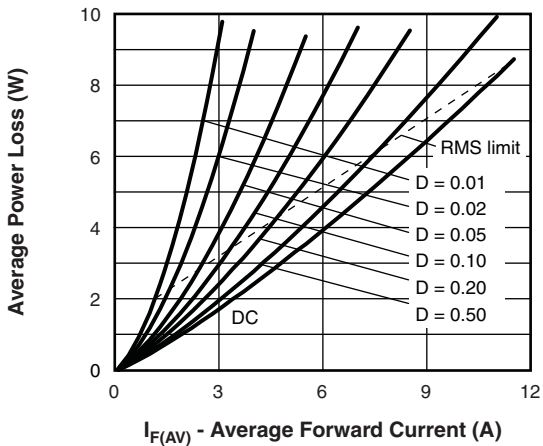


Fig. 6 - Forward Power Loss Characteristics

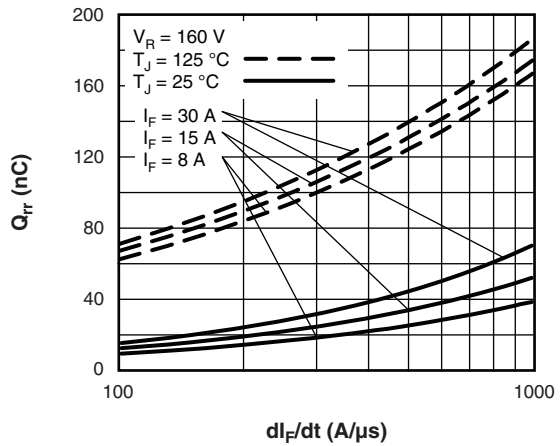


Fig. 8 - Typical Stored Charge vs.  $di_F/dt$

**Note**

- (1) Formula used:  $T_C = T_J - (P_d + P_{d_{REV}}) \times R_{thJC}$ ;
- $P_d$  = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);
- $P_{d_{REV}}$  = Inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = Rated  $V_R$

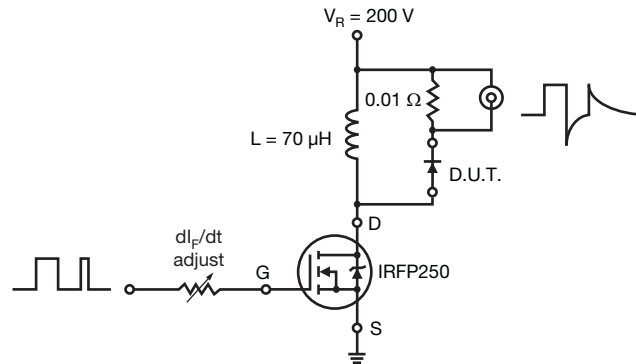
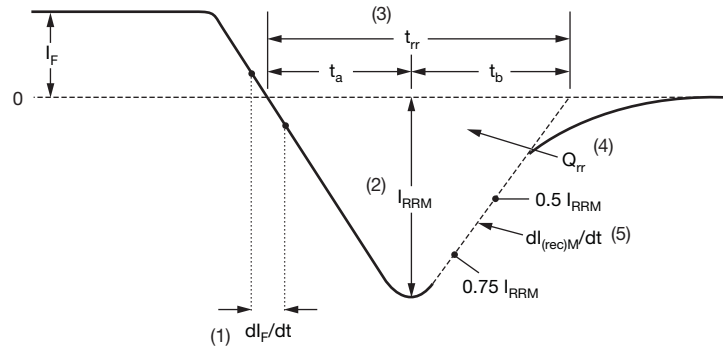


Fig. 9 - Reverse Recovery Parameter Test Circuit



(1)  $di_F/dt$  - rate of change of current through zero crossing

(2)  $I_{RRM}$  - peak reverse recovery current

(3)  $t_{rr}$  - reverse recovery time measured from zero crossing point of negative going  $I_F$  to point where a line passing through  $0.75 I_{RRM}$  and  $0.50 I_{RRM}$  extrapolated to zero current.

(4)  $Q_{rr}$  - area under curve defined by  $t_{rr}$  and  $I_{RRM}$

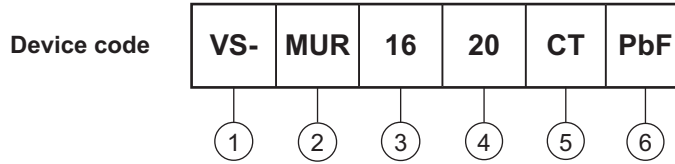
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$

Fig. 10 - Reverse Recovery Waveform and Definitions



## ORDERING INFORMATION TABLE



- 1** - Vishay Semiconductors product
- 2** - Ultrafast MUR series
- 3** - Current rating (16 = 16 A)
- 4** - Voltage rating (20 = 200 V)
- 5** - CT = Center tap (dual)
- 6** - Environmental digit:

PbF = Lead (Pb)-free and RoHS compliant

-N3 = Halogen-free, RoHS compliant and totally lead (Pb)-free

<b>ORDERING INFORMATION</b> (Example)			
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-MUR1620CTPbF	50	1000	Antistatic plastic tube
VS-MUR1620CT-N3	50	1000	Antistatic plastic tube

<b>LINKS TO RELATED DOCUMENTS</b>	
Dimensions	<a href="http://www.vishay.com/doc?95222">www.vishay.com/doc?95222</a>
Part marking information	TO-220ABPbF <a href="http://www.vishay.com/doc?95225">www.vishay.com/doc?95225</a>
	TO-220AB-N3 <a href="http://www.vishay.com/doc?95028">www.vishay.com/doc?95028</a>



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**Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.**

**Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.**

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[>>Vishay\(威世\)](#)