



Dual N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)			
20	0.280 at V _{GS} = 4.5 V	1.28			
	0.360 at V _{GS} = 2.5 V	1.13			
	0.450 at V _{GS} = 1.8 V	1.0			

FEATURES

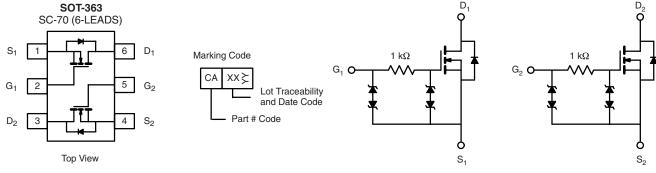
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFETs: 1.8 V Rated
- ESD Protected: 2000 V
- Thermally Enhanced SC-70 Package
- Compliant to RoHS Directive 2002/95/EC



ROHS COMPLIANT HALOGEN FREE

APPLICATIONS

- Load Switching
- PA Switch
- Level Switch



Ordering Information: Si1912EDH-T1-E3 (Lead (Pb)-free)

Si1912EDH-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATIN	GS T _A = 25 °C,	unless other	wise noted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V_{DS}	20		V
ate-Source Voltage		V _{GS}	± 12		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	I _D	1.28	1.13	А
	T _A = 85 °C		0.92	0.81	
Pulsed Drain Current		I _{DM}	4		
Continuous Diode-Current (Diode Conduction) ^a		I _S	0.61	0.48	
Maximum Power Dissipation ^a	T _A = 25 °C	В	0.74	0.57	W
	T _A = 85 °C	- P _D	0.38	0.30	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	t ≤ 5 s	- R _{thJA}	130	170	°C/W	
	Steady State		170	220		
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	80	100		

Notes:

a. Surface mounted on 1" x 1" FR4 board.

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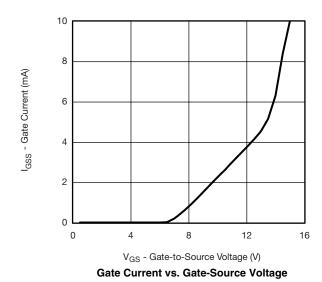
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 100 \mu A$	0.45			V	
Gate-Body Leakage	lgss	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$			± 1	μΑ	
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 10	mA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 16 V, V _{GS} = 0 V			1		
		V _{DS} = 16 V, V _{GS} = 0 V, T _J = 85 °C			5	μΑ	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 4.5 V	2			Α	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 1.13 \text{ A}$		0.220	0.280	Ω	
		V _{GS} = 2.5 V, I _D = 0.99 A		0.281	0.360		
		V _{GS} = 1.8 V, I _D = 0.2 A		0.344	0.450		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 1.13 A		2.6		S	
Diode Forward Voltage ^a	V _{SD}	I _S = 0.48 A, V _{GS} = 0 V		0.8	1.2	V	
Dynamic ^b							
Total Gate Charge	Qg			0.65	1	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 1.13 \text{ A}$		0.2			
Gate-Drain Charge	Q _{gd}			0.23			
Turn-On Delay Time	t _{d(on)}			45	70	ns	
Rise Time	t _r	$V_{DD} = 10 \text{ V}, R_{L} = 20 \Omega$		85	130		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 0.5 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 6 \Omega$		350	530		
Fall Time	t _f	1		210	320		

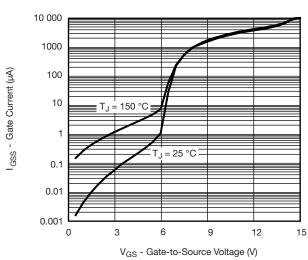
Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

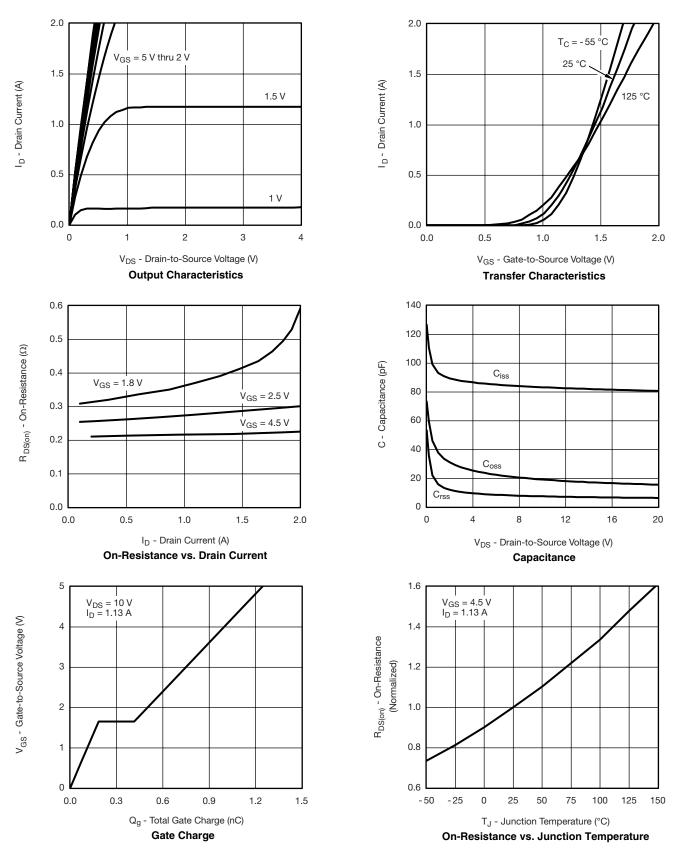




Gate Current vs. Gate-Source Voltage



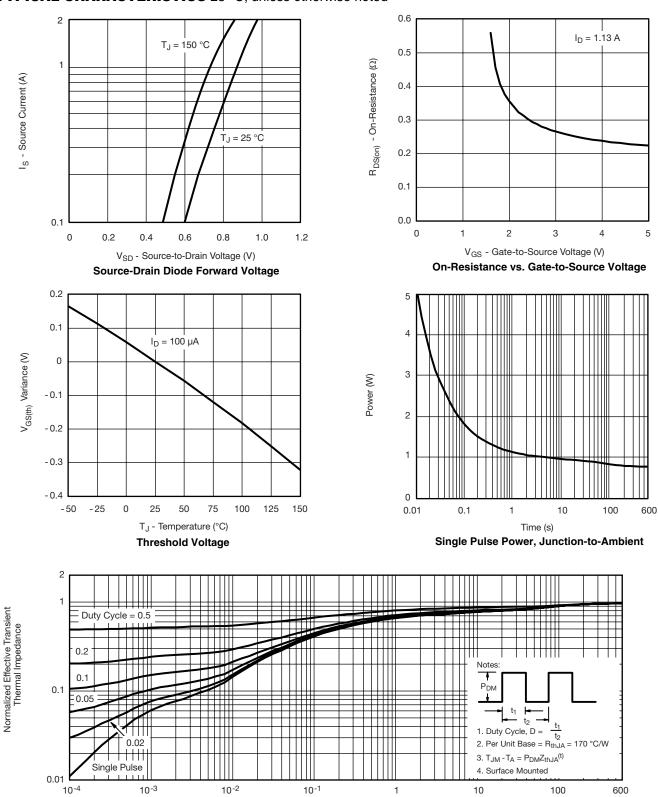
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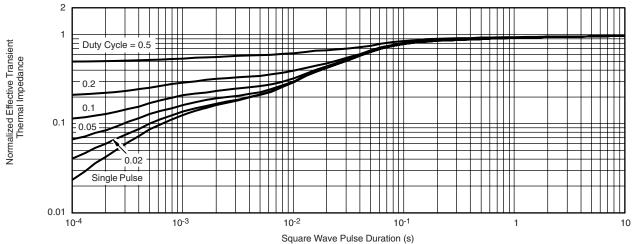
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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