

COMPLIANT

## **High Performance Schottky Rectifier, 220 A**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub> 220 A				
$V_{R}$	30 V			
Package	TO-244			
Circuit configuration	Two diodes common cathode			

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- · Center tap module
- · Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION / APPLICATIONS**

The VS-220CNQ.. center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature.

The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES U			
I <sub>F(AV)</sub>	Rectangular waveform	220	А		
V <sub>RRM</sub>		30	V		
I <sub>FSM</sub>	$t_p = 5 \mu s sine$	18 000	Α		
V <sub>F</sub>	110 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.41	V		
T <sub>J</sub>	Range	-55 to +150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-220CNQ030PbF	UNITS	
Maximum DC reverse voltage	$V_{R}$	30	V	
Maximum working peak reverse voltage	$V_{RWM}$	30	V	

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	L TEST CONDITIONS		VALUES	UNITS		
Maximum average forward current	per leg	50 % distributed at T = 100 % constant and a supplementation				50 % duty avalant T 100 % master avalance of the	110	
See fig. 5	per device	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 122 °C, rectangular waveform		А			
Maximum peak one cycle non-repetitive			Following any rated load condition and with rated	18 000				
surge current per leg See fig. 7		IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	1950			
Non-repetitive avalanche	energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 15 A, L = 1 mH		99	mJ		
Repetitive avalanche cur	rent per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_R$ typical		Α			



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V <sub>FM</sub> <sup>(1)</sup>	110 A	T 05.00	0.49	. V
Maximum forward voltage drop per leg		220 A	- T <sub>J</sub> = 25 °C	0.59	
See fig. 1		110 A	T 105 °C	0.41	
		220 A	- T <sub>J</sub> = 125 °C	0.55	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm R}$ = Rated $V_{\rm R}$	10	- mA
See fig. 2		T <sub>J</sub> = 125 °C	V <sub>R</sub> = nateu v <sub>R</sub>	650	
Maximum junction capacitance per leg	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		7400	pF
Typical series inductance per leg	L <sub>S</sub>	From top of terminal hole to mounting plane		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000		V/µs	

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temper	ature range	T <sub>J</sub> , T <sub>Stg</sub>	-55	-	150	°C
Thermal resistance, junction to case	per leg	$R_{thJC}$	-	-	0.38	°C/W
memai resistance, junction to case	per module	□thJC	-	-	0.19	
Thermal resistance, case to heatsink		R <sub>thCS</sub>	-	0.10	-	
Maria				68		g
Weight			1	2.4	-	OZ.
Mounting torque			35.4 (4)	-	53.1 (6)	
Mounting torque center hole			30 (3.4)	-	40 (4.6)	lbf ⋅ in (N ⋅ m)
Terminal torque			30 (3.4)	-	44.2 (5)	]
Vertical pull			-	-	80	11.6.1.
2" lever pull			-	-	35	- lbf ⋅ in

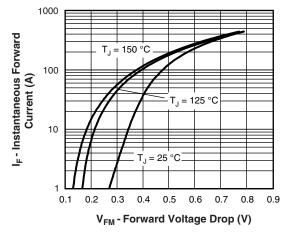


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

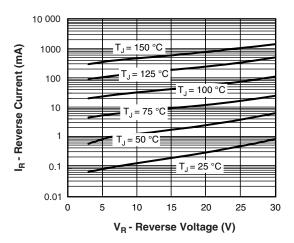


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)



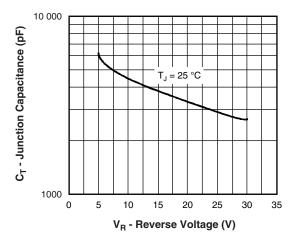


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

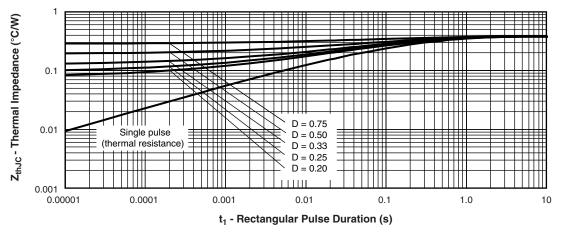


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

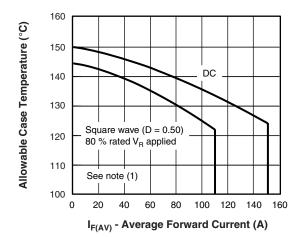


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

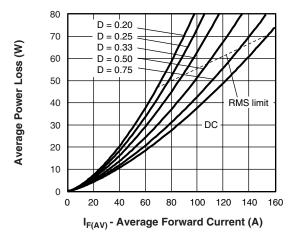


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

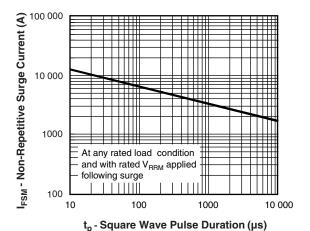


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

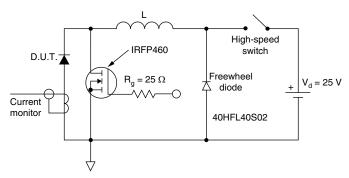


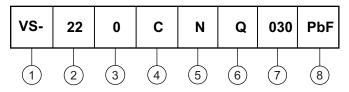
Fig. 8 - Unclamped Inductive Test Circuit

#### Note

 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>th,JC</sub>; Pd = forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>

#### **ORDERING INFORMATION TABLE**

### Device code



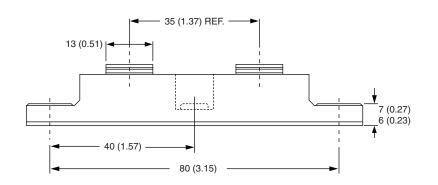
- Vishay Semiconductors product
- Average current rating (x 10)
- Product silicon identification
- C = circuit configuration
- 5 N = not isolated
- 6 Q = Schottky rectifier diode
- 7 Voltage rating (30 V)
- 8 Lead (Pb)-free

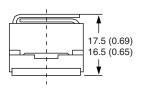
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95021			

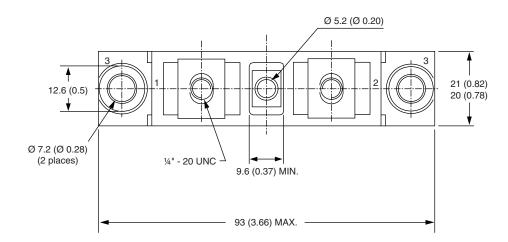


### **TO-244**

### **DIMENSIONS** in millimeters (inches)









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