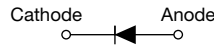


## Hyperfast Rectifier, 3 A FRED Pt<sup>®</sup>


**SMC (DO-214AB)**


### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	3 A
$V_R$	200 V
$V_F$ at $I_F$	0.69 V
$t_{rr}$	25 ns
$T_J$ max.	175 °C
Package	SMC (DO-214AB)
Circuit configuration	Single

### FEATURES

- Hyperfast recovery time, reduced  $Q_{rr}$ , and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

### DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, piezo-injection, as high frequency rectifiers, and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

### MECHANICAL DATA

**Case:** SMC (DO-214AB)

Molding compound meets UL 94 V-0 flammability rating

**Terminals:** matte tin plated leads, solderable per J-STD-002

**Polarity:** color band denotes cathode end

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		200	V
Average rectified forward current	$I_{F(AV)}$	$T_{Sp} = 142$ °C	3	A
Non-repetitive peak surge current	$I_{FSM}$	$T_J = 25$ °C, 6 ms square pulse	130	
Operating junction and storage temperatures	$T_J, T_{Stg}$		-55 to +175	°C



ELECTRICAL SPECIFICATIONS (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	200	-	-	V
Forward voltage, per diode	V <sub>F</sub>	I <sub>F</sub> = 3 A	-	0.83	0.90	
		I <sub>F</sub> = 3 A, T <sub>J</sub> = 125 °C	-	0.69	0.75	
Reverse leakage current, per diode	I <sub>R</sub>	V <sub>R</sub> = V <sub>R</sub> rated	-	-	2	μA
		T <sub>J</sub> = 125 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	2	10	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V	-	23	-	pF

DYNAMIC RECOVERY CHARACTERISTICS (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 1 A, di <sub>F</sub> /dt = 50 A/μs, V <sub>R</sub> = 30 V	-	27	-	ns
		I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, I <sub>rr</sub> = 0.25 A	-	-	25	
		T <sub>J</sub> = 25 °C	-	18	-	
		T <sub>J</sub> = 125 °C	-	30	-	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	-	2.1	-	A
		T <sub>J</sub> = 125 °C	-	4	-	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C	-	19	-	nC
		T <sub>J</sub> = 125 °C	-	60	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C
Thermal resistance, junction to mount	R <sub>thJM</sub>	Device mounted on PCB with 2 x 3.5 mm soldering lands	-	7.7	14	°C/W
Approximate weight			0.24		g	
			0.008		oz.	
Marking device		Case style SMC (DO-214AB)	3H2			

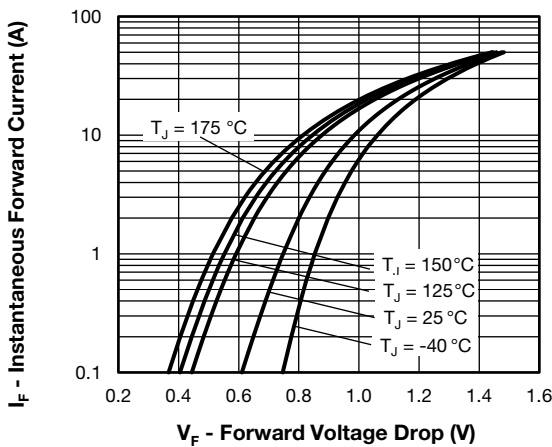


Fig. 1 - Typical Forward Voltage Drop Characteristics

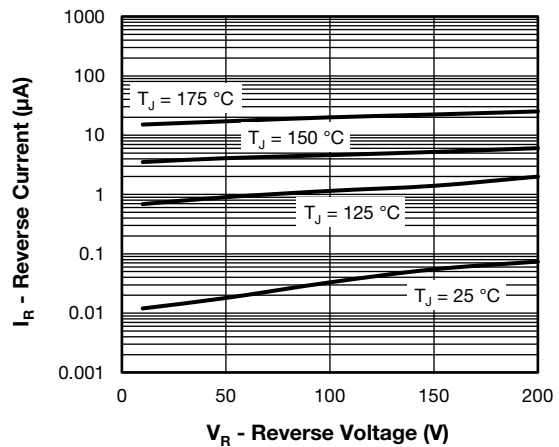


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

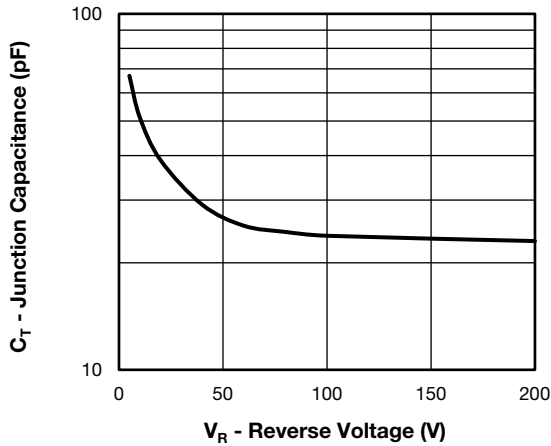


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

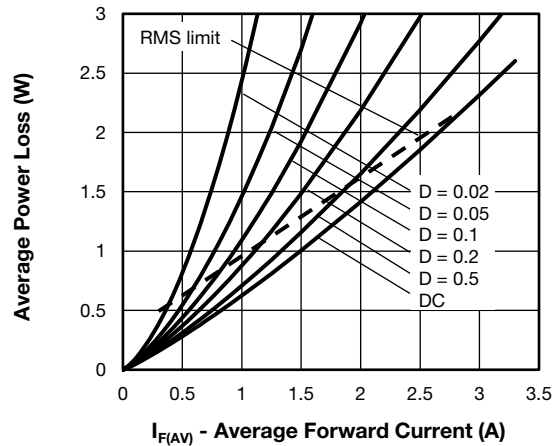


Fig. 5 - Forward Power Loss Characteristics

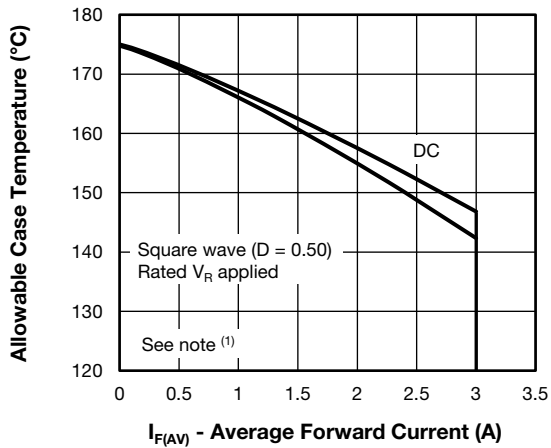


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

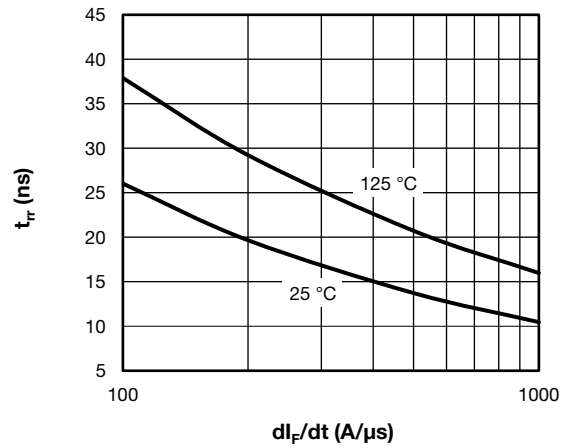


Fig. 6 - Typical Reverse Recovery Time vs.  $dI_F/dt$

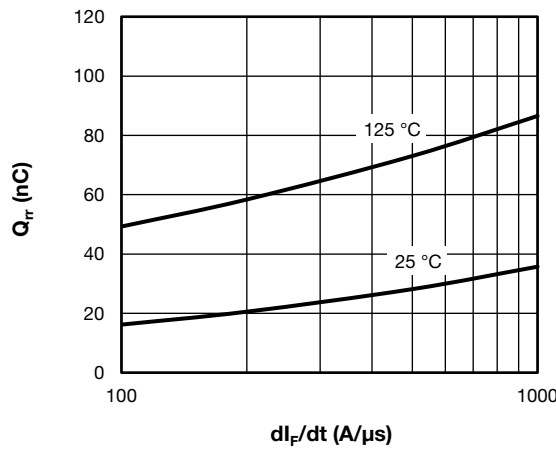
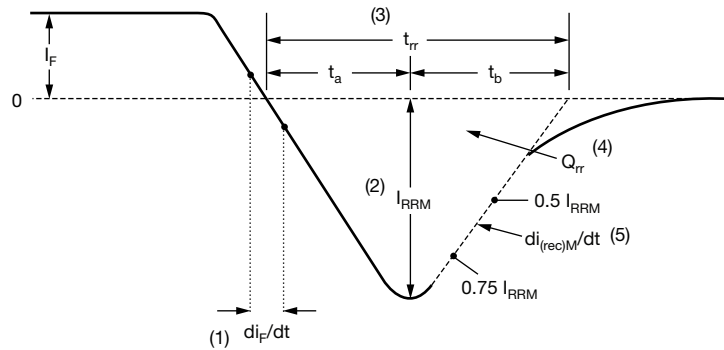


Fig. 7 - Typical Stored Charge vs.  $dI_F/dt$

**Note**

(1) Formula used:  $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$ ;  
 $P_d$  = forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 5);  
 $P_{dREV}$  = inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = rated  $V_R$



- (1)  $di_F/dt$  - rate of change of current through zero crossing
- (2)  $I_{RRM}$  - peak reverse recovery current
- (3)  $t_{rr}$  - reverse recovery time measured from zero crossing point of negative going  $I_F$  to point where a line passing through  $0.75 I_{RRM}$  and  $0.50 I_{RRM}$  extrapolated to zero current.
- (4)  $Q_{rr}$  - area under curve defined by  $t_{rr}$  and  $I_{RRM}$
- $$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$
- (5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$

Fig. 8 - Reverse Recovery Waveform and Definitions

**ORDERING INFORMATION TABLE**

Device code	<b>VS-</b>	<b>3</b>	<b>E</b>	<b>C</b>	<b>H</b>	<b>02</b>	<b>H</b>	<b>M3</b>
	①	②	③	④	⑤	⑥	⑦	⑧

- ① - Vishay Semiconductors product
- ② - Current code (3 = 3 A)
- ③ - Circuit configuration:  
E = single diode
- ④ - C = SMC package
- ⑤ - Process type,  
H = hyperfast recovery
- ⑥ - Voltage code (02 = 200 V)
- ⑦ - H = AEC-Q101 qualified
- ⑦ - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

**ORDERING INFORMATION (Example)**

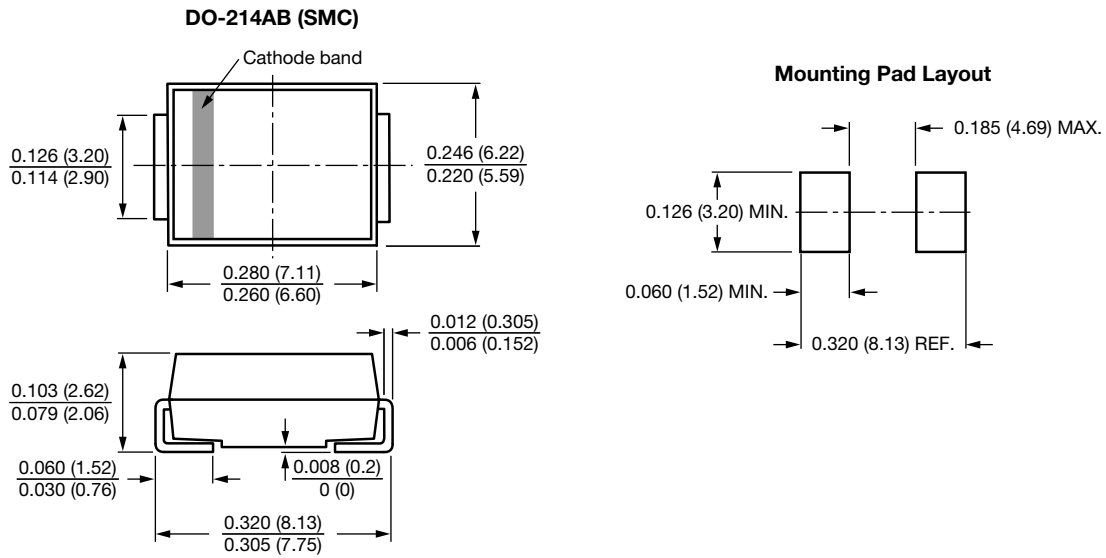
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-3ECH02HM3/9AT	3500	3500	13" diameter plastic tape and reel

**LINKS TO RELATED DOCUMENTS**

Dimensions	<a href="http://www.vishay.com/doc?95402">www.vishay.com/doc?95402</a>
Part marking information	<a href="http://www.vishay.com/doc?95472">www.vishay.com/doc?95472</a>
Packaging information	<a href="http://www.vishay.com/doc?95404">www.vishay.com/doc?95404</a>

## SMC

**DIMENSIONS** in inches (millimeters)





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