Vishay Semiconductors



Standard SMD LED PLCC-2



DESCRIPTION

These devices have been designed to meet the increasing demand for surface mounting technology.

The package of the VLMG31-series is the PLCC-2.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled up with clear epoxy.

PRODUCT GROUP AND PACKAGE DATA

- Product group: LED
- Package: SMD PLCC-2
- · Product series: standard
- Angle of half intensity: ± 60°

FEATURES

- SMD LEDs with exceptional brightness
- · Luminous intensity categorized
- EIA and ICE standard package
- Available in 8 mm tape
- Low power consumption
- Low profile package
- · Compatible with IR reflow, vapor phase and wave solder processes according to CECC 00802 and J-STD-020



e:-

- Compatible with automatic placement equipment
- Non-diffused lens: excellent for coupling to light pipes and backlighting
- Luminous intensity ratio in one packaging unit $I_{Vmax.}/I_{Vmin.} \le 1.6$
- Preconditioning according to JEDEC[®] level 2a
- · ESD-withstand voltage: up to 2 kV according to JESD22-A114-B
- AEC-Q101 gualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- · Automotive: backlighting in dashboards and switches
- Telecommunication: indicator and backlighting in telephone and fax
- · Indicator and backlight for audio and video equipment
- Indicator and backlight in office equipment
- · Flat backlight for LCDs, switches, and symbols
- General use

PARTS TABLE														
PART	COLOR	LUMINOUS INTENSITY (mcd)		at I _F (mA)	WAVELENGTH (nm)		at I _F (mA)	FORWARD VOLTAGE (V)		at I _F (mA)	TECHNOLOGY			
		MIN.	TYP.	MAX.	(IIIA)	MIN.	TYP.	MAX.	(IIIA)	MIN.	TYP.	MAX.	(111,4)	
VLMG31K1L2-GS08	Green	7.1	12	18	10	562	572	575	10	-	2.2	2.8	20	GaP on GaP
VLMG31K1L2-GS18	Green	7.1	12	18	10	562	572	575	10	-	2.2	2.8	20	GaP on GaP
VLMG31K1M2-GS08	Green	7.1	12	28	10	562	572	575	10	-	2.2	2.8	20	GaP on GaP
VLMG31K1M2-GS18	Green	7.1	12	28	10	562	572	575	10	-	2.2	2.8	20	GaP on GaP
VLMG31L1M2-GS08	Green	11.2	13.7	28	10	562	572	575	10	-	2.2	2.8	20	GaP on GaP
VLMG31L1M2-GS18	Green	11.2	13.7	28	10	562	572	575	10	-	2.2	2.8	20	GaP on GaP

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VLMG31..



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified) VLMG31					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage ⁽¹⁾		V _R	6	V	
DC forward current	T _{amb} ≤ 60 °C	I _F	30	mA	
Surge forward current	t _p ≤ 10 μs	I _{FSM}	0.5	A	
Power dissipation		Pv	100	mW	
Junction temperature		Тj	100	°C	
Operating temperature range		T _{amb}	-40 to +100	°C	
Storage temperature range		T _{stg}	-40 to +100	°C	
Thermal resistance junction / ambient	Mounted on PC board (pad size > 16 mm ²)	R _{thJA}	400	K/W	

Note

⁽¹⁾ Driving LED in reverse direction is suitable for short term application.

OPTICAL AND ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified) VLMG31... GREEN

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
		VLMG31K1L2	Ι _V	7.1	12	18	mcd
Luminous intensity ⁽¹⁾	I _F = 10 mA	VLMG31K1M2	Ι _V	7.1	12	28	mcd
		VLMG31L1M2	Ι _V	11.2	13.7	28	mcd
Dominant wavelength	I _F = 10 mA		λ_d	562	572	575	nm
Peak wavelength	I _F = 10 mA		λρ	-	565	-	nm
Angle of half intensity	I _F = 10 mA		φ	-	± 60	-	deg
Forward voltage	I _F = 20 mA		V _F	-	2.2	2.8	V
Reverse voltage	I _R = 10 μA		V _R	6	15	-	V
Junction capacitance	V _R = 0 V, f = 1 MHz		Cj	-	15	-	pF

Note

⁽¹⁾ In one packing unit $I_{Vmax}/I_{Vmin} \le 1.6$

LUMINOUS INTENSITY CLASSIFICATION						
GROUP	LIGHT INTENSITY (mcd)					
STANDARD	OPTIONAL	MIN.	MAX.			
К	1	7.1	9			
r.	2	9	11.2			
L	1	11.2	14.0			
	2	14.0	18.0			
М	1	18.0	22.4			
	2	22.4	28.0			

Note

Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of ± 11 %.

The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel). In order to ensure availability, single brightness groups will not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.

In order to ensure availability, single wavelength groups will not be orderable.

COLOR CLASSIFICATION					
	GREEN DOM. WAVELENGTH (nm)				
GROUP					
	MIN.	MAX.			
3	562	565			
4	564	567			
5	566	569			
6	568	571			
7	570	573			
8	572	575			

Note

Wavelengths are tested at a current pulse duration of 25 ms.

CROSSING TABLE					
VISHAY	OSRAM				
VLMG31K1L2	LGT670-K1L2				
VLMG31K1M2	LGT670-K1M2				
VLMG31L1M2	LGT670-L1M2				

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TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

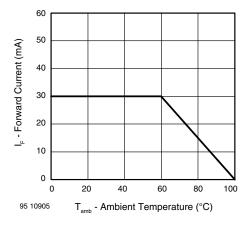


Fig. 1 - Forward Current vs. Ambient Temperature

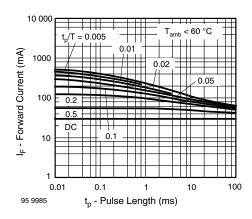


Fig. 2 - Pulse Forward Current vs. Pulse Duration

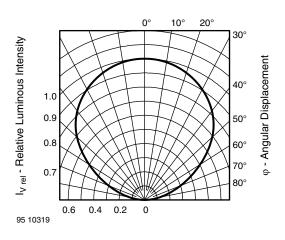


Fig. 3 - Relative Luminous Intensity vs. Angular Displacement

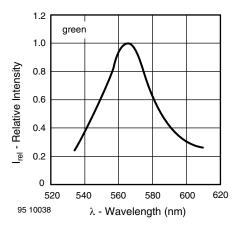


Fig. 4 - Relative Intensity vs. Wavelength

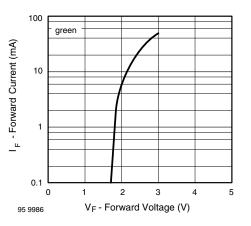
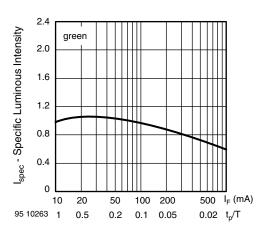


Fig. 5 - Forward Current vs. Forward Voltage





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3 For technical questions, contact: LED@vishay.com Document Number: 81323

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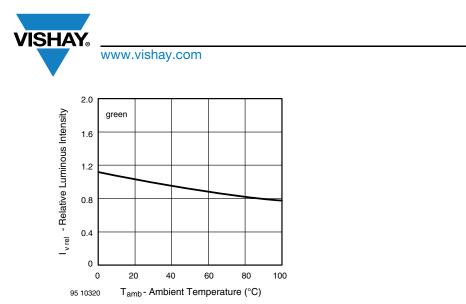
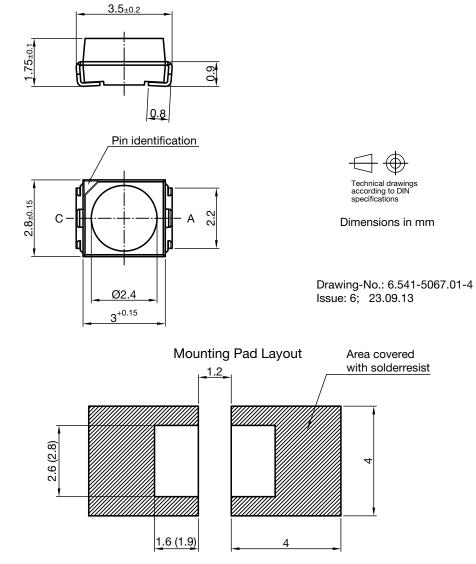


Fig. 7 - Relative Luminous Intensity vs. Ambient Temperature

PACKAGE DIMENSIONS in millimeters



Dimensions: Reflow and vapor phase (wave soldering)

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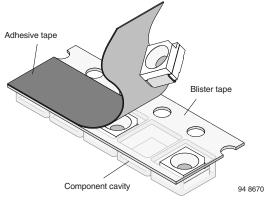




METHOD OF TAPING / POLARITY AND TAPE AND REEL

SMD LED (VLM3-SERIES)

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertion. The blister tape is a plastic strip with impressed component cavities, covered by a top tape.



TAPING OF VLM.3..

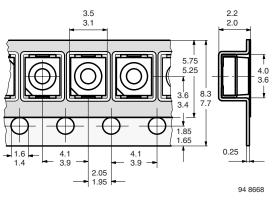


Fig. 8 - Tape Dimensions in mm for PLCC-2

REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS08 (= 1500 PCS.)

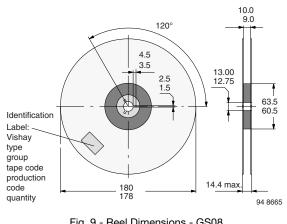


Fig. 9 - Reel Dimensions - GS08

REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS18 (= 8000 PCS.) PREFERRED

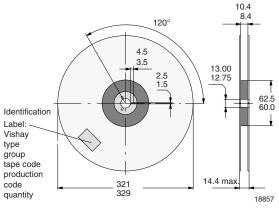


Fig. 10 - Reel Dimensions - GS18

SOLDERING PROFILE

IR Reflow Soldering Profile for Lead (Pb)-free Soldering Preconditioning acc. to JEDEC level 2a

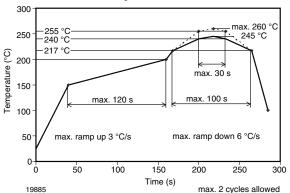


Fig. 11 - Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020)

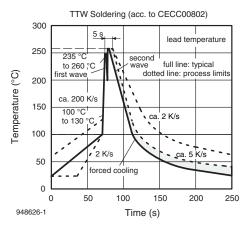


Fig. 12 - Double Wave Soldering of Opto Devices (all Packages)

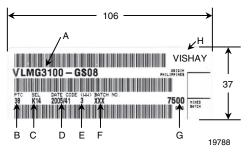
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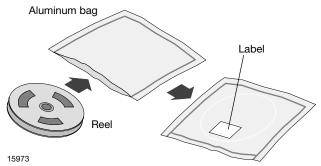
BAR CODE PRODUCT LABEL (example)



- A. Type of component
- B. Manufacturing plant
- C. SEL selection code (bin):
- e.g.: K1= code for luminous intensity group 4 = code for color group
- D. Date code year / week
- E. Day code (e.g. 2: Tuesday)
- F. Batch no.
- G. Total quantity
- H. Company code

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

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RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity \leq 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 $^{\circ}\text{C}$ + 5 $^{\circ}\text{C}$ / - 0 $^{\circ}\text{C}$ and < 5 % RH (dry air / nitrogen) or

96 h at 60 $^{\circ}\text{C}$ + 5 $^{\circ}\text{C}$ and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.

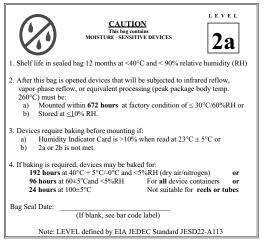


Fig. 13 - Example of JESD22-A112 level 2a label

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



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