


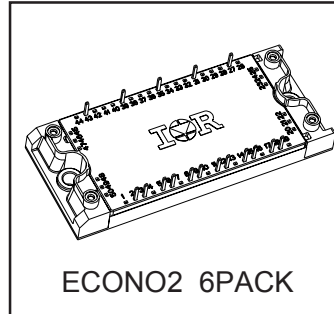
GB35XF120K

Features

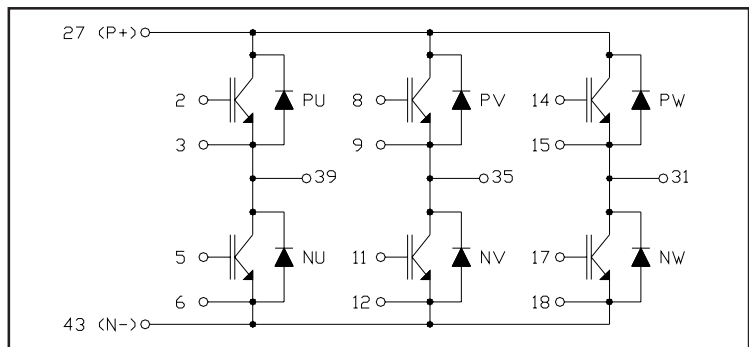
- Low VCE (on) Non Punch Through IGBT Technology
- Low Diode VF
- 10µs Short Circuit Capability
- Square RBSOA
- HEXFRED Antiparallel Diode with Ultrasoft Reverse Recovery Characteristics
- Positive VCE (on) Temperature Coefficient
- Ceramic DBC Substrate
- Low Stray Inductance Design
- TOTALLY LEAD-FREE

Benefits

- Benchmark Efficiency for Motor Control
- Rugged Transient Performance
- Low EMI, Requires Less Snubbing
- Direct Mounting to Heatsink
- PCB Solderable Terminals
- Low Junction to Case Thermal Resistance
- UL Approved E78996 



$V_{CES} = 1200V$
 $I_C = 35A @ T_C = 80^\circ C$
 $t_{sc} > 10\mu s @ T_J = 150^\circ C$
 $V_{CE(on)} \text{ typ.} = 2.40V$



Absolute Maximum Ratings

	Parameter	Max.	Units
V_{CES}	Collector-to-Emitter Voltage	1200	V
$I_C @ T_C = 25^\circ C$	Continuous Collector Current	50	A
$I_C @ T_C = 80^\circ C$	Continuous Collector Current	35	
I_{CM}	Pulsed Collector Current (Ref. Fig. C.T.5)	100	
I_{LM}	Clamped Inductive Load Current	100	
$I_F @ T_C = 25^\circ C$	Diode Continuous Forward Current	50	
$I_F @ T_C = 80^\circ C$	Diode Continuous Forward Current	35	V
I_{FM}	Pulsed Diode Maximum Forward Current	100	
V_{GE}	Gate-to-Emitter Voltage	± 20	
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation (IGBT and Diode)	284	W
$P_D @ T_C = 80^\circ C$	Maximum Power Dissipation (IGBT and Diode)	159	
T_J	Maximum Operating Junction Temperature	150	$^\circ C$
T_{STG}	Storage Temperature Range	-40 to +125	
V_{ISOL}	Isolation Voltage	AC 2500 (MIN)	

Thermal and Mechanical Characteristics

	Parameter	Min	Typical	Maximum	Units
$R_{\theta JC}$ (IGBT)	Junction-to-Case IGBT	-	-	0.44	$^\circ C/W$
$R_{\theta JC}$ (Diode)	Junction-to-Case Diode	-	-	0.80	
$R_{\theta CS}$ (Module)	Case-to-Sink, flat, greased surface	-	0.05	-	
	Mounting Torque (M5)	2.7	-	3.3	N*m
	Weight	-	170	-	g

Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
BV _(CES)	Collector-to-Emitter Breakdown Voltage	1200	-	-	V	V _{GE} = 0 IC = 500μA
ΔV _{(BR)CES} /ΔT _J	Temp. Coefficient of Breakdown Voltage	-	0.7	-	V/°C	V _{GE} = 0 IC = 1mA (25°C - 125°C)
V _{CE(ON)}	Collector-to-Emitter Voltage	-	2.40	2.60	V	I _C = 35A V _{GE} = 15V
		-	2.75	3.00		I _C = 50A V _{GE} = 15V
		-	2.80	-		I _C = 35A V _{GE} = 15V T _J = 125°C
		-	3.30	-		I _C = 50A V _{GE} = 15V T _J = 125°C
V _{GE(th)}	Gate Threshold Voltage	4.0	5.25	6.0		V _{CE} = V _{GE} IC = 250μA
ΔV _{GE(th)} /ΔT _J	Threshold Voltage temp. coefficient	-	-11	-	mV/°C	V _{CE} = V _{GE} IC = 1mA (25°C-125°C)
I _{CES}	Zero Gate Voltage Collector Current	-	-	100	μA	V _{GE} = 0 V _{CE} = 1200V
		-	500	-		V _{GE} = 0 V _{CE} = 1200V T _J = 125°C
V _{FM}	Diode Forward Voltage Drop	-	1.90	2.35	V	I _F = 35A
		-	2.15	2.65		I _F = 50A
		-	2.00	-		I _F = 35A T _J = 125°C
		-	2.35	-		I _F = 50A T _J = 125°C
I _{GES}	Gate-to-Emitter Leakage Current	-	-	±200	nA	V _{GE} = ±20V

Switching Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
Q _G	Total Gate Charge (turn-on)	-	255	385	nC	I _C = 35A
Q _{GE}	Gate-to-Emitter Charge (turn-on)	-	25	40		V _{CC} = 600A
Q _{GC}	Gate-to-Collector Charge (turn-on)	-	125	90		V _{GE} = 15V
E _{ON}	Turn-On Switching Loss	-	2700	4075	μJ	I _C = 35A V _{CC} = 600V
E _{OFF}	Turn-Off Switching Loss	-	2500	3775		V _{GE} = 15V R _G = 10Ω L = 400μH
E _{TOT}	Total Switching Loss	-	5200	7850		T _J = 25°C ①
E _{ON}	Turn-On Switching Loss	-	3750	5450	μJ	I _C = 35A V _{CC} = 600V
E _{OFF}	Turn-Off Switching Loss	-	3675	5100		V _{GE} = 15V R _G = 10Ω L = 400μH
E _{TOT}	Total Switching Loss	-	7425	10550		T _J = 125°C ①
t _{d(on)}	Turn-On delay time	-	50	65	ns	I _C = 35A V _{CC} = 600V
t _r	Rise time	-	35	50		V _{GE} = 15V R _G = 10Ω L = 400μH
t _{d(off)}	Turn-Off delay time	-	415	560		T _J = 125°C
t _f	Fall time	-	230	300		
C _{ies}	Input Capacitance	-	3475	-	pF	V _{GE} = 0
C _{oes}	Output Capacitance	-	615	-		V _{CC} = 30V
C _{res}	Reverse Transfer Capacitance	-	90	-		f = 1Mhz
RBSOA	Reverse Bias Safe Operating Area	FULL SQUARE				T _J = 150°C I _C = 100A R _G = 10Ω V _{GE} = 15V to 0
SCSOA	Short Circuit Safe Operating Area	10	-	-	μs	T _J = 150°C V _{CC} = 900V V _P = 1200V R _G = 10Ω V _{GE} = 15V to 0
I _{rr}	Diode Peak Rev. Recovery Current	-	73	-	A	T _J = 125°C V _{CC} = 600V I _F = 35A L = 400μH V _{GE} = 15V R _G = 10Ω

① Energy losses include "tail" and diode reverse recovery.

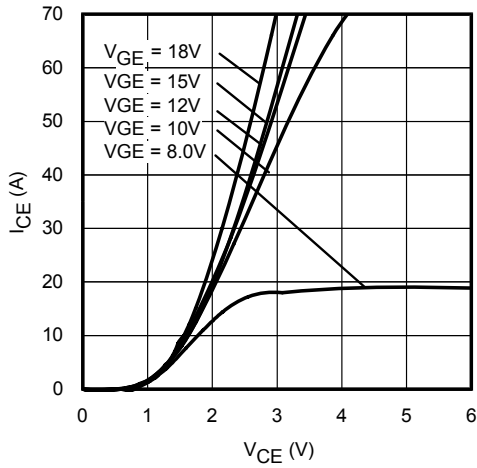


Fig. 1 - Typ. IGBT Output Characteristics
 $T_J = 25^\circ\text{C}; t_p = 80\mu\text{s}$

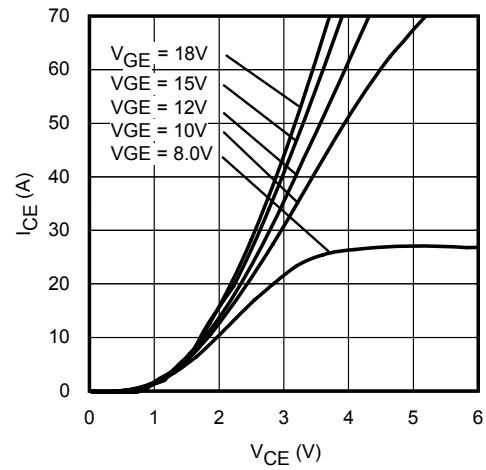


Fig. 2 - Typ. IGBT Output Characteristics
 $T_J = 125^\circ\text{C}; t_p = 80\mu\text{s}$

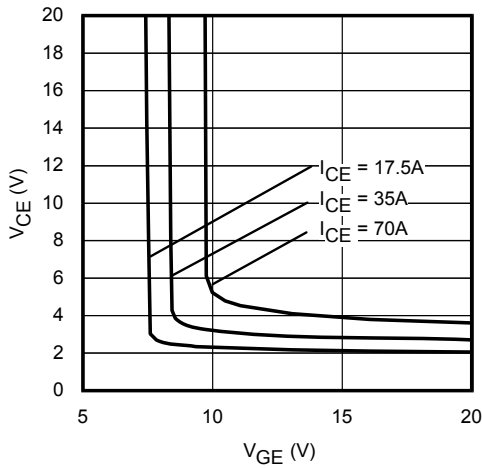


Fig. 3 - Typical V_{CE} vs. V_{GE}
 $T_J = 25^\circ\text{C}$

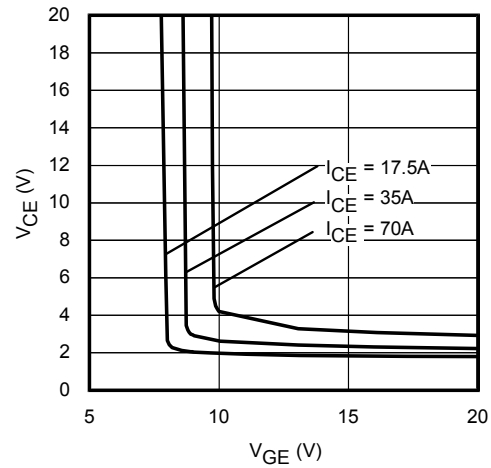


Fig. 4 - Typical V_{CE} vs. V_{GE}
 $T_J = 125^\circ\text{C}$

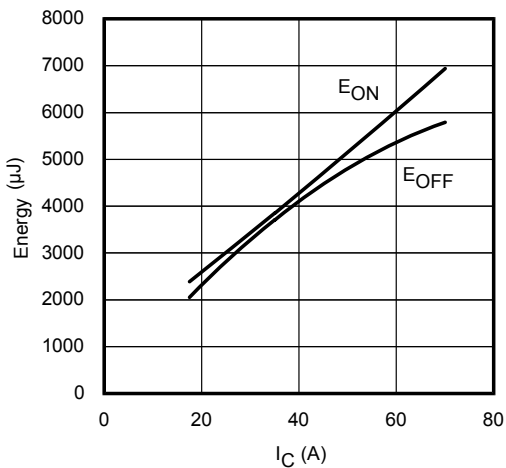


Fig. 5 - Typ. Energy Loss vs. I_C
 $T_J = 125^\circ\text{C}; L = 400\mu\text{H}; V_{CE} = 600\text{V}$
 $R_G = 10\Omega; V_{GE} = 15\text{V}$

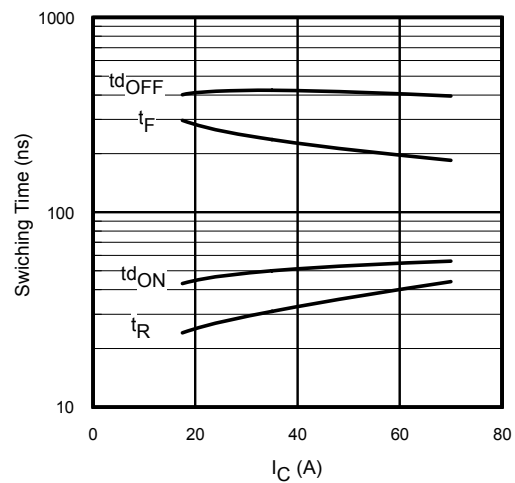


Fig. 6 - Typ. Switching Time vs. I_C
 $T_J = 125^\circ\text{C}; L = 400\mu\text{H}; V_{CE} = 600\text{V}$
 $R_G = 10\Omega; V_{GE} = 15\text{V}$

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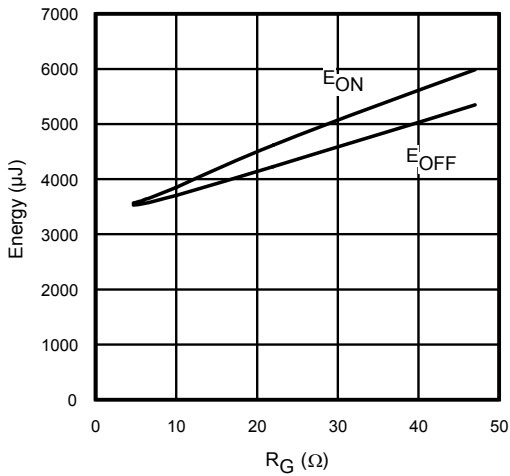


Fig. 7 - Typ. Energy Loss vs. R_G
 $T_J = 125^\circ\text{C}$; $L = 400\mu\text{H}$; $V_{CE} = 600\text{V}$
 $I_{CE} = 35\text{A}$; $V_{GE} = 15\text{V}$

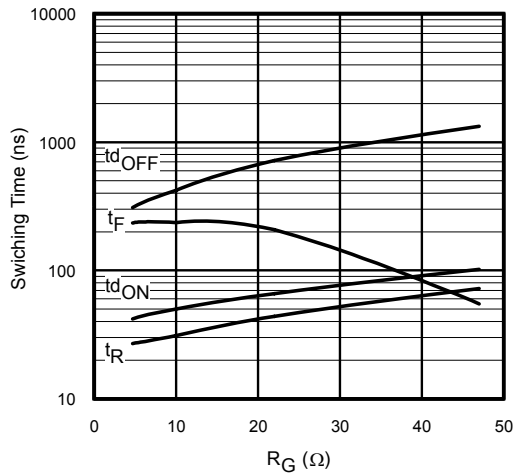


Fig. 8 - Typ. Switching Time vs. R_G
 $T_J = 125^\circ\text{C}$; $L = 400\mu\text{H}$; $V_{CE} = 600\text{V}$
 $I_{CE} = 35\text{A}$; $V_{GE} = 15\text{V}$

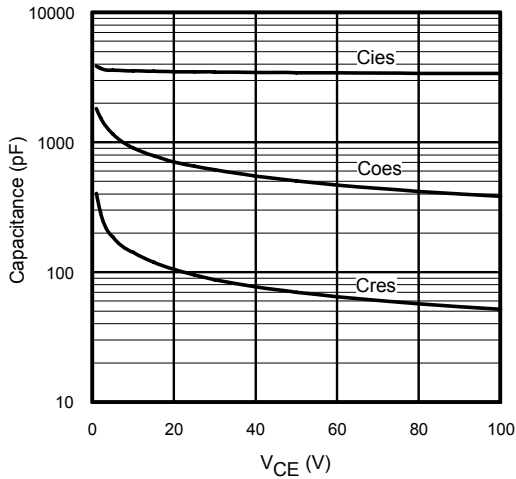


Fig. 9 - Typ. Capacitance vs. V_{CE}
 $V_{GE} = 0\text{V}$; $f = 1\text{MHz}$

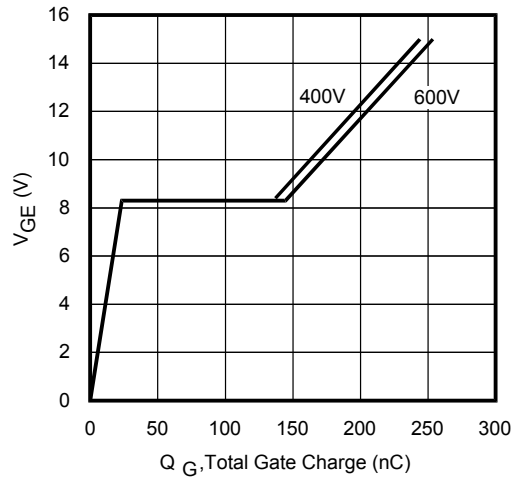


Fig. 10 - Typical Gate Charge vs. V_{GE}
 $I_{CE} = 35\text{A}$; $L = 600\mu\text{H}$

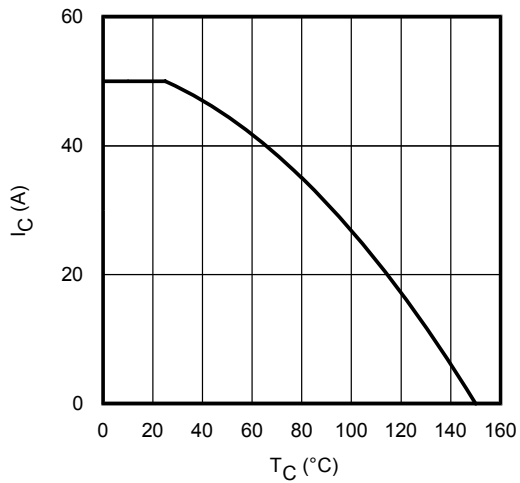


Fig. 11 - Maximum DC Collector Current vs. Case Temperature

Fig. 12 - Power Dissipation vs. Case Temperature

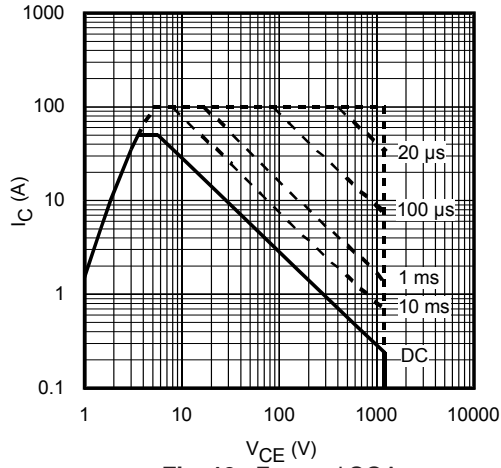


Fig. 13 - Forward SOA
 $T_C = 25^\circ\text{C}; T_J \leq 150^\circ\text{C}$

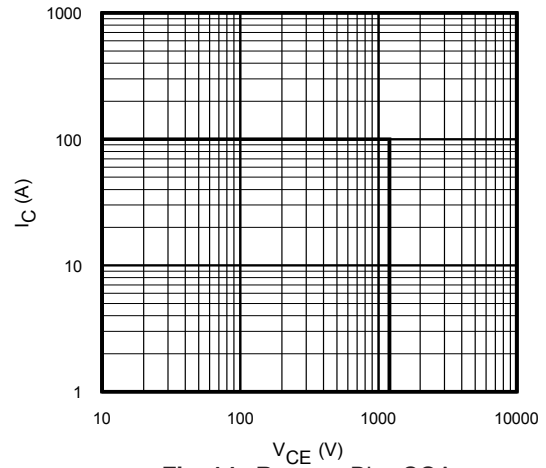


Fig. 14 - Reverse Bias SOA
 $T_J = 150^\circ\text{C}; V_{GE} = 15\text{V}$

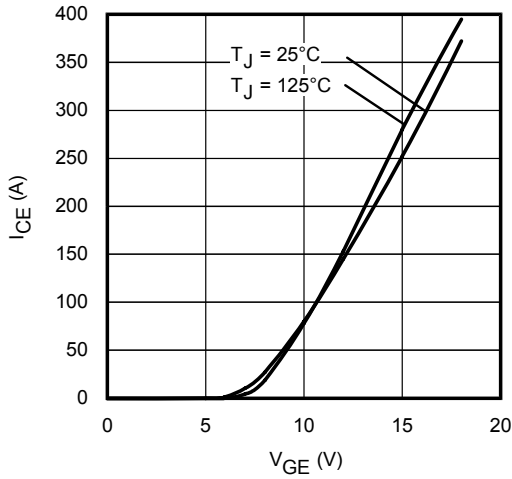


Fig. 15 - Typ. Transfer Characteristics
 $V_{CE} = 50\text{V}; t_p = 10\mu\text{s}$

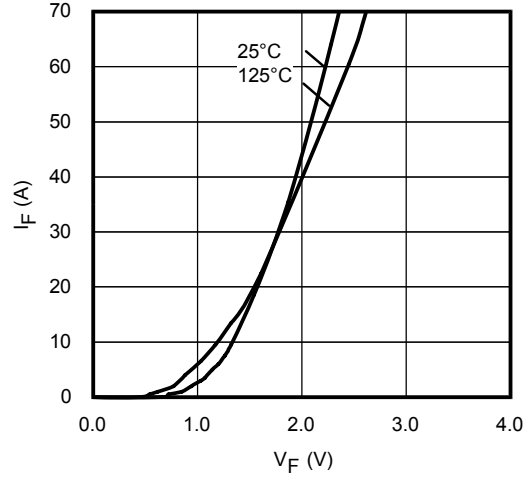


Fig. 16 - Typ. Diode Forward Characteristics
 $t_p = 80\mu\text{s}$

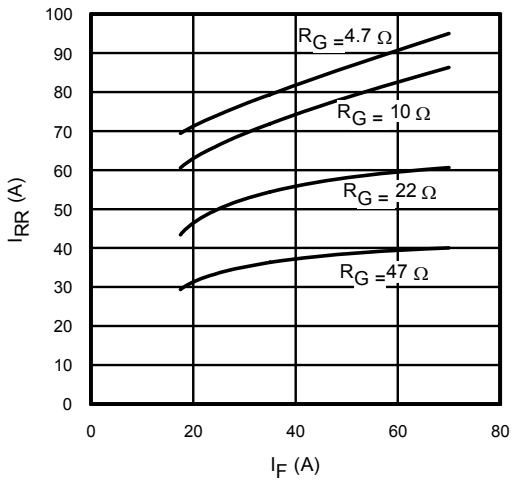


Fig. 17 - Typical Diode I_{RR} vs. I_F
 $T_J = 125^\circ\text{C}$

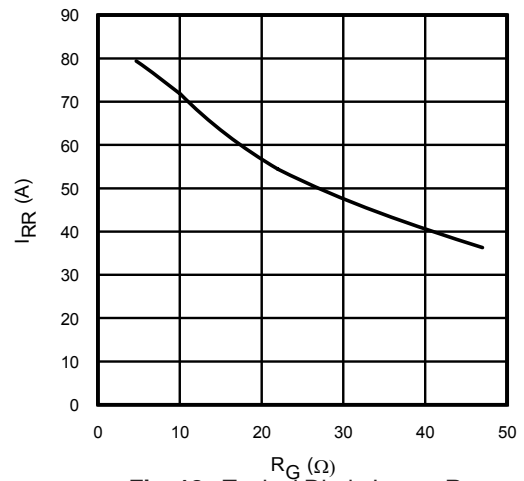


Fig. 18 - Typical Diode I_{RR} vs. R_G
 $T_J = 125^\circ\text{C}, I_F = 35\text{A}$

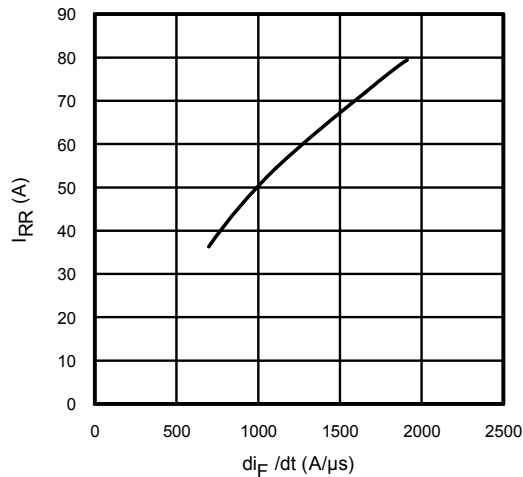


Fig. 19 - Typical Diode I_{RR} vs. di_F/dt ; $V_{CC} = 600V$;
 $V_{GE} = 15V$; $I_{CE} = 35A$; $T_J = 125^\circ C$

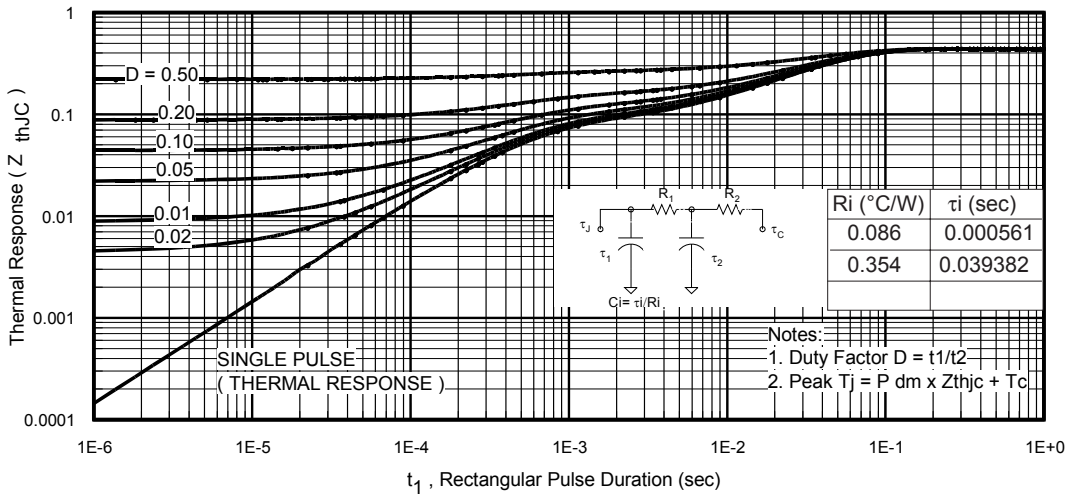


Fig 20. Maximum Transient Thermal Impedance, Junction-to-Case (IGBT)

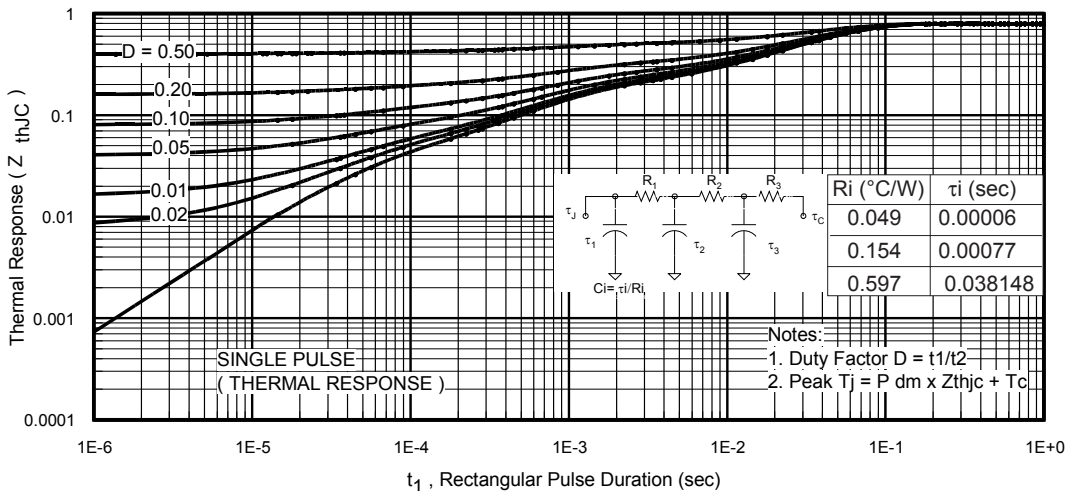


Fig 21. Maximum Transient Thermal Impedance, Junction-to-Case (DIODE)

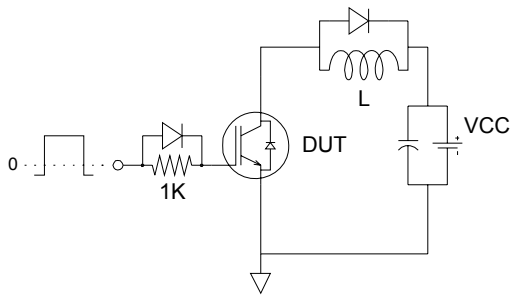


Fig.C.T.1 - Gate Charge Circuit (turn-off)

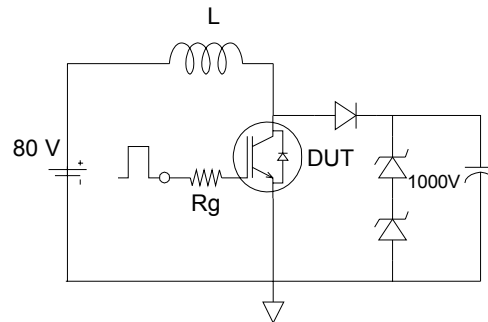


Fig.C.T.2 - RBSOA Circuit

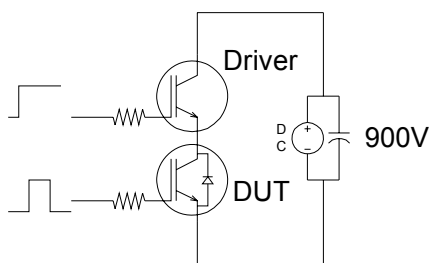


Fig.C.T.3 - S.C. SOA Circuit

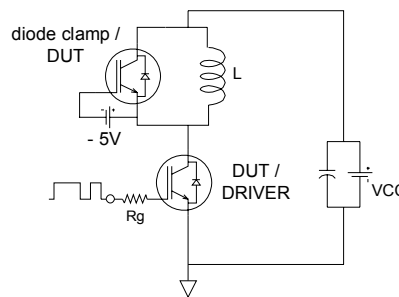


Fig.C.T.4 - Switching Loss Circuit

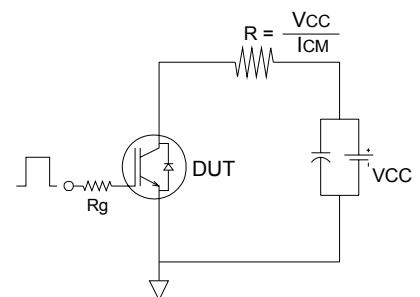


Fig.C.T.5 - Resistive Load Circuit

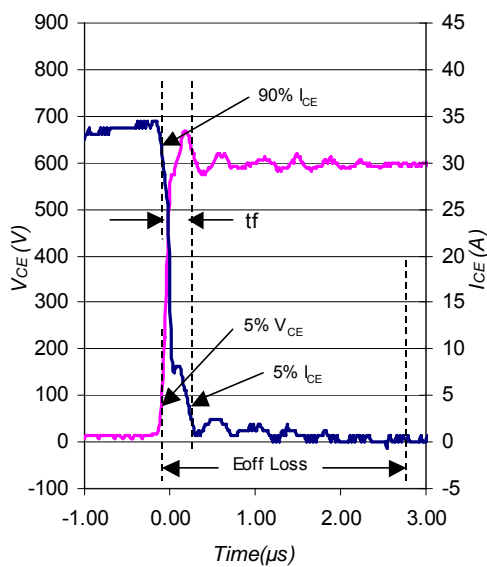


Fig. WF1- Typ. Turn-off Loss Waveform
@ $T_J = 125^\circ\text{C}$ using Fig. CT.4

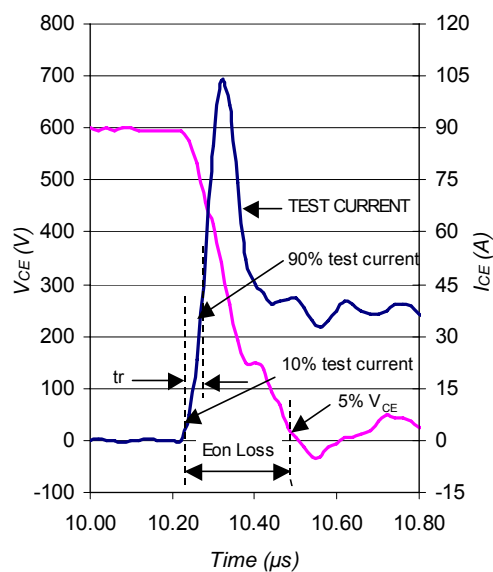


Fig. WF2- Typ. Turn-on Loss Waveform
@ $T_J = 125^\circ\text{C}$ using Fig. CT.4

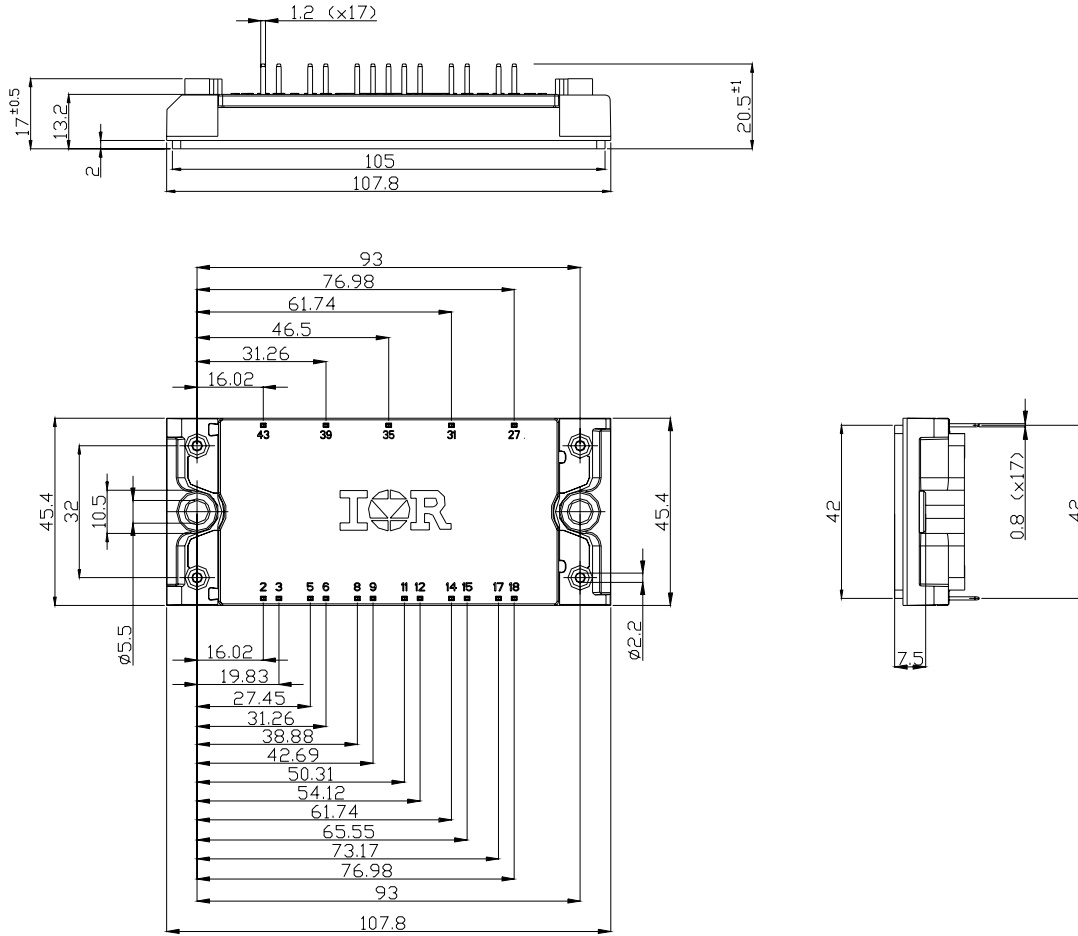
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Econo2 6Pak Package Outline

Dimensions are shown in millimeters (inches)



Econo2 6Pak Part Marking Information



Data and specifications subject to change without notice.
This product has been designed and qualified for Industrial market.
Qualification Standards can be found on IR's Web site.

International
IR Rectifier

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