

Vishay Siliconix

Bi-Directional N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
30	0.019 at V _{GS} = 4.5 V	10.7		
	0.0195 at V _{GS} = 4.0 V	10.5		
	0.022 at V _{GS} = 3.1 V	9.9		
	0.027 at V _{GS} = 2.5 V	9.0		

FEATURES

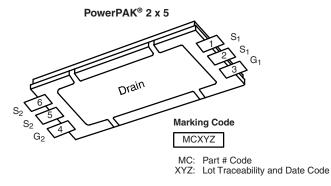
- Halogen-free
- TrenchFET[®] Power MOSFET: 2.5 V Rated

 D_1

ESD Protected: 3000 V

APPLICATIONS

- Battery Protection Circuitry
- 1-Cell Li-Ion Battery Pack
 - LiB/LiP
 - Lithium-Polymer



Ordering Information: SiF912EDZ-T1-GE3 (Lead (Pb)-free and Halogen-free)

9	9
S ₁	S ₂

ABSOLUTE MAXIMUM RATINGS T_{μ}	_A = 25 °C, unle	ss otherwise r	oted			
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	30		V	
Gate-Source Voltage		V _{GS}	± 12			
Continuous Drain Current /T 150 °C)	T _A = 25 °C	- I _D	10.7	7.4		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 85 °C		7.7	5.3	٨	
Pulsed Drain Current (V _{GS} = 8 V, 10 μs)		I _{DM}	80		A	
Continuous Diode Current (Diode Conduction) ^a		۱ _S	2.9	1.3		
	T _A = 25 °C	- P _D	3.5	1.6	W	
Maximum Power Dissipation ^a	T _A = 85 °C		1.8	0.86		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 10 s	- R _{thJA} R _{thJC}	30	36		
Maximum Junction-to-Ambient ^a	Steady State		61	76	°C/W	
Maximum Junction-to-Case (Drain)	Steady State		4.5	5.6		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.



COMPLIANT

 D_2

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Parameter	Symbol	Test Conditions Min. Typ.		Max.	Unit		
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	0.6		1.5	V	
Gate-Body Leakage	1	$V_{DS} = 0 V, V_{GS} = \pm 4.5 V$			± 10		
	I _{GSS} –	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 500	μA	
Zero Gate Voltage Drain Current		$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1		
	IDSS	V_{DS} = 30 V, V_{GS} = 0 V, T_{J} = 85 °C		5			
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 4.5 V$	40			А	
Drain-Source On-State Resistance ^a		$V_{GS} = 4.5 \text{ V}, I_D = 7.4 \text{ A}$		0.0155	0.019	Ω	
	R _{DS(on)} –	$V_{GS} = 4.0 \text{ V}, I_D = 7.3 \text{ A}$		0.016	0.0195		
		$V_{GS} = 3.1 \text{ V}, I_D = 6.8 \text{ A}$		0.018	0.022		
		$V_{GS} = 2.5 \text{ V}, I_D = 3.5 \text{ A}$		0.022	0.027		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 7.4 \text{ A}$		37		S	
Diode Forward Voltage ^a	V _{SD}	$I_{S} = 2.9 \text{ A}, V_{GS} = 0 \text{ V}$		0.75	1.1	V	
Dynamic ^b				•			
Total Gate Charge	Qg			9.8	15		
Gate-Source Charge	Q _{gs}	V_{DS} = 15 V, V_{GS} = 4.5 V, I_D = 7.4 A		2.5		nC	
Gate-Drain Charge	Q _{gd}			2.9			
Turn-On Delay Time	t _{d(on)}			0.53	0.8		
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		0.70	1.1		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ 1 A, V_{GEN} = 10 V, R_g = 6 Ω		8.0	12	μs	
Fall Time	t _f			3.4	5		

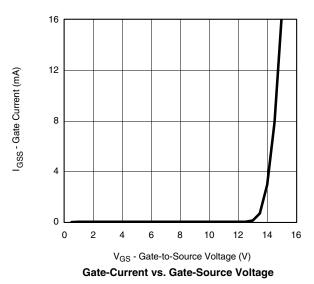
Notes:

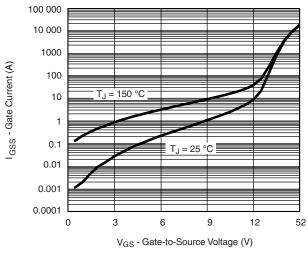
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



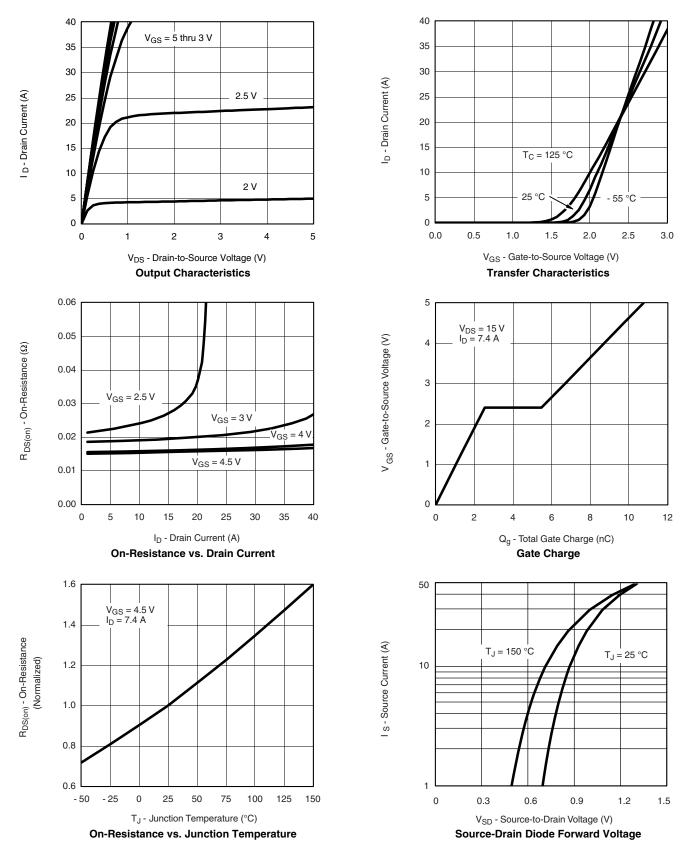


Gate Current vs. Gate-Source Voltage



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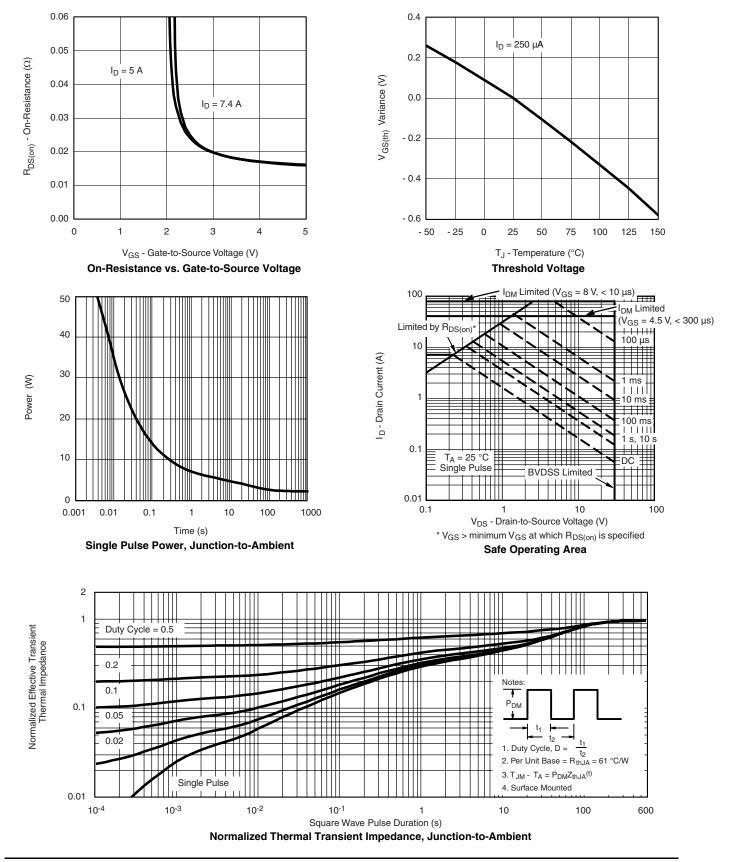
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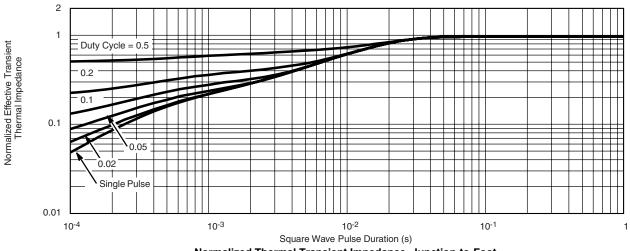




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Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?72952.



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