# V35PWM10

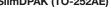
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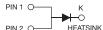
## **High Current Density Surface-Mount** TMBS<sup>®</sup> (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.41$  V at  $I_F = 5$  A



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**DESIGN SUPPORT TOOLS** 



PRIMARY CHARACTERISTICS			
I <sub>F(AV)</sub>	35 A		
V <sub>RRM</sub>	100 V		
I <sub>FSM</sub>	260 A		
$V_F$ at $I_F$ = 35 A ( $T_A$ = 125 °C)	0.68 V		
T <sub>J</sub> max.	175 °C		
Package	SlimDPAK (TO-252AE)		
Circuit configuration	Single		

#### **FEATURES**

- · Very low profile typical height of 1.3 mm
- Trench MOS Schottky technology
- · Ideal for automated placement
- · Low forward voltage drop, low power losses
- · High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### TYPICAL APPLICATIONS

For use in low voltage high frequency DC/DC converters, freewheeling diodes, and polarity protection applications.

### **MECHANICAL DATA**

Case: SlimDPAK (TO-252AE)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V35PWM10	UNIT	
Device marking code		V35PWM10		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	100	V	
Maximum average forward rectified current (Fig. 1)	I <sub>F(AV)</sub> <sup>(1)</sup>	35	А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	260	A	
Operating junction temperature range	T <sub>J</sub> <sup>(2)</sup>	-40 to +175	°C	
Storage temperature range	T <sub>STG</sub>	-55 to +175	°C	

Notes

<sup>(1)</sup> With infinite heatsink

<sup>(2)</sup> The heat generated must be less than the thermal conductivity from junction to ambient:  $dP_D/dT_J < 1/R_{0JA}$ 

BoHS COMPLIANT

HALOGEN

FREE

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V35PWM10



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25$ °C unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> (1)	0.50	-	V
	I <sub>F</sub> = 17.5 A			0.69	-	
	I <sub>F</sub> = 35 A			0.82	0.90	
	$I_{F} = 5.0 \text{ A}$	T <sub>A</sub> = 125 °C		0.41	-	
	I <sub>F</sub> = 17.5 A			0.61	-	
	I <sub>F</sub> = 35 A			0.68	0.76	
Reverse current	V <sub>B</sub> = 70 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> (2)	0.01	-	mA
	v <sub>R</sub> = 70 v	T <sub>A</sub> = 125 °C		4	-	
	V <sub>B</sub> = 100 V	T <sub>A</sub> = 25 °C		-	0.8	
	v <sub>R</sub> = 100 v	T <sub>A</sub> = 125 °C		8	25	
Typical junction capacitance	4.0 V, 1 MHz		CJ	2500	-	pF

Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V35PWM10	UNIT	
Typical thermal resistance	R <sub>0JA</sub> (1)(2)	55	°C/W	
	R <sub>0JM</sub> <sup>(3)</sup>	1.5	C/W	

#### Notes

 $^{(1)}$  The heat generated must be less than thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

 $^{(2)}$  Free air, mounted on recommended copper pad area; thermal resistance  $R_{\theta JA}$  - junction to ambient

<sup>(3)</sup> Mounted on infinite heat sink; thermal resistance  $R_{\theta JM}$  - junction-to-mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V35PWM10-M3/I	0.20	I	4500	13" diameter plastic tape and reel	
V35PWM10HM3/I <sup>(1)</sup>	0.20	l	4500	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified



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### **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

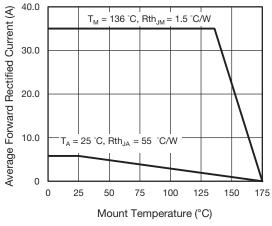
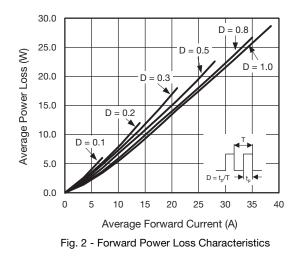
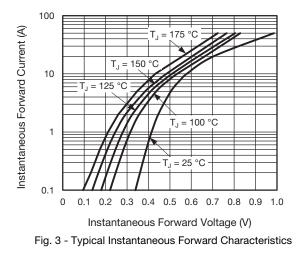


Fig. 1 - Maximum Forward Current Derating Curve





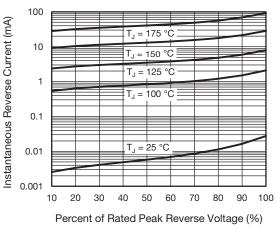
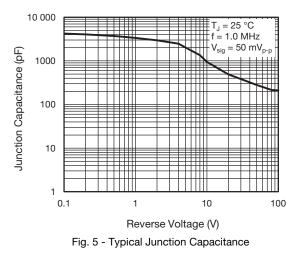
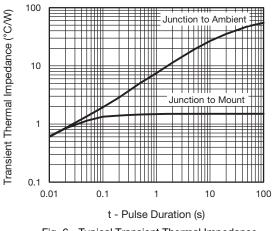
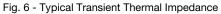


Fig. 4 - Typical Reverse Leakage Characteristics







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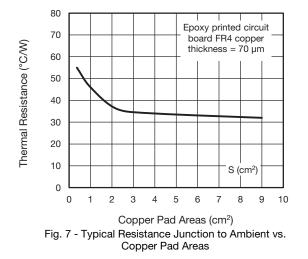
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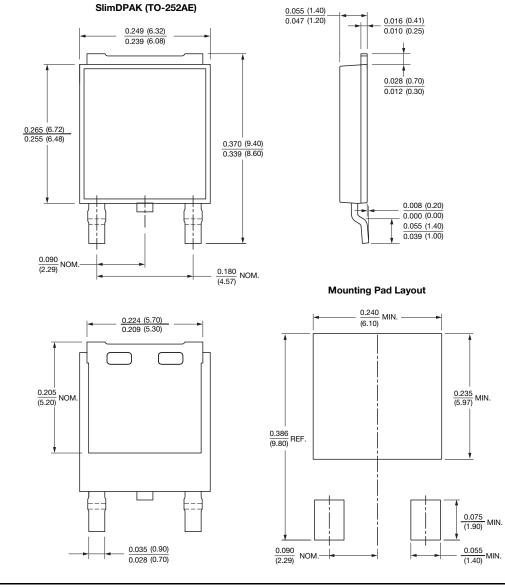
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