



Dual N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
30 -	0.0095 at V _{GS} = 10 V	12.2		
	0.016 at V _{GS} = 4.5 V	9.4		

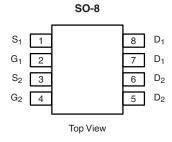
FEATURES

- Halogen-free Option Available
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested



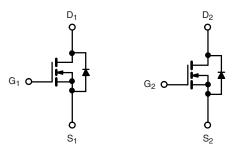
APPLICATIONS

- DC/DC Conversion
- Load Switching



Ordering Information: Si4944DY-T1-E3 (Lead (Pb)-free)

Si4944DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T	A = 25 °C, unle	ss otherwise r	noted			
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	30		V	
Gate-Source Voltage		V _{GS}	± 20			
Continuous Dusin Comment /T 150 90\8	T _A = 25 °C	- I _D	12.2	9.3		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 85 °C		8.8	6.7	Δ.	
Pulsed Drain Current		I _{DM}	30		Α	
Continuous Source Current (Diode Conduction) ^a		I _S	1.9	1.1	İ	
M	T _A = 25 °C	В	2.3	1.3	W	
Maximum Power Dissipation ^a	T _A = 85 °C	- P _D	1.2	0.7	VV 	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Mariana landia la Andria d	t ≤ 10 s	R _{thJA}	42	55	°C/W
Maximum Junction-to-Ambient ^a	Steady State		75	95	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	19	25	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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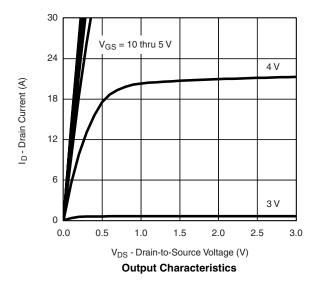
MOSFET SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1		3	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zava Cata Valtaga Dvain Curvent	1	V _{DS} = 30 V, V _{GS} = 0 V			1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 85 ^{\circ}\text{C}$			5	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α	
	В	V _{GS} = 10 V, I _D = 12.2 A		0.0075	0.0095	0	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 9.4 \text{ A}$	0.013 0.01		0.016	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 12.2 A		32		S	
Diode Forward Voltage ^a	V_{SD}	I _S = 1.9 A, V _{GS} = 0 V		0.8	1.2	V	
Dynamic ^b							
Total Gate Charge	Q_g			13.5	21		
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 12.2 \text{ A}$		7.1		nC	
Gate-Drain Charge	Q_{gd}			4.7			
Gate Resistance	R_g	f = 1 MHz	0.5	1.0	1.7	Ω	
Turn-On Delay Time	t _{d(on)}			10	15		
Rise Time	t _r	$ \begin{array}{c c} \hline t_r & V_{DD} = 15 \text{ V, } R_L = 15 \Omega \\ \hline t_{d(off)} & I_D \cong 1 \text{ A, } V_{GEN} = 10 \text{ V, } R_G = 6 \Omega \\ \end{array} $		10	15	ns	
Turn-Off Delay Time	t _{d(off)}			40	60		
Fall Time	t _f			12	20		
Source-Drain Reverse Recovery Time	t _{rr}	$I_F = 1.9 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$		45	70		

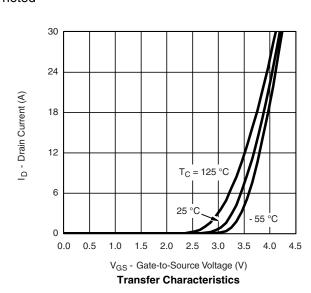
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

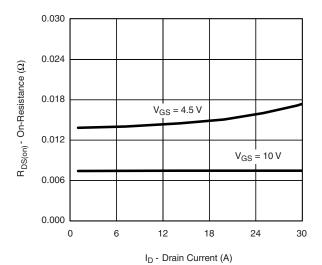
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



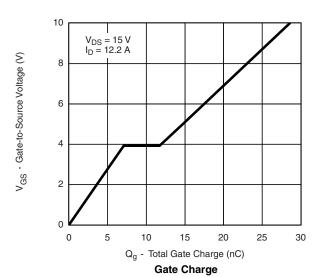


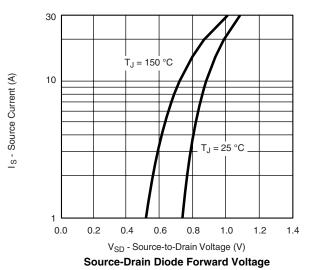


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



On-Resistance vs. Drain Current





2400 C_{iss}

1800

1200

600

C_{rss}

0

6 12

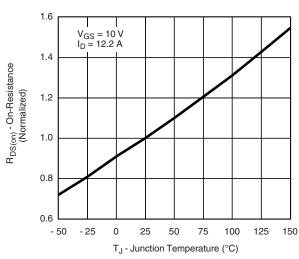
18

24

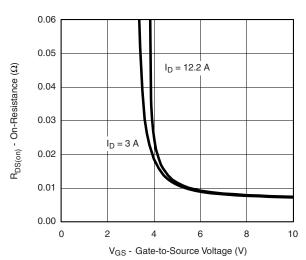
30

V_{DS} - Drain-to-Source Voltage (V)

Capacitance



On-Resistance vs. Junction Temperature

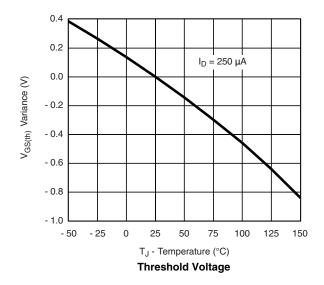


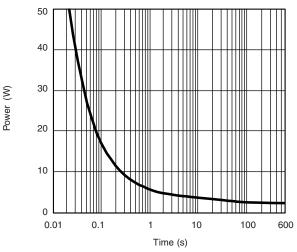
On-Resistance vs. Gate-to-Source Voltage

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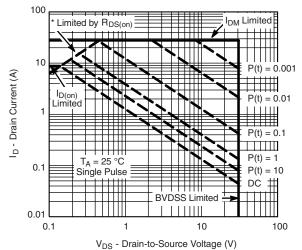
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



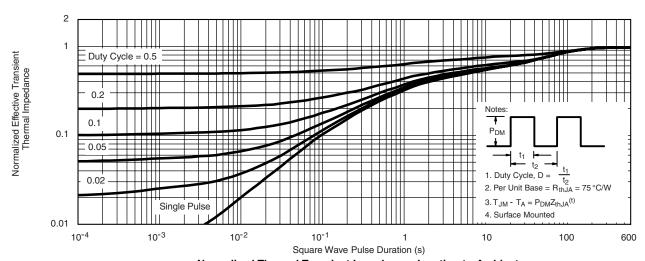


Single Pulse Power



* V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified

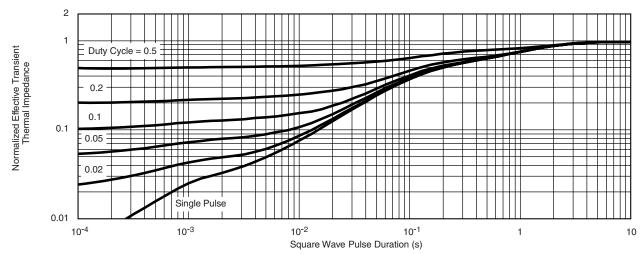
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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