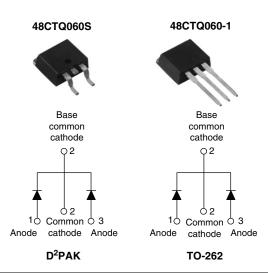
Vishay High Power Products

## Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY	1
I <sub>F(AV)</sub>	2 x 20 A
V <sub>R</sub>	60 V

### FEATURES

- 150 °C T<sub>J</sub> operation
- Center tap configuration
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for Q101 level

### DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	40	A		
V <sub>RRM</sub>		60	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1000	A		
V <sub>F</sub>	20 Apk, T <sub>J</sub> = 125 °C (per leg)	0.58	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	48CTQ060S 48CTQ060-1	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	60	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	80	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS VALUES		UNITS	
Maximum average per leg		50 % duty cycle at T <sub>C</sub> = 111 °C, r	actangular waveform	20	
See fig. 5 per device	IF(AV)			40	_
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	1000	A
non-repetitive surge current per leg See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	260	
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1.50 A, L = 11.5 mH 13		mJ	
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s1.50Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>B</sub> typical1.50		A	

# Vishay High Power Products Schottky Rectifier, 2 x 20 A



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS VA		VALUES	UNITS
		20 A	T - 25 °C	0.61	
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	40 A	– T <sub>J</sub> = 25 °C	0.83	v
See fig. 1	V FM	20 A	– T <sub>J</sub> = 125 °C	0.58	V
		40 A		0.75	
Maximum reverse leakage current per leg	I <sub>BM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>B</sub> = Rated V <sub>B</sub>	2	mA
See fig. 2	'RM ` ´	$T_J = 125 \text{ °C}$	89		
Threshold voltage	V <sub>F(TO)</sub>	$T_{,l} = T_{,l} maximum$		0.37	V
Forward slope resistance	r <sub>t</sub>			8.26	mΩ
Maximum junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C 1220		pF	
Typical series inductance per leg	Ls	Measured lead to lead 5 mm from package body 8.0		nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000		V/µs	

Note

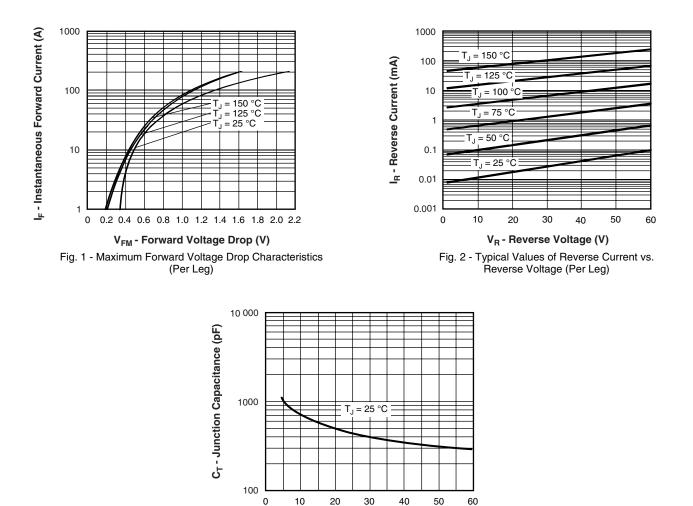
 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	)	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C
Maximum thermal resistance, junction to case per leg		D		2.0	
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	1.0	°C/W
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased (Only for TO-262)	0.50	-
Approvimeto weight				2	g
Approximate weight				0.07	oz.
Manuatian tanan	minimum			6 (5)	kgf · cm
Mounting torque	maximum			12 (10)	(lbf · in)
Martine device			Case style D <sup>2</sup> PAK	48CTQ	060S
Marking device			Case style TO-262	48CTQ0	)60-1

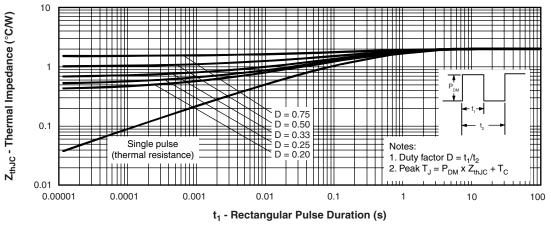


## 48CTQ060S/48CTQ060-1

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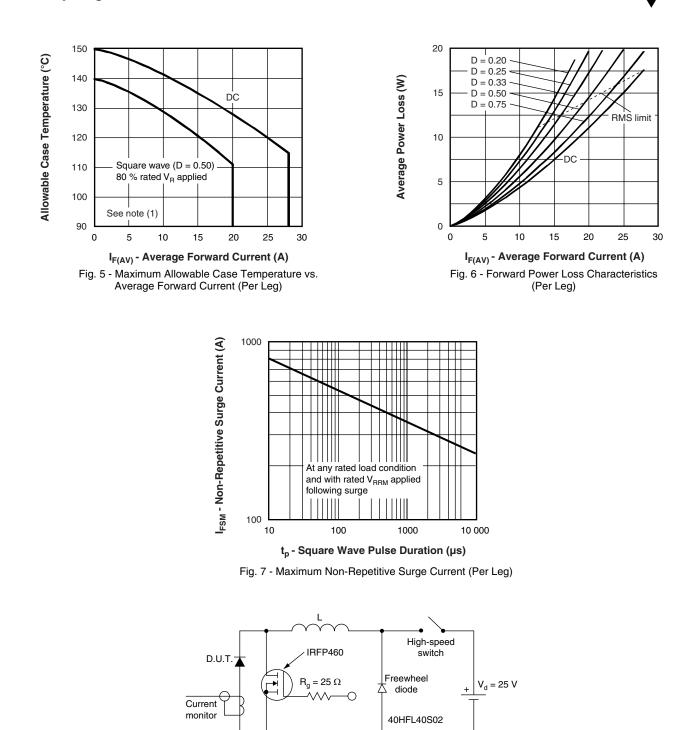
V<sub>R</sub> - Reverse Voltage (V) Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





# 48CTQ060S/48CTQ060-1

## Vishay High Power Products Schottky Rectifier, 2 x 20 A



#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mbox{Pd} = \mbox{Forward power loss} = I_{F(AV)} \times V_{FM} \mbox{ at } (I_{F(AV)}/D) \mbox{ (see fig. 6);} \\ \mbox{Pd}_{REV} = \mbox{Inverse power loss} = V_{R1} \times I_R \mbox{ (1 - D); } I_R \mbox{ at } V_{R1} = 10 \ V \end{array}$ 

Fig. 8 - Unclamped Inductive Test Circuit



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#### ORDERING INFORMATION TABLE

2 - Circ C = 3 - T = <sup>-1</sup> 4 - Schu 5 - Volta 6 - • S = • -1	uit confi Commo	4 ing (40 A) figuration: on cathoo	:	6	7	8	
2 - Circ C = 3 - T = <sup>-1</sup> 4 - Schu 5 - Volta 6 - • S = • -1	uit confi Commo	iguration:	:				
3 - T = 4 - Schu 5 - Volta 6 - • S = • -1		on cathoo	de				
_	-	Q" series ing (060 =	= 60 V)				
• TF		262 Tube (50 p upe and re ape and re	eel (left o eel (righ	nt orien			

LINKS TO RELATED DOCUMENTS		
Dimensions	http://www.vishay.com/doc?95014	
Part marking information	http://www.vishay.com/doc?95008	
Packaging information	http://www.vishay.com/doc?95032	



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