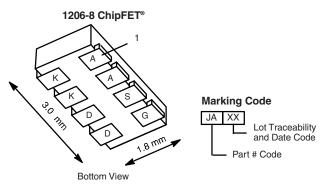


Vishay Siliconix

P-Channel 1.8 V (G-S) MOSFET with Schottky Diode

MOSFET	MOSFET PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)				
	0.110 at V _{GS} = - 4.5 V	- 3.6				
- 20	0.160 at V _{GS} = - 2.5 V	- 3.0				
	0.240 at V _{GS} = - 1.8 V	- 2.4				

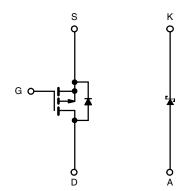
SCHOTTKY PRODUCT SUMMARY V_f (V) V_f (V) Diode Forward Voltage 20 0.48 V at 0.5 A 1.0



FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- LITTLE FOOT[®] Plus
- Compliant to RoHS Directive 2002/95/EC





P-Channel MOSFET

Ordering Information: Si5853DC-T1-E3 (Lead (Pb)-free)

Si5853DC-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted Symbol **Steady State** Unit Parameter 5 s Drain-Source Voltage (MOSFET and Schottky) V_{DS} v - 20 Reverse Voltage (Schottky) V_{KA} 20 V Gate-Source Voltage (MOSFET) V_{GS} ± 8 ± 8 T_A = 25 °C - 2.7 - 3.6 Continuous Drain Current (T_J = 150 °C) (MOSFET)^a I_D T_A = 85 °C - 2.6 - 1.9 Pulsed Drain Current (MOSFET) IDM - 10 Α Continuous Source Current (MOSFET Diode Conduction)^a Is - 1.8 - 0.9 Average Forward Current (Schottky) I_{F} 1.0 Pulsed Forward Current (Schottky) I_{FM} 7 T_A = 25 °C 2.1 1.1 Maximum Power Dissipation (MOSFET)^a T_A = 85 °C 1.1 0.6 P_D w T_A = 25 °C 0.96 1.3 Maximum Power Dissipation (Schottky)^a T_A = 85 °C 0.68 0.59 T_J, T_{stg} - 55 to 150 Operating Junction and Storage Temperature Range °C 260 Soldering Recommendations (Peak Temperature)^{b, c}

Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. See reliability manual for profile. The ChipFET is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.

c. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.

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THERMAL RESISTANCE RATINGS								
Parameter		Device	Symbol	Typical	Maximum	Unit		
	t ≤ 5 s	MOSFET	R _{thJA}	50	60	°C/W		
		Schottky		77	95			
Junction-to-Ambient ^a	Steady State	MOSFET		90	110			
		Schottky		110	130			
lunction to Fact	Oto a du Oto to	MOSFET	- R _{thJF}	30	40			
Junction-to-Foot	Steady State	Schottky		33	40			

Notes:

a. Surface mounted on 1" x 1" FR4 board.

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 0.45		- 1.0	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V$, $V_{GS} = \pm 8 V$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = -20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	<u>.</u> иА	
	IDSS	V_{DS} = - 20 V, V_{GS} = 0 V, T_{J} = 85 °C			- 5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS}{\leq}{-}5$ V, $V_{GS}{=}{-}4.5$ V	- 10			Α	
		V_{GS} = - 4.5 V, I _D = - 2.7 A		0.095	0.110	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V_{GS} = - 2.5 V, I _D = - 2.2 A		0.137	0.160		
		V _{GS} = - 1.8 V, I _D = - 1 A		0.205	0.240		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 2.7 A		7		S	
Diode Forward Voltage ^a	V _{SD}	$I_{S} = -0.9 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.8	- 1.2	V	
Dynamic ^b	<u> </u>						
Total Gate Charge	Qg			5.1	7.7	nC	
Gate-Source Charge	Q _{gs}	Q_{gs} V_{DS} = - 10 V, V_{GS} = - 4.5 V, I_D = - 2.7 A		1.2			
Gate-Drain Charge	Q _{gd}			1.0			
Turn-On Delay Time	t _{d(on)}			16	25		
Rise Time	t _r	V_{DD} = - 10 V, R_L = 10 Ω		30	45		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 1 A, V_GEN = - 4.5 V, R_g = 6 Ω		30	45	ns	
Fall Time	t _f			27	40		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 0.9 A, dl/dt = 100 A/μs		20	40		

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

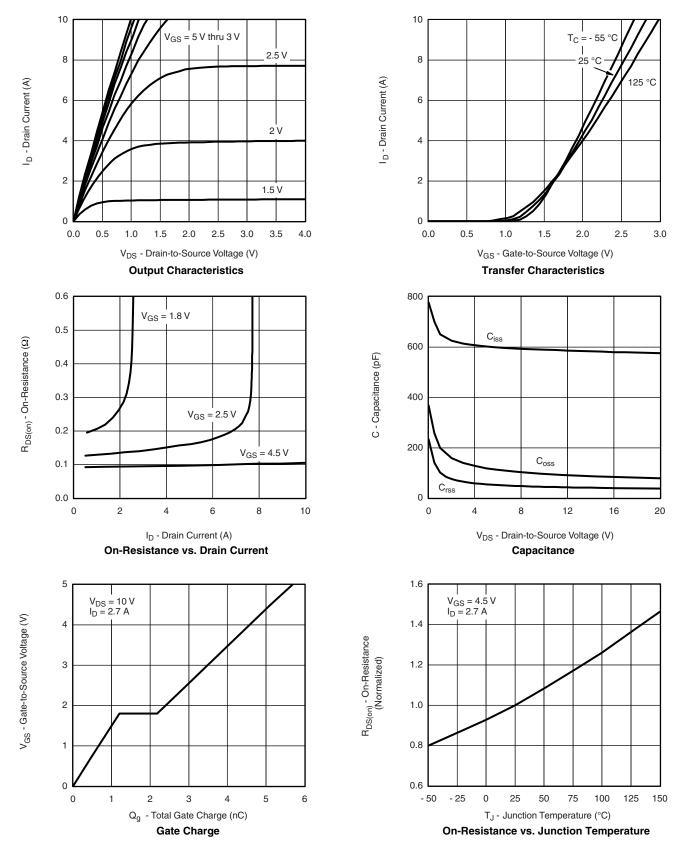
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

SCHOTTKY SPECIFICATIONS $T_J = 25 \text{ °C}$, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Forward Voltage Drop	V _F	I _F = 0.5 A		0.42	0.48	V	
		I _F = 0.5 A, T _J = 125 °C		0.33	0.4	v	
	I _{rm}	V _r = 20 V		0.002	0.100		
Maximum Reverse Leakage Current		V _r = 20 V, T _J = 85 °C		0.10	1	mA	
		V_r = 20 V, T_J = 125 °C		1.5	10		
Junction Capacitance	CT	V _r = 10 V		31		pF	



Si5853DC Vishay Siliconix

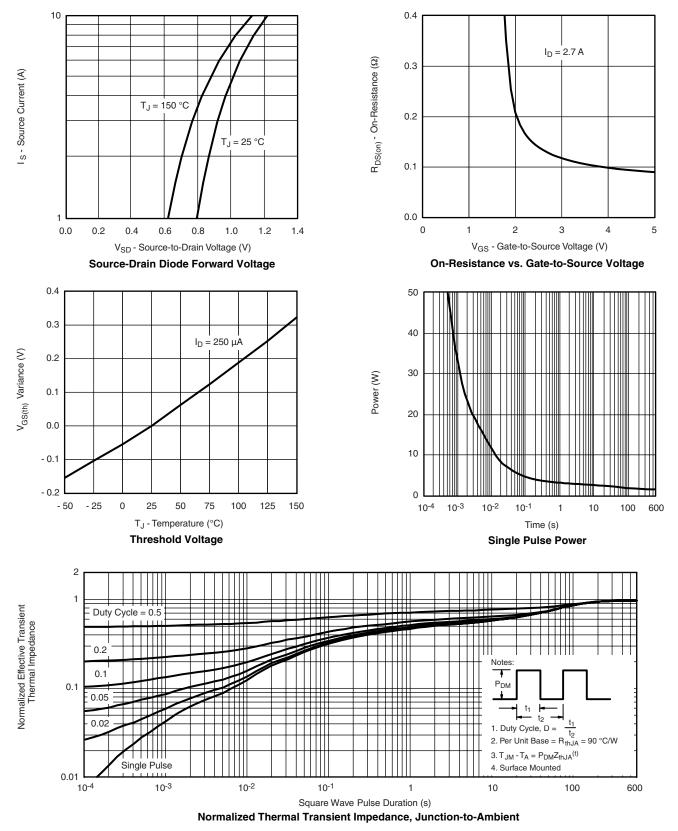
MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Si5853DC

Vishay Siliconix





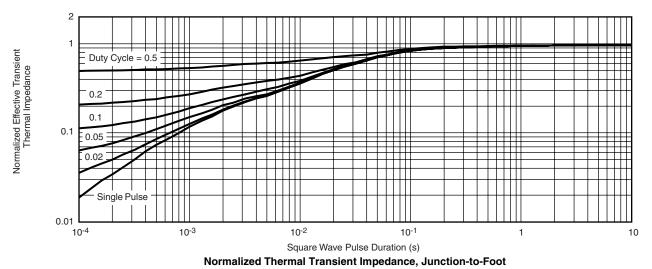
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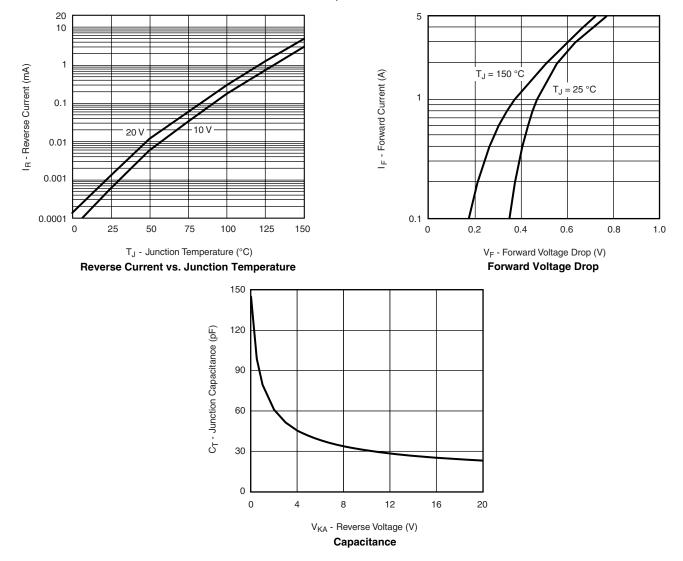
Si5853DC

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MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







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2 1 Duty Cycle = 0.5 ЩШ Normalized Effective Transient Thermal Impedance Notes: 0.2 ТШ ł P_{DM} 0.1 0.1 0.05 t₁ t₂ ₩ t₁ 1. Duty Cycle, D = t2 0.02 2. Per Unit Base = RthJA = 110 °C/W 3. T_{JM} - $T_A = P_{DM}Z_{thJA}^{(t)}$ 4. Surface Mounted Single Pulse 0.01 10-4 10⁻³ 10-2 10-1 10 100 600 1 Square Wave Pulse Duration (s) Normalized Thermal Transient Impedance, Junction-to-Ambient 2 1 Normalized Effective Transient Thermal Impedance Duty Cycle = 0.5 0.2 0.1 0.1 E 0.05 t ॑ Ш 0.02 Single Pulse 0.01 10-4 10⁻³ 10-2 10-1 1 10 Square Wave Pulse Duration (s)

SCHOTTKY TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

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Normalized Thermal Transient Impedance, Junction-to-Foot



Vishay

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