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Automotive P-Channel 30 V (D-S) 175 °C MOSFET

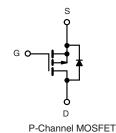


PRODUCT SUMMARY				
V _{DS} (V)	-30			
$R_{DS(on)} (\Omega)$ at $V_{GS} = -10 V$	0.0085			
$R_{DS(on)} (\Omega)$ at $V_{GS} = -4.5 V$	0.0200			
I _D (A)	-22			
Configuration	Single			

FEATURES

- TrenchFET[®] power MOSFET
- AEC-Q101 qualified ^c
- 100 % $R_{\rm q}$ and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>





ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and halogen-free	SQ4483EY (for detailed order number please see <u>www.vishay.com/doc?79771</u>)

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-source voltage	V _{DS}	-30	V		
Gate-source voltage	V _{GS}	± 20	v		
Continuous drain current		-30			
	ID	-30			
Continuous source current (diode conduction)	I _S	-30	A		
Pulsed drain current ^a	I _{DM}	-84			
Single pulse avalanche current	I _{AS}	-32			
Single pulse avalanche energy	E _{AS}	51	mJ		
Maximum power dissipation ^a	П	7	w		
	P _D	2			
Operating junction and storage temperature range	T _J , T _{stg}	-55 to +175	°C		

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-ambient	PCB mount ^b	R _{thJA}	85	°C/W	
Junction-to-case (drain)		R _{thJF}	21	C/W	

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %

b. When mounted on 1" square PCB (FR4 material)

c. Parametric verification ongoing

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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static	-	-			•		
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = -250 \mu\text{A}$		-30	-	-	V
Gate-source threshold voltage	V _{GS(th)}	V _{DS} =	V _{DS} = V _{GS} , I _D = -250 μA		-2.0	-2.5	V
Gate-source leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 V, V_{GS} = \pm 20 V$		-	± 100	nA
		$V_{GS} = 0 V$	V _{DS} = -30 V	-	-	-1	
Zero gate voltage drain current	I _{DSS}	$V_{GS} = 0 V$	V_{DS} = -30 V, T _J = 125 °C	-	-	-50	μA
		$V_{GS} = 0 V$	V_{DS} = -30 V, T _J = 175 °C	-	-	-150	
On-state drain current ^a	I _{D(on)}	V _{GS} = -10 V	$V_{DS} \le -5 V$	-30	-	-	Α
		$V_{GS} = -10 V$	I _D = -10 A	I	0.0070	0.0085	
Drain-source on-state resistance ^a	Base	$V_{GS} = -10 V$	I_D = -10 A, T_J = 125 °C	-	-	0.0130	Ω
	R _{DS(on)}	$V_{GS} = -10 V$	I _D = -10 A, T _J = 175 °C	-	-	0.0150	52
		V_{GS} = -4.5 V	I _D = -7 A	I	0.0160	0.0200	
Forward transconductance ^b	9 _{fs}	V _{DS} =	V _{DS} = -10 V, I _D = -10 A		32	-	S
Dynamic ^b							
Input capacitance	C _{iss}		V _{GS} = 0 V V _{DS} = -15 V, f = 1 MHz	-	3400	4500	
Output capacitance	C _{oss}			-	712	890	pF
Reverse transfer capacitance	C _{rss}			-	580	770	1
Total gate charge ^c	Qg			-	75	113	
Gate-source charge ^c	Q _{gs}	V _{GS} = -10 V	$V = V_{DS} = -15 V, I_D = -10 A$	-	9.5	-	nC
Gate-drain charge ^c	Q _{gd}			-	19	-	1
Gate resistance	Rg		f = 1 MHz		2	3	Ω
Turn-on delay time ^c	t _{d(on)}			-	20	25	
Rise time ^c	t _r		V _{DD} = -15 V. Rι = 1.5 Ω		146	189	
Turn-off delay time ^c	t _{d(off)}	$I_D \cong$ -10 Å, V_{GEN} = -10 V, R_g = 1 Ω		-	57	75	ns
Fall time ^c	t _f			-	20	25	
Source-Drain Diode Ratings and Char	acteristics ^b						
Pulsed current ^a	I _{SM}			-	-	-84	Α
Forward voltage	V _{SD}	$I_{\rm F} = -3$ A, $V_{\rm GS} = 0$ V		-	-0.75	-1.2	V

Notes

d. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$

e. Guaranteed by design, not subject to production testing

f. Independent of operating temperature

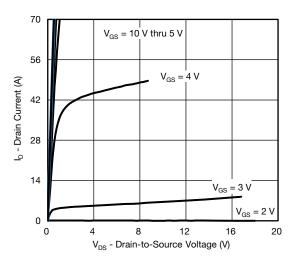
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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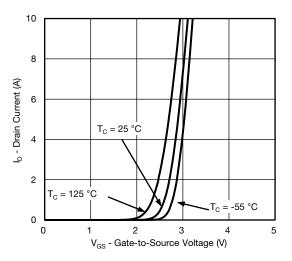


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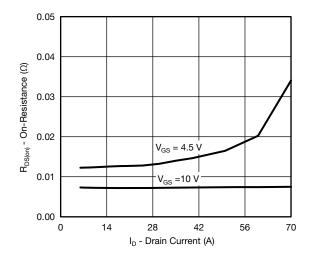
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

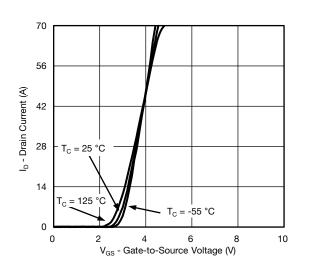


Output Characteristics



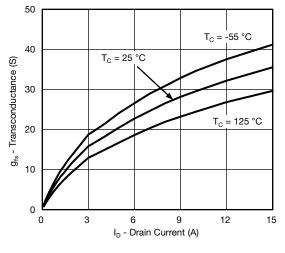
Transfer Characteristics





On-Resistance vs. Drain Current

Transfer Characteristics



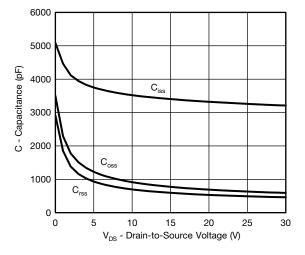
Transconductance

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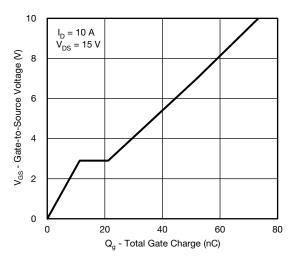


Capacitance

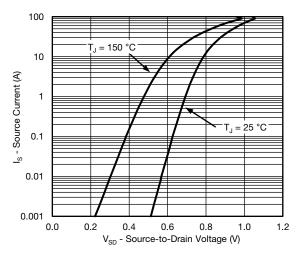


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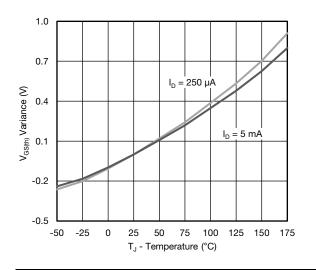
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

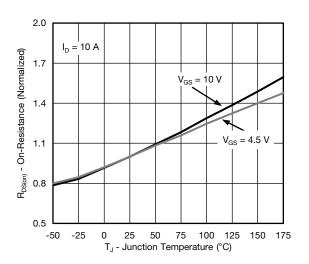


Gate Charge



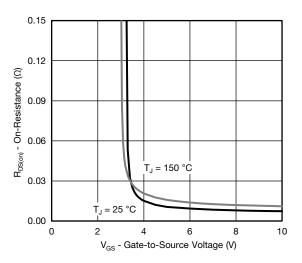
Source Drain Diode Forward Voltage





Threshold Voltage

On-Resistance vs. Junction Temperature



On-Resistance vs. Gate-to-Source Voltage

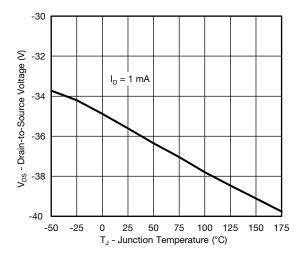
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For technical questions, contact: <u>automostechsupport@vishay.com</u>
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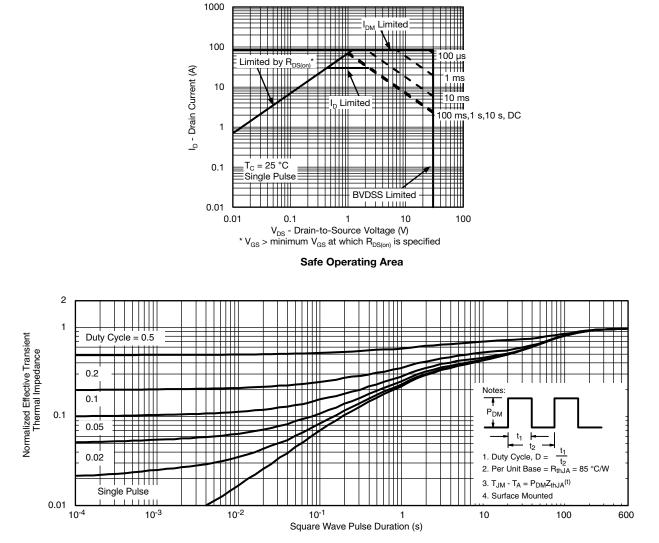


Drain Source Breakdown vs. Junction Temperature



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THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)

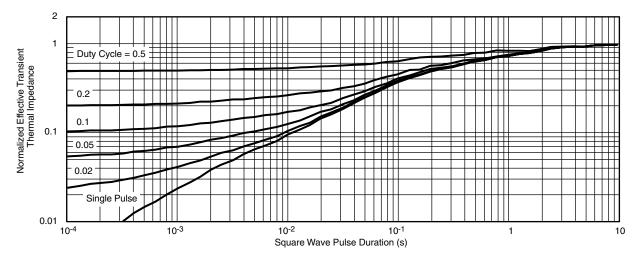


Normalized Thermal Transient Impedance, Junction-to-Ambient



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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

· The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?74794.



Package Information

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SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012





	MILLIMETERS		INCHES		
DIM	Min	Мах	Min	Max	
A	1.35	1.75	0.053	0.069	
A ₁	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
E	3.80	4.00	0.150	0.157	
е	1.27 BSC		0.050 BSC		
н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498					

Application Note 826

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RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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