**Vishay Semiconductors** 





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AAP Gen 7 (TO-240AA)

PRIMARY CHARACTERISTICS			
I <sub>F(AV)</sub> 110 A			
V <sub>R</sub>	30 V		
Package	AAP Gen 7 (TO-240AA)		
Circuit configuration	Two diodes doubler circuit		

### **MECHANICAL DESCRIPTION**

The AAP Gen 7, new generation of ADD-A-PAK module, combines the excellent thermal performances obtained by the usage of exposed direct bonded copper substrate, with advanced compact simple package solution and simplified internal structure with minimized number of interfaces.

### FEATURES

- 150 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- Low thermal resistance
- UL approved file E78996
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### BENEFITS

- Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- High surge capability
- Easy mounting on heatsink

### **ELECTRICAL DESCRIPTION / APPLICATIONS**

The VS-VSKDS220.. Schottky rectifier doubler has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature.

Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I <sub>F(AV)</sub>	Rectangular waveform	110	А	
V <sub>RRM</sub>		30	V	
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	18 000	А	
V <sub>F</sub>	110 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.57	V	
TJ	Range	-55 to +150	C°	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-VSKDS220/030	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	30	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	50	v	





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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at $T_C$ = 110 °C, rectangular waveform		110	
Maximum peak one cycle non-repetitive surge current	less s	5 $\mu s$ sine or 3 $\mu s$ rect. pulse	Following any rated load condition and with	18 000	А
	10 ms sine or 6 ms rect. pulse	rated V <sub>RRM</sub> applied	2000		
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 15 A, L = 1 mH		99	mJ
Repetitive avalanche current	I <sub>AR</sub>			А	

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	. TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V <sub>FM</sub>	110 A	T <sub>J</sub> = 25 °C	0.59	V
		220 A		0.78	
		110 A	T <sub>J</sub> = 125 °C	0.57	
		220 A		0.82	
Maximum reverse leakage current	I <sub>RM</sub>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	10	mA
		T <sub>J</sub> = 125 °C		650	
Maximum junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		7400	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs
Maximum RMS insulation voltage	V <sub>INS</sub>	50 Hz		3000 (1 min) 3600 (1 s)	V

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to +150	°C	
Maximum thermal resistance, junction to case per leg		R <sub>thJC</sub> DC operation		0.52	°C/W	
Typical thermal resistance, case to heatsink per module		R <sub>thCS</sub>		0.1		
Approvimeto weight				75	g	
Approximate weight				2.7	oz.	
Mounting torque ± 10 %	to heatsink		A mounting compound is recommended and the torque	4	Nm	
	busbar		should be rechecked after a period of 3 h to allow for the spread of the compound.	3		
Case style			JEDEC®	TO-240AA co	mpatible	

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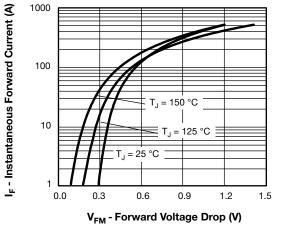
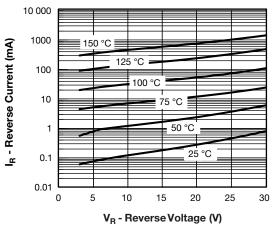
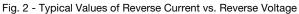


Fig. 1 - Maximum Forward Voltage Drop Characteristics





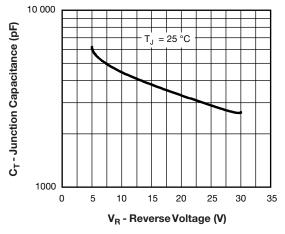


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

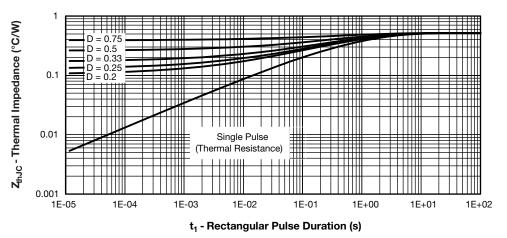


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

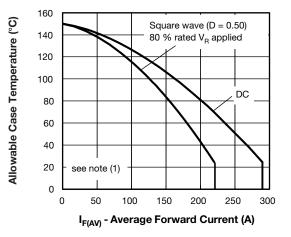
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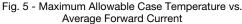
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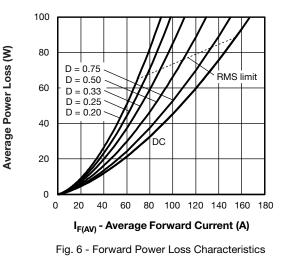
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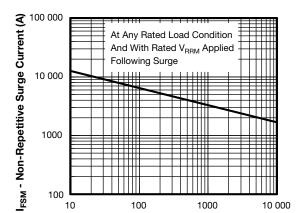


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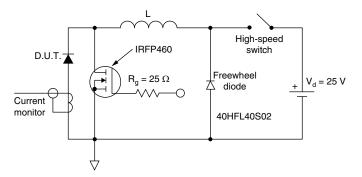






t<sub>p</sub> - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current





#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$ 

 $\begin{array}{l} \mbox{Pd} = \mbox{forward power loss} = \mbox{I}_{F(AV)} \times \mbox{V}_{FM} \mbox{ at } (\mbox{I}_{F(AV)}/\mbox{D}) \mbox{ (see fig. 6);} \\ \mbox{Pd}_{REV} = \mbox{inverse power loss} = \mbox{V}_{R1} \times \mbox{I}_{R} \mbox{ (1 - D); I}_{R} \mbox{ at } \mbox{V}_{R1} = 80 \ \% \mbox{ rated } \mbox{V}_{R} \end{array}$ 

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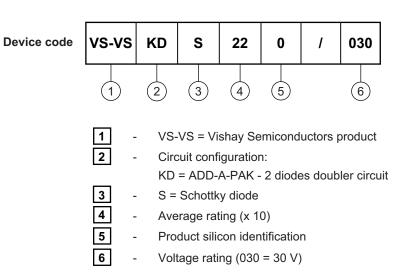
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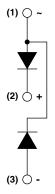
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### **ORDERING INFORMATION TABLE**



### **CIRCUIT CONFIGURATION**



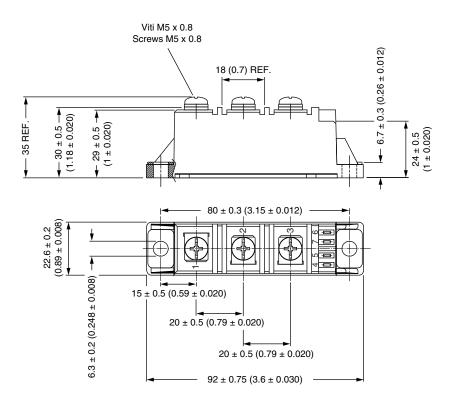
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95369			

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## **ADD-A-PAK Generation VII - Diode**

### **DIMENSIONS** in millimeters (inches)





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