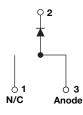
**Vishay Semiconductors** 

### HEXFRED<sup>®</sup> Ultrafast Soft Recovery Diode, 6 A



www.vishay.com



PRODUCT SUMMARY								
Package	TO-263AB (D <sup>2</sup> PAK)							
I <sub>F(AV)</sub>	6 A							
V <sub>R</sub>	1200 V							
V <sub>F</sub> at I <sub>F</sub>	2.4 V							
t <sub>rr</sub> (typ.)	26 ns							
T <sub>J</sub> max.	150 °C							
Diode variation	Single die							

#### FEATURES

- Ultrafast and ultrasoft recovery
- Very low I<sub>RRM</sub> and Q<sub>rr</sub>
- Specified at operating conditions
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### BENEFITS

- Reduced RFI and EMI
- · Reduced power loss in diode and switching transistor
- Higher frequency operation
- Reduced snubbing
- Reduced parts count

#### DESCRIPTION

VS-HFA06TB120S is a state of the art ultrafast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 1200 V and 6 A continuous current, the VS-HFA06TB120S is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultrafast recovery time, the HEXFRED® product line features extremely low values of peak recovery current (I<sub>BBM</sub>) and does not exhibit any tendency to "snap-off" during the t<sub>b</sub> portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED VS-HFA06TB120S is ideally suited for applications in power supplies and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Cathode to anode voltage	V <sub>R</sub>		1200	V					
Maximum continuous forward current	۱ <sub>F</sub>	T <sub>C</sub> = 100 °C	6						
Single pulse forward current	I <sub>FSM</sub>		80	А					
Maximum repetitive forward current	I <sub>FRM</sub>		24						
Maximum nawar dissinction	D-	T <sub>C</sub> = 25 °C	62.5	W					
Maximum power dissipation	PD	T <sub>C</sub> = 100 °C	25	vv					
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +150	°C					

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<b>ELECTRICAL SPECIFICATIONS</b> ( $T_J$ = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Cathode to anode breakdown voltage	V <sub>BR</sub>	I <sub>R</sub> = 100 μA	1200	-	-				
Maximum forward voltage	V <sub>FM</sub>	I <sub>F</sub> = 6.0 A	-	2.7	3.0	V			
		I <sub>F</sub> = 12 A	-	3.5	3.9				
		I <sub>F</sub> = 6.0 A, T <sub>J</sub> = 125 °C	-	2.4	2.8				
Maximum reverse		$V_{R} = V_{R}$ rated	-	0.26	5.0				
leakage current	I <sub>RM</sub>	$T_J = 125 \text{ °C}, V_R = 0.8 \text{ x } V_R \text{ rated}$	-	110	500	μΑ			
Junction capacitance	CT	C <sub>T</sub> V <sub>R</sub> = 200 V		9.0	14	pF			
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nH			

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS	
	t <sub>rr</sub>	I <sub>F</sub> = 1.0 A, dI <sub>F</sub> /dt = 200	A/ $\mu$ s, V <sub>R</sub> = 30 V	-	26	-	A nC	
Reverse recovery time	t <sub>rr1</sub>	T <sub>J</sub> = 25 °C	$I_{\rm F} = 6.0 \text{ A}$	-	53	80		
	t <sub>rr2</sub>	T <sub>J</sub> = 125 °C		-	87	130		
Peak recovery current	I <sub>RRM1</sub>	T <sub>J</sub> = 25 °C		-	4.4	8.0		
	I <sub>RRM2</sub>	T <sub>J</sub> = 125 °C		-	5.0	9.0		
Reverse recovery charge	Q <sub>rr1</sub>	T <sub>J</sub> = 25 °C	dl <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 200 V	-	116	320		
neverse recovery charge	Q <sub>rr2</sub>	T <sub>J</sub> = 125 °C		-	233	585		
Peak rate of recovery current	dl <sub>(rec)M</sub> /dt1	T <sub>J</sub> = 25 °C		-	180	-		
during t <sub>b</sub>	dl <sub>(rec)M</sub> /dt2	T <sub>J</sub> = 125 °C		-	100	-	A/µs	

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Lead temperature	T <sub>lead</sub>	0.063" from case (1.6 mm) for 10 s	-	-	300	°C		
Thermal resistance, junction to case	R <sub>thJC</sub>		-	-	2.0			
Thermal resistance, junction to ambient	R <sub>thJA</sub>	Typical socket mount	-	-	80	K/W		
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-			
Weight			-	2.0	-	g		
weight			-	0.07	-	oz.		
Marking device		Case style TO-263AB (D <sup>2</sup> PAK)		HFA06	TB120S			



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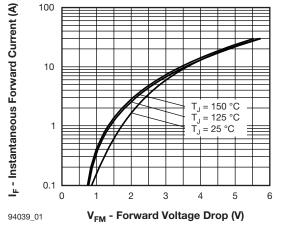


Fig. 1 - Typical Forward Voltage Drop Characteristics

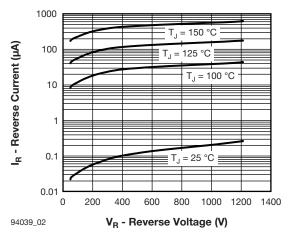


Fig. 2 - Typical Reverse Current vs. Reverse Voltage

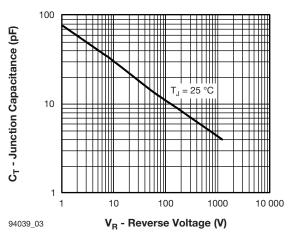


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

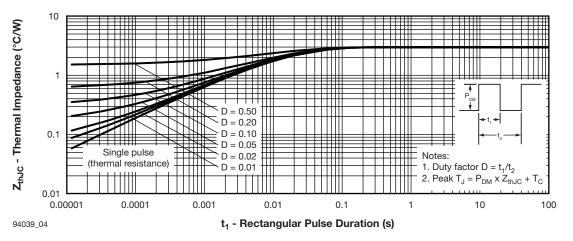


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

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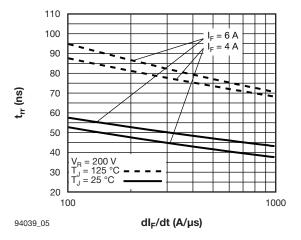


Fig. 5 - Typical Reverse Recovery Time vs. dI<sub>F</sub>/dt

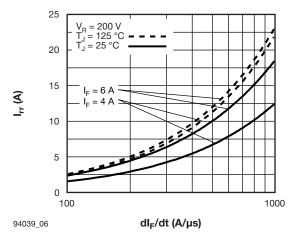


Fig. 6 - Typical Recovery Current vs. dl<sub>F</sub>/dt

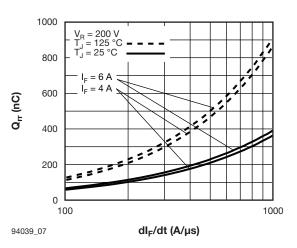


Fig. 7 - Typical Stored Charge vs. dl<sub>F</sub>/dt

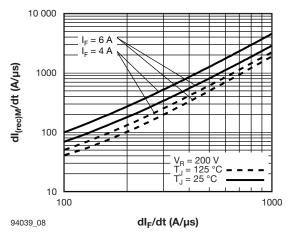


Fig. 8 - Typical dl<sub>(rec)M</sub>/dt vs. dl<sub>F</sub>/dt



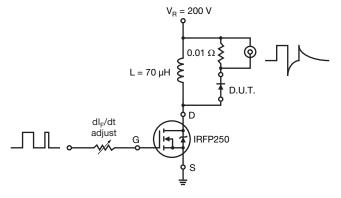


Fig. 9 - Reverse Recovery Parameter Test Circuit

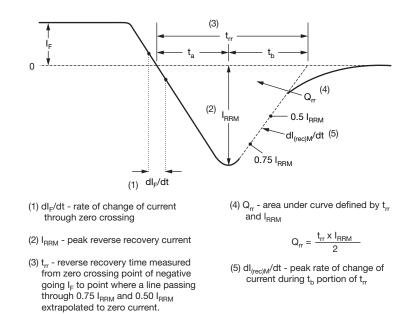


Fig. 10 - Reverse Recovery Waveform and Definitions

#### **ORDERING INFORMATION TABLE**

Device code	VS-	HF	Α	06	тв	120	S	TRL	PbF		
				<u> </u>							
		(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)		
	$\bigcirc$		$\bigcirc$	4	$\bigcirc$	$\bigcirc$	$\cup$	$\bigcirc$	9		
	1 -	- Visł	nav Sem	niconduc	ctors pro	oduct					
	2 -										
		<ul> <li>HEXFRED<sup>®</sup> family</li> <li>Process designator: A = electron irradiated</li> </ul>									
	4 -			0 (	,						
	5 -	Pac	kage ou	utline (TI	B = TO-	220, 2	eads)				
	6 -	Volt	age rati	ng (120	= 1200	V)					
	7 -	S =	D <sup>2</sup> PAK								
	8 -	• No	one = tu	be							
		• TF	RL = tap	e and re	el (left	oriented	d)				
		• TF	RR = tap	be and r	eel (righ	nt orient	ed)				
	9 -	• Pk	oF = lea	d (Pb)-fi	ree, for	tube pa	ckaged				
		• P	= lead (	Pb)-free	, for tap	e and r	eel pac	kaged			

LINKS TO RELATED DOCUMENTS							
Dimensions	www.vishay.com/doc?95046						
Part marking information	www.vishay.com/doc?95054						
Packaging information	www.vishay.com/doc?95032						

ORDERING INFORMATION (Example)										
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION							
VS-HFA06TB120SPBF	50	1000	Antistatic plastic tube							
VS-HFA06TB120STRRP	800	800	13" diameter reel							
VS-HFA06TB120STRLP	800	800	13" diameter reel							

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## VS-HFA06TB120SPbF

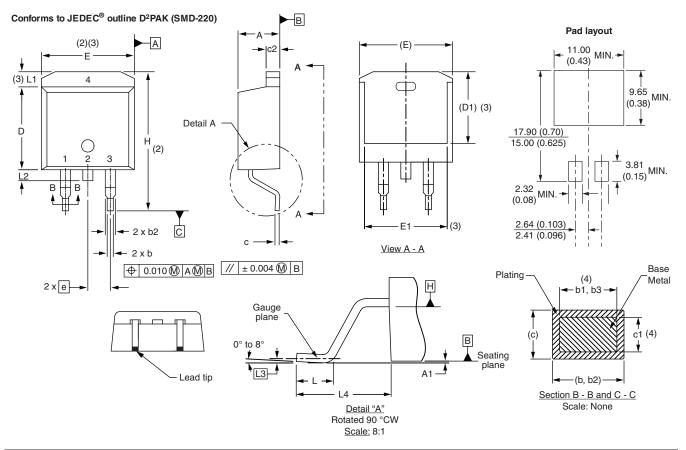
### **Outline Dimensions**



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D<sup>2</sup>PAK

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INC	HES	NOTES		SYMBOL	MILLIN	ETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STINDUL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54 BSC 0.100 BSC				
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	) BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5 M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Datum A and B to be determined at datum plane H

<sup>(6)</sup> Controlling dimension: inch

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-263AB

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