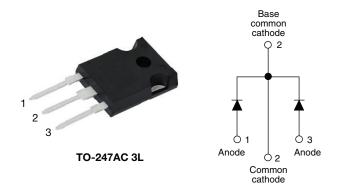
Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 20 A



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PRIMARY CHARACTERISTICS								
I _{F(AV)}	2 x 20 A							
V _R	50 V to 60 V							
V _F at I _F	0.49 V							
I _{RM} typ.	96 mA at 125 °C							
T _J max.	150 °C							
E _{AS}	18 mJ							
Package	TO-247AC 3L							
Circuit configuration	Common cathode							

FEATURES

- 150 °C T_J operation
- · Very low forward voltage drop
- · High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC[®]-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-40CPQ... center tap Schottky rectifier has been optimized for very low forward voltage drop with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	VALUES	UNITS							
I _{F(AV)}	Rectangular waveform	40	А						
V _{RRM}		50/60	V						
I _{FSM}	t _p = 5 μs sine	3200	А						
V _F	$20 \text{ A}_{\text{pk}}, \text{ T}_{\text{J}} = 125 \text{ °C} \text{ (per leg)}$	0.49	V						
TJ		-55 to +150	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-40CPQ050-N3	VS-40CPQ060-N3	UNITS				
Maximum DC reverse voltage	V _R	50	60	V				
Maximum working peak reverse voltage	V _{RWM}	50	80	v				

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS					
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_C = 120 °C	40						
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse Following any rated load condition and with rated		3200	А				
non-repetitive surge current per leg See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	320						
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 \ ^{\circ}C, \ I_{AS} = 2 \ A, \ L = 9.0 \ m$	18	mJ					
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to ze Frequency limited by T _J maxim	2	А					

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ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS						
		20 A	T _{.1} = 25 °C	0.53					
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	40 A	1j=25 C	0.68	V				
	VFM (''	20 A	T.I = 125 °C	0.49					
		40 A	1j = 125 C	0.64					
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	1.7	mA				
Maximum reverse leakage current per leg	IRM ("	T _J = 125 °C	VR - naleu VR	180					
Typical reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = Rated V _R	96	mA				
Maximum junction capacitance per leg C _T		$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		1600	pF				
Typical series inductance per leg	L _S	Measured lead to lead 5 m	7.5	nH					
Maximum voltage rate of change	dV/dt	Rated V _R	Rated V _R 10 000						

Note

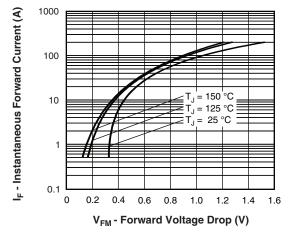
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

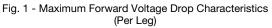
THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to 150	°C			
Maximum thermal resistance, junction to case per leg		D	DC operation See fig. 4	1.25				
Maximum thermal resistance, junction to case per package		- R _{thJC}	DC operation	0.63	°C/W			
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.24				
Approvimate weight				6	g			
Approximate weight				0.21	oz.			
Mounting torque	minimum		Non-lubricated threads	6 (5)	kgf ⋅ cm			
Mounting torque	maximum		Non-Indificated threads	12 (10)	(lbf · in)			
Marking davias				40CPQ050				
Marking device			Case style TO-247AC 3L	40CP	Q060			

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VS-40CPQ050-N3, VS-40CPQ060-N3

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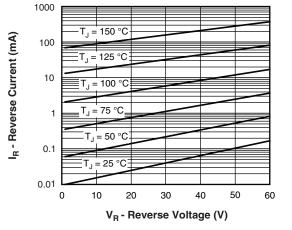


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

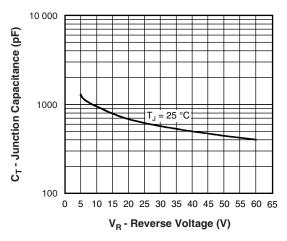
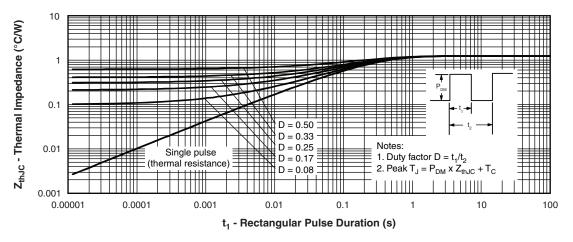
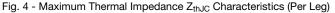


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





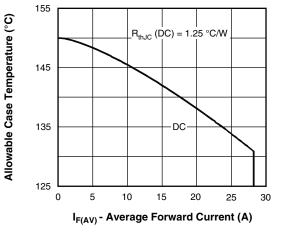
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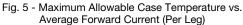
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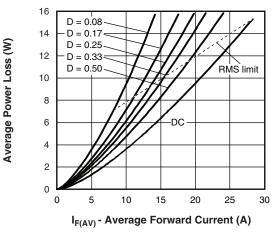


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

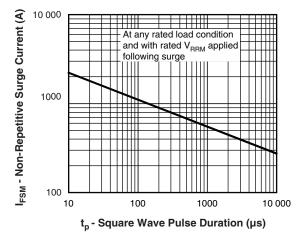


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

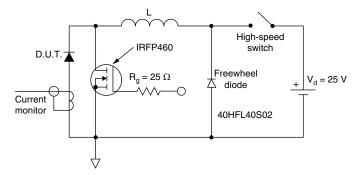


Fig. 8 - Unclamped Inductive Test Circuit



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(Pb)-free

ORDERING INFORMATION TABLE

Device code	VS-	40	С	Р	Q	060	-N3
		2	3	4	5	6	7
	1 -			niconduc	•	duct	
	2 -			ng (40 =			
	3 -	Circ	uit confi	iguratior	1:		
	_	C =	commo	n cathoo	de		
	4 -	Pac	kage:				
		P =	TO-247				
	5 -	Sch	ottky "Q	" series		Г	0=0
	6 -	Volt	age cod	le —			050 = 500 060 = 600
	7 -	Env	ironmer	ntal digit		L	000 - (
		-N3	= halog	gen-free	, RoHS-	complia	nt, and

ORDERING INFORMATION (Example)									
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTIO									
VS-40CPQ050-N3	25	500	Antistatic plastic tube						
VS-40CPQ060-N3	25	500	Antistatic plastic tube						

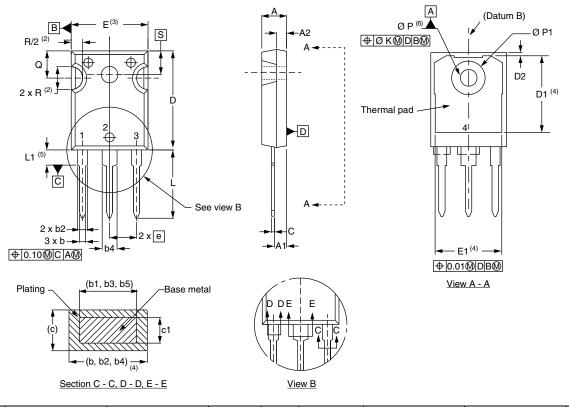
LINKS TO RELATED DOCUMENTS							
Dimensions www.vishay.com/doc?96138							
Part marking information	www.vishay.com/doc?95007						



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TO-247AC 3L

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INC	HES	NOTES	NOTES		MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES		SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.65	5.31	0.183	0.209			D2	0.51	1.35	0.020	0.053	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.17	1.37	0.046	0.054			E1	13.46	-	0.53	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	5 BSC	
b1	0.99	1.35	0.039	0.053			ØК	0.2	254	0.0)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			ØΡ	3.56	3.66	0.14	0.144	
b5	2.59	3.38	0.102	0.133			Ø P1	-	7.39	-	0.291	
С	0.38	0.89	0.015	0.035			Q	5.31	5.69	0.209	0.224	
c1	0.38	0.84	0.015	0.033			R	4.52	5.49	0.178	0.216	
D	19.71	20.70	0.776	0.815	3		S	5.51	BSC	0.217	' BSC	
D1	13.08	-	0.515	-	4							

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

⁽⁵⁾ Lead finish uncontrolled in L1

⁽⁶⁾ Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-247 with exception of dimension Q

Revision: 20-Jun-17

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