

**Vishay Siliconix** 

RoHS

COMPLIANT

# N-Channel 75-V (D-S) MOSFET

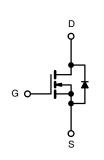
PRODUCT SUMMARY			
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω)	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)
75	0.0068 at $V_{GS}$ = 10 V	90 <sup>d</sup>	75

#### **FEATURES**

- TrenchFET<sup>®</sup> Power MOSFET
- 175 °C Junction Temperature ٠
- 100 % R<sub>g</sub> and UIS Tested •
- Compliant to RoHS Directive 2002/95/EC

### **APPLICATIONS**

- Power Supply
- Secondary Synchronous Rectification
- Industrial



N-Channel MOSFET

FO-220AE	3
0	

Top View Ordering Information: SUP90N08-6m8P-E3 (Lead (Pb)-free)

<b>ABSOLUTE MAXIMUM RATINGS</b>	$T_C = 25 \ ^{\circ}C$ , unless oth	erwise noted			
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	75	v	
Gate-Source Voltage		V <sub>GS</sub>	± 22	v	
Continuous Drain Current (T <sub>1</sub> = 175 °C)	T <sub>C</sub> = 25 °C	L	90 <sup>d</sup>	А	
Continuous Drain Current $(1) = 175$ C)	T <sub>C</sub> = 70 °C	I <sub>D</sub>	90 <sup>d</sup>		
Pulsed Drain Current		I <sub>DM</sub>	240	A	
Avalanche Current		I <sub>AS</sub>	50		
Single Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	125	mJ	
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 25 °C	Р	272 <sup>b</sup>	w	
	T <sub>A</sub> = 25 °C <sup>c</sup>	- P <sub>D</sub> -	3.75		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Limit	Unit	
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	40	°C/W	
Junction-to-Case (Drain)	R <sub>thJC</sub>	0.55		

Notes:

a. Duty cycle  $\leq$  1 %.

b. See SOA curve for voltage derating.c. When Mounted on 1" square PCB (FR-4 material).

d. Package limited.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = 250 \mu A$	75			V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	2.5		4.5	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 250	nA	
		$V_{DS} = 75 \text{ V}, V_{GS} = 0 \text{ V}$			1	 I	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = 75 V, $V_{GS}$ = 0 V, $T_{J}$ = 125 °C			50	50 μA 250	
		$V_{DS} = 75 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 150 ^{\circ}\text{C}$			250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 10 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	70			Α	
Drain-Source On-State Resistance <sup>a</sup>	Б	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A		0.0056	0.0068	Ω	
	R <sub>DS(on)</sub>	$V_{GS}$ = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C		0.009	0.011		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 20 A		50		S	
Dynamic <sup>b</sup>	•			•			
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 30 V, f = 1 MHz		4620			
Output Capacitance	C <sub>oss</sub>			517		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			247			
Total Gate Charge <sup>c</sup>	Qg			75	115		
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 50 \text{ A}$		25.5		nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			20			
Gate Resistance	R <sub>g</sub>	f = 1 MHz		1.2	2.4	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			16	30		
Rise Time <sup>c</sup>	t <sub>r</sub>	$\label{eq:VDD} \begin{array}{l} V_{DD} = 30 \; V,  R_L = 0.6 \; \Omega \\ I_D \cong 50 \; A,  V_GEN = 10 \; V,  R_g = 1 \; \Omega \end{array}$		11	20		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			24	40	ns	
Fall Time <sup>c</sup>	t <sub>f</sub>	1		10	20		
Source-Drain Diode Ratings and Cha	aracteristics 7	<sub>C</sub> = 25 °C <sup>b</sup>					
Continuous Current	۱ <sub>S</sub>				85	^	
Pulsed Current	I <sub>SM</sub>				240	A	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{F} = 20 \text{ A}, V_{GS} = 0 \text{ V}$		0.83	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>			60	100	ns	
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = 75 A, dl/dt = 100 A/μs		3.3	4.5	Α	
Reverse Recovery Charge	Q <sub>rr</sub>			100	150	nC	

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

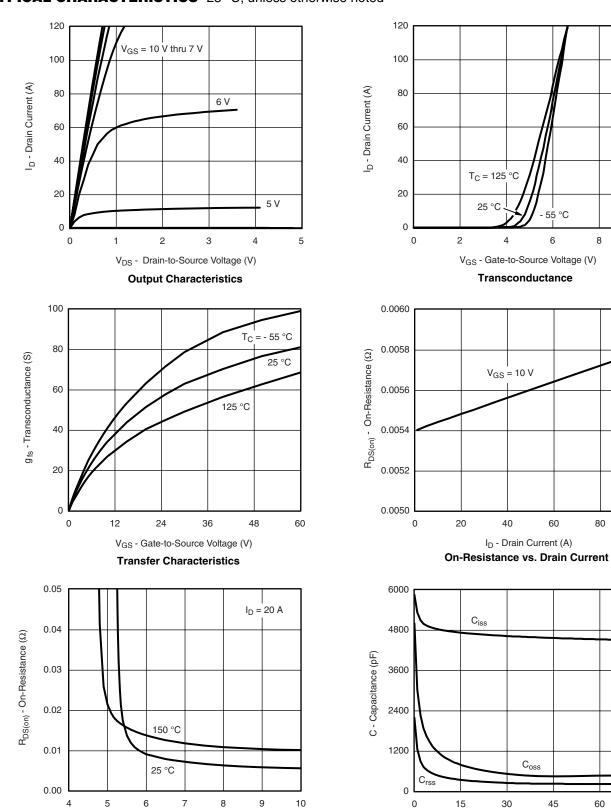


# SUP90N08-6m8P

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10

100



#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

Document Number: 69538 S09-2435-Rev. C, 16-Nov-09

V<sub>GS</sub> - Gate-to-Source Voltage (V)

**On-Resistance vs. Gate-to-Source Voltage** 

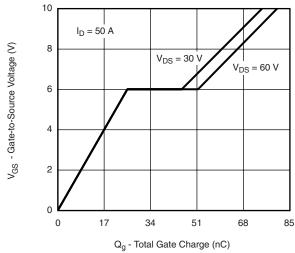
V<sub>DS</sub> - Drain-to-Source Voltage (V)

Capacitance

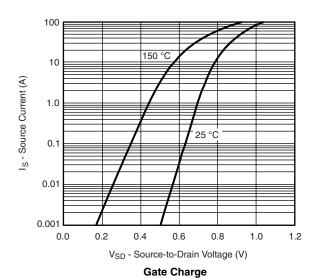
75

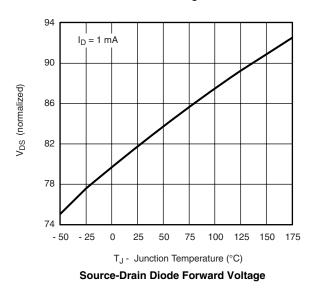
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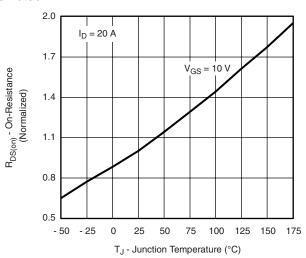
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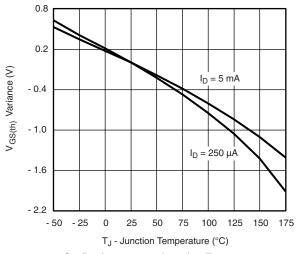




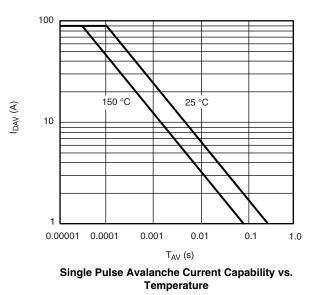


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Threshold Voltage



**On-Resistance vs. Junction Temperature** 

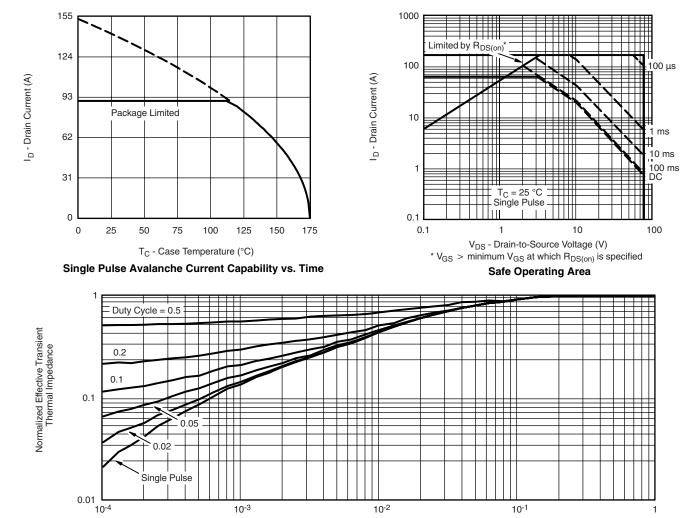


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### **Vishay Siliconix**

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

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Square Wave Pulse Duration (s) Normalized Thermal Transient Impedance, Junction-to-Case

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