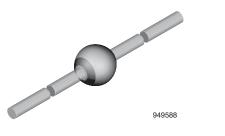
www.vishay.com

# 1N5624, 1N5625, 1N5626, 1N5627

**Vishay Semiconductors** 

# **Standard Avalanche Sinterglass Diode**



#### **DESIGN SUPPORT TOOLS**



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#### **MECHANICAL DATA**

Case: SOD-64

Terminals: plated axial leads, solderable per MIL-STD-750, method 2026

Polarity: color band denotes cathode end

#### Mounting position: any

Weight: approx. 858 mg

#### **FEATURES**

- Glass passivated junction
- · Hermetically sealed package
- · Controlled avalanche characteristics
- Low reverse current
- High surge current loading
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **APPLICATIONS**

Rectification diode, general purpose

ORDERING INFORMATION (Example)					
DEVICE NAME	ORDERING CODE TAPED UNITS MINIMUM ORDER QUANT				
1N5627	1N5627-TR	2500 per 10" tape and reel	12 500		
1N5627	1N5627-TAP	2500 per ammopack	12 500		

PARTS TABLE		
PART	TYPE DIFFERENTIATION	PACKAGE
1N5624	V <sub>R</sub> = 200 V; I <sub>F(AV)</sub> = 3 A	SOD-64
1N5625	$V_{R} = 400 \text{ V}; \text{ I}_{F(AV)} = 3 \text{ A}$	SOD-64
1N5626	$V_{R} = 600 \text{ V}; I_{F(AV)} = 3 \text{ A}$	SOD-64
1N5627	V <sub>R</sub> = 800 V; I <sub>F(AV)</sub> = 3 A	SOD-64

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)						
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
	See electrical characteristics	1N5624	$V_R = V_{RRM}$	200	V	
Reverse voltage = repetitive peak reverse		1N5625	$V_R = V_{RRM}$	400	V	
voltage		1N5626	$V_R = V_{RRM}$	600	V	
		1N5627	$V_{R} = V_{RRM}$	800	V	
Peak forward surge current	t <sub>p</sub> = 10 ms, half sine wave		I <sub>FSM</sub>	100	А	
Repetitive peak forward current			I <sub>FRM</sub>	18	А	
Average forward current			I <sub>F(AV)</sub>	3	А	
Pulse avalanche peak power	t <sub>p</sub> = 20 µs, half sine wave, T <sub>j</sub> = 175 °C		P <sub>R</sub>	1000	W	
Pulse energy in avalanche mode, non repetitive (inductive load switch off)	I <sub>(BR)R</sub> = 1 A, T <sub>j</sub> = 175 °C		E <sub>R</sub>	20	mJ	
i <sup>2</sup> *t-rating			i <sup>2</sup> *t	40	A²*s	
Junction and storage temperature range			$T_j = T_{stg}$	-55 to +175	°C	

Rev. 1.6, 22-Feb-18

Document Number: 86063



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MAXIMUM THERMAL RESISTANCE (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Junction ambient	I = 10 mm, T <sub>L</sub> = constant	R <sub>thJA</sub>	25	K/W	
	On PC board with spacing 25 mm	R <sub>thJA</sub>	70	K/W	

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb} = 25 \degree C$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 3 A		V <sub>F</sub>	-	-	1	V
Reverse current	$V_{R} = V_{RRM}$		I <sub>R</sub>	-	0.1	1	μA
neverse current	$V_{R} = V_{RRM}, T_{j} = 100 \ ^{\circ}C$		I <sub>R</sub>	-	5	10	μA
Breakdown voltage	$ I_{\rm R} = 100 \; \mu {\rm A},  t_p / T = 0.01, \\ t_p = 0.3 \; ms $		V <sub>(BR)</sub>	-	-	1600	V
Diode capacitance	$V_{R} = 4 V, f = 1 MHz$		CD	-	40	60	pF
Reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, i_R = 0.25 \text{ A}$		t <sub>rr</sub>	-	3.5	5	μs
neverse recovery lime	$I_F = 1 \text{ A}, d_l/d_t = 5 \text{ A}/\mu \text{s}, V_R = 50 \text{ V}$		t <sub>rr</sub>	-	4.5	7.5	μs
Reverse recovery charge	$I_F = 1 \text{ A}, d_l/d_t = 5 \text{ A}/\mu \text{s}$		Q <sub>rr</sub>	-	8	12	μC

#### TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

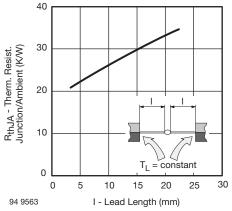
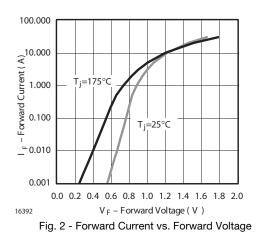


Fig. 1 - Max. Thermal Resistance vs. Lead Length



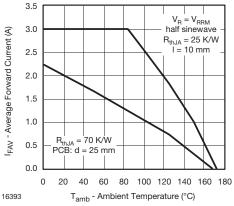


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

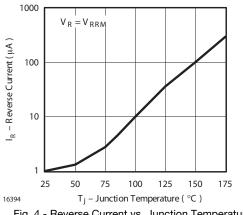


Fig. 4 - Reverse Current vs. Junction Temperature

Rev. 1.6, 22-Feb-18

2

Document Number: 86063

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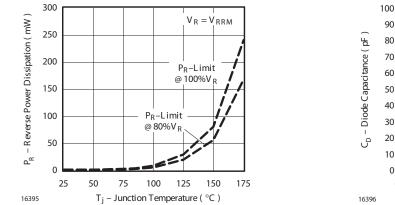


Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

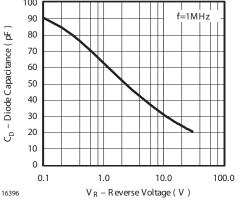
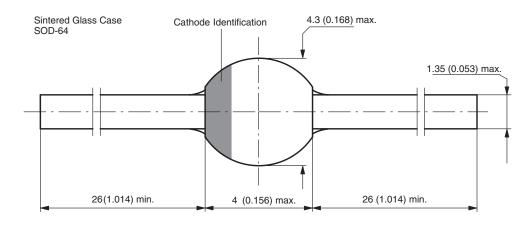


Fig. 6 - Diode Capacitance vs. Reverse Voltage

#### PACKAGE DIMENSIONS in millimeters (inches): SOD-64



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