

## N-Channel 60 V (D-S) MOSFET



PRODUCT SUMMARY				
V <sub>DS</sub> (V)	60			
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 10 \text{ V}$	0.0115			
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 4.5 \text{ V}$	0.0153			
Q <sub>g</sub> typ. (nC)	7			
I <sub>D</sub> (A)	43.4			
Configuration	Single			

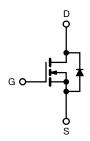
#### **FEATURES**

- TrenchFET® Gen IV power MOSFET
- Very low R<sub>DS</sub> x Q<sub>g</sub> figure-of-merit (FOM)
- Tuned for the lowest R<sub>DS</sub> x Q<sub>oss</sub> FOM
- 100 % R<sub>a</sub> and UIS tested
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

# HALOGEN **FREE**

#### **APPLICATIONS**

- · Synchronous rectification
- · Primary side switch
- DC/DC converters
- OR-ing
- Power supplies
- Motor drive control
- · Battery and load switch



N-Channel MOSFET

ORDERING INFORMATION	
Package	PowerPAK SO-8
Lead (Pb)-free and halogen-free	SIR4608LDP-T1-GE3

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		$V_{DS}$	60	V	
Gate-source voltage		$V_{GS}$	± 20		
Continuous drain current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 25 °C		43.4	A	
	T <sub>C</sub> = 70 °C	1 .	34.7		
	T <sub>A</sub> = 25 °C	l <sub>D</sub>	13.3 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		10.6 b, c		
Pulsed drain current (t = 100 μs)		I <sub>DM</sub>	100	Α .	
Continuous source-drain diode current	T <sub>C</sub> = 25 °C	I <sub>S</sub>	35.5 <sup>a</sup>		
	T <sub>A</sub> = 25 °C		3.3 b, c		
Single pulse avalanche current	L = 0.1 mH	I <sub>AS</sub>	15	1	
Single pulse avalanche energy	L = U. I MIH	E <sub>AS</sub>	11.25	mJ	
Maximum power dissipation	T <sub>C</sub> = 25 °C		39		
	T <sub>C</sub> = 70 °C	P <sub>D</sub>	25	W	
	T <sub>A</sub> = 25 °C		3.6 b, c		
	T <sub>A</sub> = 70 °C	1	2.3 b, c	Ī	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C	
Soldering recommendations (peak temperature) d, e		3	260	1	

THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT		
Maximum junction-to-ambient <sup>b</sup>	t ≤ 10 s	R <sub>thJA</sub>	24	34	°C/W	
Maximum junction-to-case (drain)	Steady state	R <sub>thJC</sub>	2.5	3.2	7 6/00	

#### Notes

- a.  $T_C = 25$  °C
- b. Surface mounted on 1" x 1" FR4 board
- d. See solder profile (www.vishay.com/doc?73257). The PowerPAK SO-8 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection
- e. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components
- Maximum under steady state conditions is 70 °C/W



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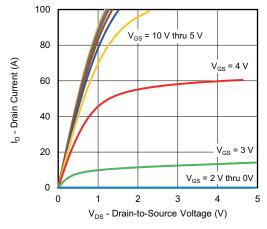
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static					•		
Drain-source breakdown voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	60	-	-	V	
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = 10 mA	-	34	-	ma\//06	
V <sub>GS(th)</sub> temperature coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA	-	-4.1	-	mV/°C	
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1	-	3	V	
Gate-source leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	100	nA	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V	-	-	1	μA	
		V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70 °C	-	-	15		
Dynin actives on state varietones a	В	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A	-	0.0094	0.0115	Ω	
Drain-source on-state resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$	-	0.0128	0.0153		
Forward transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 10 A	-	31	-	S	
Dynamic <sup>b</sup>							
Input capacitance	C <sub>iss</sub>		-	905	-	pF	
Output capacitance	C <sub>oss</sub>	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	198	-		
Reverse transfer capacitance	C <sub>rss</sub>		-	6.2	-		
Table de de com	0	$V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$	-	15	23		
Total gate charge	Qg		-	7	11		
Gate-source charge	$Q_{gs}$	$V_{DS} = 30 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$	-	3.1	-	nC	
Gate-drain charge	Q <sub>gd</sub>		-	1.95	-	1	
Output charge	Q <sub>oss</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	-	12.4	-		
Gate resistance	$R_g$	f = 1 MHz	0.7	1.5	2.6	Ω	
Turn-on delay time	t <sub>d(on)</sub>		-	9	18		
Rise time	t <sub>r</sub>	$V_{DD}=30~V,~R_L=3~\Omega,~I_D\cong10~A,$	-	6	12		
Turn-off delay time	t <sub>d(off)</sub>	$V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$	-	17	34		
Fall time	t <sub>f</sub>		-	4	8		
Turn-on delay time	t <sub>d(on)</sub>		-	13	26	ns	
Rise time	t <sub>r</sub>	$V_{DD} = 30 \text{ V}, \text{ R}_L = 3 \Omega, \text{ I}_D \cong 10 \text{ A},$	-	58	116		
Turn-off delay time	t <sub>d(off)</sub>	$V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$	-	16	32		
Fall time	t <sub>f</sub>		-	8	16		
Drain-Source Body Diode Characterist	ics						
Continuous source-drain diode current	Is	T <sub>C</sub> = 25 °C	-	-	35.5		
Pulse diode forward current	I <sub>SM</sub>		-	-	100	A	
Body diode voltage	V <sub>SD</sub>	$I_{S} = 5 \text{ A}, V_{GS} = 0 \text{ V}$	-	0.81	1.1	V	
Body diode reverse recovery time	t <sub>rr</sub>		-	19	38	ns	
Body diode reverse recovery charge	Q <sub>rr</sub>	$I_F = 10 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s},$	-	9	18	nC	
Reverse recovery fall time	t <sub>a</sub>	$T_J = 25 ^{\circ}\text{C}$	-	9	-		
Reverse recovery rise time	t <sub>b</sub>		_	10	_	ns	

### Notes

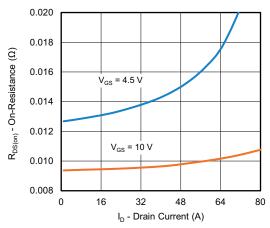
- a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

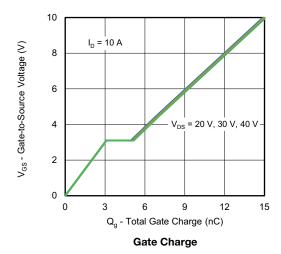


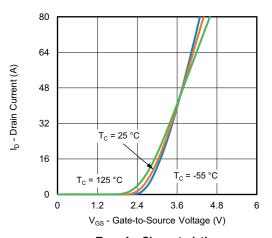


#### **Output Characteristics**

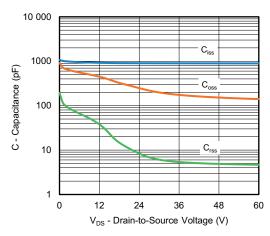


On-Resistance vs. Drain Current and Gate Voltage

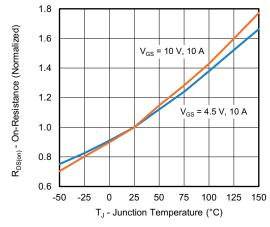




**Transfer Characteristics** 

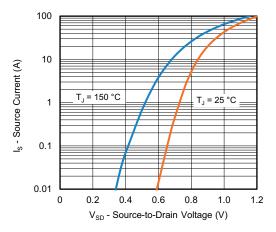


Capacitance

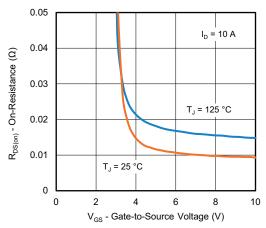


On-Resistance vs. Junction Temperature

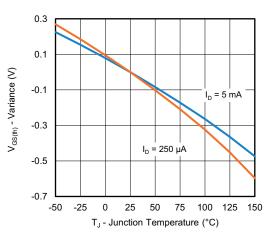




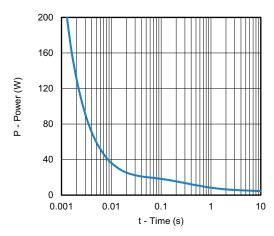
#### Source-Drain Diode Forward Voltage



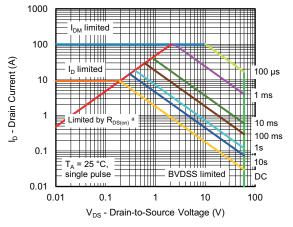
On-Resistance vs. Gate-to-Source Voltage



**Threshold Voltage** 



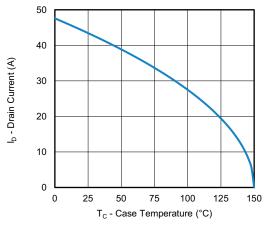
Single Pulse Power, Junction-to-Ambient



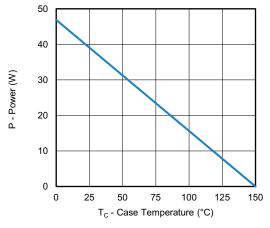
Safe Operating Area, Junction-to-Ambient

## Note

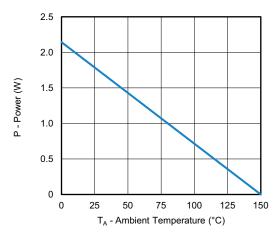
a.  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified



#### Current Derating a





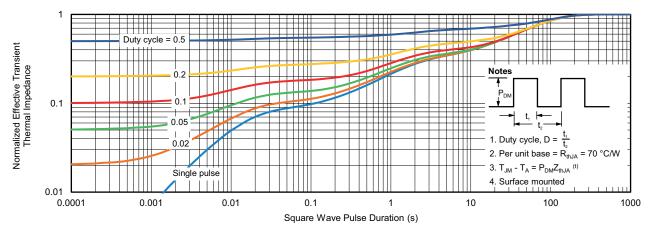


Power, Junction-to-Ambient

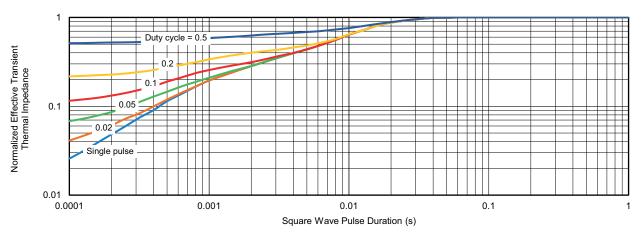
#### Note

a. The power dissipation  $P_D$  is based on  $T_J$  max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit





Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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