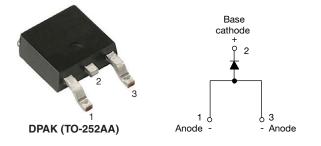
Vishay Semiconductors

High Voltage Surface Mountable Input Rectifier Diode, 8 A



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PRIMARY CHARACTERISTICS				
I _{F(AV)}	8 A			
V _R	1200 V			
V _F at I _F	1.1 V			
I _{FSM}	150 A			
T _J max.	150 °C			
Package	DPAK (TO-252AA)			
Circuit configuration	Single			

FEATURES

- Glass passivated pellet chip junction
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Flexible solution for reliable AC power rectification
- High surge, low V_F rugged blocking diode for DC charging stations
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- On-board and off-board EV / HEV battery chargers
- Renewable energy inverters

DESCRIPTION

The VS-8EWS12SLHM3 rectifier high voltage series has been optimized for very low forward voltage drop, with moderate leakage.

The high reverse voltage range available allows design of input stage primary rectification with outstanding voltage surge capability.

OUTPUT CURRENT IN TYPICAL APPLICATIONS							
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS				
NEMA FR-4 or G10 glass fabric-based epoxy with 4 oz. (140 $\mu m)$ copper	1.2	1.6					
Aluminum IMS, R _{thCA} = 15 °C/W	2.5	2.8	A				
Aluminum IMS with heatsink, $R_{thCA} = 5 \text{ °C/W}$	5.5	6.5					

Note

T_A = 55 °C, T_J = 125 °C, footprint 300 mm²

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Sinusoidal waveform	8	A		
V _{RRM}		1200	V		
I _{FSM}		150	A		
V _F	8 A, T _J = 25 °C	1.10	V		
TJ		-55 to +150	°C		

VOLTAGE RATINGS						
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 ℃ mA			
VS-8EWS12SLHM3	1200	1300	0.50			

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum average forward current	I _{F(AV)}	$T_C = 105 \text{ °C}$, 180° conduction half sine wave	8		
Maximum peak one cycle I _{FSM}		10 ms sine pulse, rated V _{RRM} applied	125	A	
		10 ms sine pulse, no voltage reapplied	150		
Maximum I ² t for fusing I ² t		10 ms sine pulse, rated V _{RRM} applied	78	A ² s	
		10 ms sine pulse, no voltage reapplied	110	A-5	
Maximum I ² √t for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied	1100	A²√s	

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS		UNITS
Maximum forward voltage drop	V _{FM}	8 A, T _J = 25 °C		1.1	V
Forward slope resistance	r _t	T _J = 150 °C		20	mΩ
Threshold voltage	V _{F(TO)}			0.82	V
Maximum reverse leakage current		T _J = 25 °C	V roted V	0.05	mA
Maximum reverse leakage current I _{RM}	IRM	$T_J = 150 \text{ °C}$ $V_R = \text{rated } V_{RRM}$		0.50	ШA

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55 to +150	°C
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	2.5	°C/W
Typical thermal resistance, junction to ambient (PCB mount)	R _{thJA} ⁽¹⁾		62	C/W
Approvimeto weight			1	g
Approximate weight			0.03	oz.
Marking device		Case style DPAK (TO-252AA)	8EWS	12SH

Note

 $^{(1)}$ When mounted on 1" square (650 mm²) PCB of FR-4 or G-10 material 4 oz. (140 μm) copper 40 °C/W

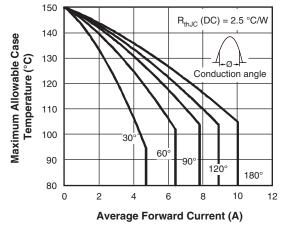


Fig. 1 - Current Rating Characteristics

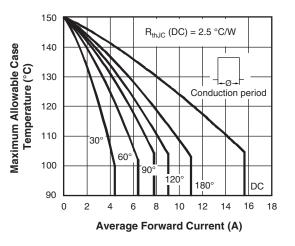


Fig. 2 - Current Rating Characteristics

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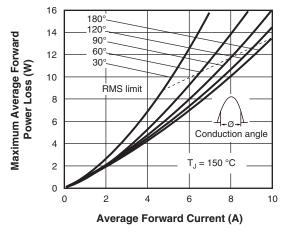
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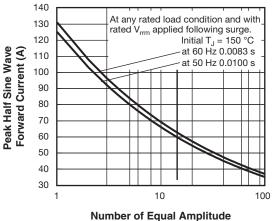


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Fig. 3 - Forward Power Loss Characteristics



Half Cycle Current Pulses (N)

Fig. 5 - Maximum Non-Repetitive Surge Current

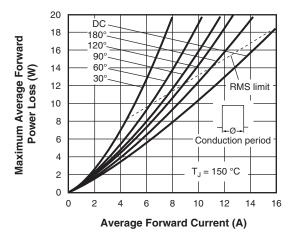


Fig. 4 - Forward Power Loss Characteristics

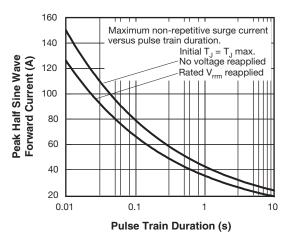
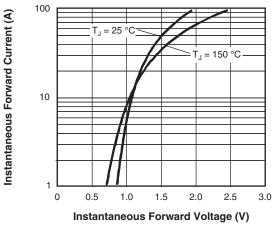
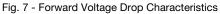


Fig. 6 - Maximum Non-Repetitive Surge Current





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VS-8EWS12SLHM3

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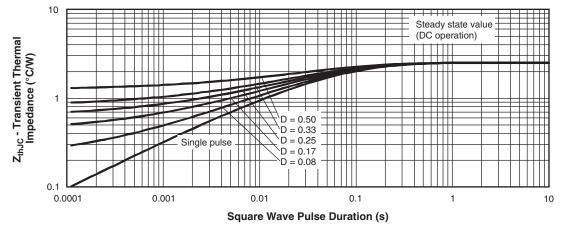


Fig. 8 - Thermal Impedance ZthJC Characteristics

ORDERING INFORMATION TABLE

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Device code	VS-	8	Е	w	s	12	s	L	н	М3
		(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)
		C	C	\odot	C	C	Ċ	C	C	
	1	- Vis	hay Sen	nicondu	ctors pro	oduct				
	2	- Cur	rent rati	ng (8 =	8 A)					
	3	- Circ	cuit conf	iguratio	n:					
		E =	single							
	4	- Pao	kage:							
		W =	= DPAK	(TO-25	2AA)					
	5	- Тур	e of sili	con:						
		S =	standa	rd recov	ery rect	ifier				
	6	- Vol	tage coo	de x 100) = V _{RRN}	л — Г	12 = 12	00 V		
	7	- S=	surface	mounta	able	_				
		- L=	tape an	d reel (I	eft orier	ited). fo	r differe	nt orien	tation c	ontact fa
			-	101 qua		,,				
	10			ntal digit						
				-		omolio	nt and	tormino	tionala	
		IVIS	- nalog	en-nee,	K0H2-0	Jomplia	ni, anu	termina	uons lea	ad (Pb)-f

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-8EWS12SLHM3	3000	3000	13" diameter reel				

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95519			
Part marking information	www.vishay.com/doc?95518			
Packaging information	www.vishay.com/doc?96495			
SPICE model	www.vishay.com/doc?96668			

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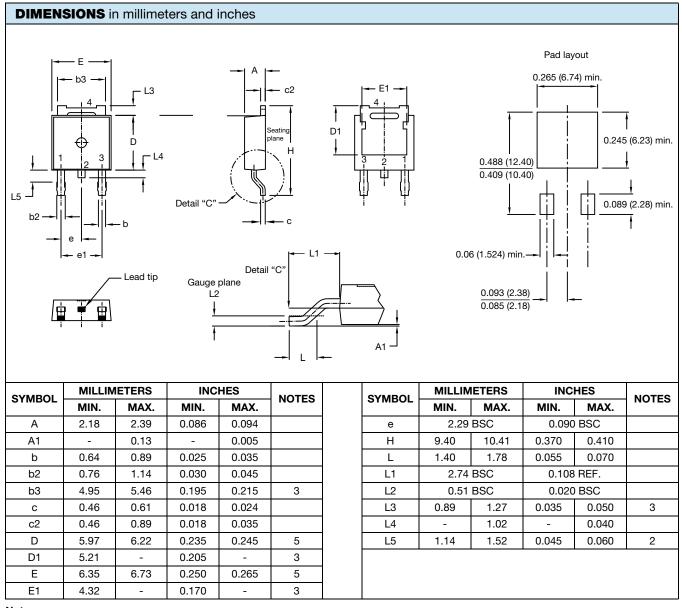
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Outline Dimensions



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DPAK (TO-252AA)



Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

⁽⁴⁾ Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁵⁾ Outline conforms to JEDEC[®] outline TO-252AA

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