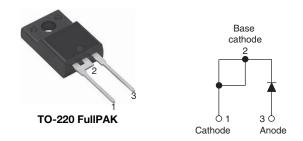
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# VS-30ETH06FP-F3, VS-30ETH06FP-N3

Vishay Semiconductors

## Hyperfast Rectifier, 30 A FRED Pt<sup>®</sup>



PRIMARY CHARACTERISTICS				
Package	TO-220 FullPAK			
I <sub>F(AV)</sub>	30 A			
V <sub>R</sub>	600 V			
V <sub>F</sub> at I <sub>F</sub>	1.34 V			
t <sub>rr</sub> (typ.)	23 ns			
T <sub>J</sub> max.	175 °C			
Circuit configuration	Single			

### **FEATURES**

- Reduced Q<sub>rr</sub> and soft recovery
- 175 °C T<sub>J</sub> maximum
- For PFC CRM/CCM operation
- Fully isolated package (V<sub>INS</sub> = 2500 V<sub>RMS</sub>)
- UL E78996 approved



COMPLIANT

HALOGEN

FREE

- Designed and qualified according to JEDEC<sup>®</sup>-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **DESCRIPTION / APPLICATIONS**

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	V <sub>RRM</sub>		600	V	
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 37 °C	30	٨	
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	220	A	
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C	

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-	
Forward voltage V <sub>F</sub>	I <sub>F</sub> = 30 A	-	2.00	2.60	V	
	$I_F = 30 \text{ A}, T_J = 150 \text{ °C}$	-	1.34	1.75		
		$V_R = V_R$ rated	-	0.3	50	
Reverse leakage current	I <sub>R</sub>	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	60	500	μA
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	33	-	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8	-	nH

Revision: 27-Oct-17



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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 $^{\circ}$ C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time t <sub>rr</sub>		$I_F=1$ A, $dI_F/dt=50$ A/µs, $V_R=30$ V		-	28	35	ns
	+	$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$		-	23	30	
	T <sub>J</sub> = 25 °C		-	31	-		
		T <sub>J</sub> = 125 °C		-	77	-	
Poak recovery ourrent	Peak recovery current	T <sub>J</sub> = 25 °C	l <sub>F</sub> = 30 A dl <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 200 V	-	3.5	-	А
reak recovery current IRRM		T <sub>J</sub> = 125 °C		-	7.7	-	~
Reverse recovery charge Q <sub>rr</sub>	0	T <sub>J</sub> = 25 °C		-	65	-	nC
	T <sub>J</sub> = 125 °C		-	345	-	10	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C
Thermal resistance, junction-to-case per leg	R <sub>thJC</sub>		-	-	2.85	
Thermal resistance, junction-to-ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W
Thermal resistance, case-to-heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.2	-	
Weight			-	2	-	g
Weight			-	0.07	-	oz.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220 FullPAK		30ETH	H06FP	

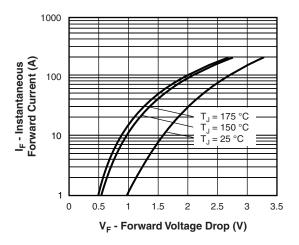
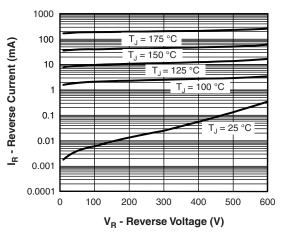
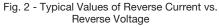


Fig. 1 - Typical Forward Voltage Drop Characteristics







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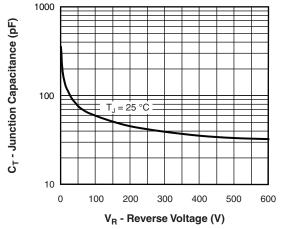
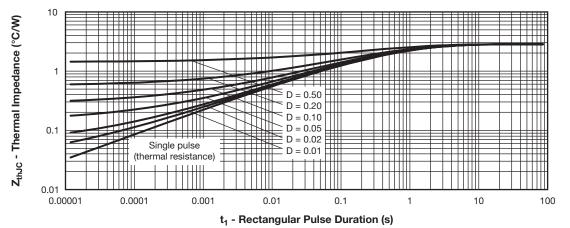
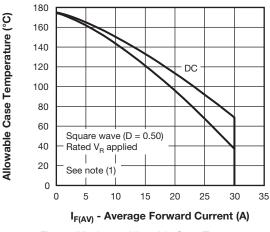
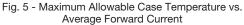


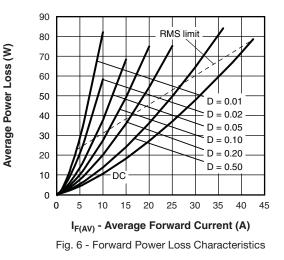
Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage











Revision: 27-Oct-17

3

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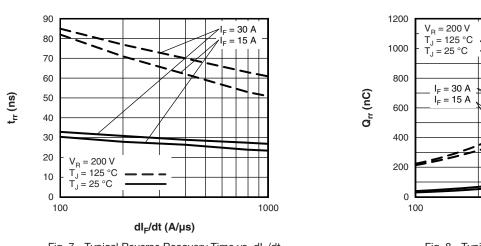
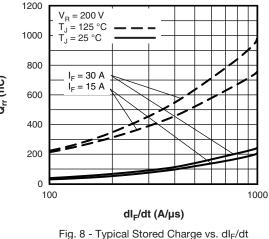


Fig. 7 - Typical Reverse Recovery Time vs. dI<sub>F</sub>/dt

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#### Note

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<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{Rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

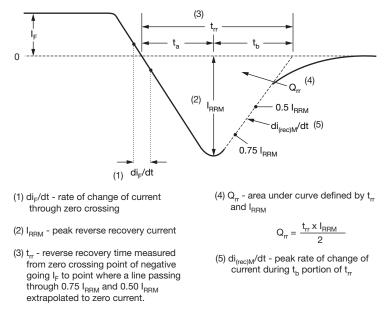
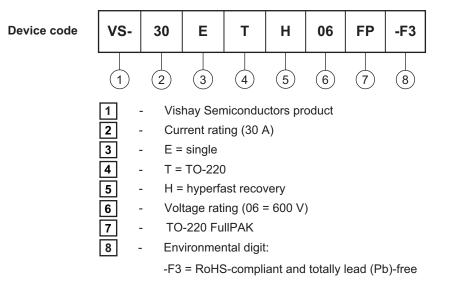


Fig. 9 - Reverse Recovery Waveform and Definitions



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### **ORDERING INFORMATION TABLE**



-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-30ETH06FP-F3	50	1000	Antistatic plastic tube		
VS-30ETH06FP-N3	50	1000	Antistatic plastic tube		

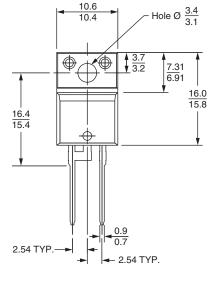
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95005			
Part marking information	www.vishay.com/doc?95440			
SPICE model	www.vishay.com/doc?96440			



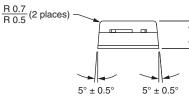
## **Outline Dimensions**

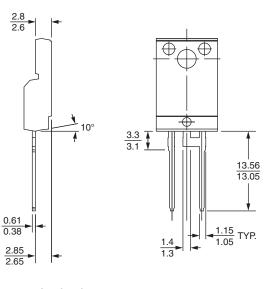
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### **DIMENSIONS** in millimeters



 $\frac{4.8}{4.6}$ 





Lead assignments Diodes 1 + 2 - Cathode 3 - Anode

Conforms to JEDEC outline TO-220 FULL-PAK



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