

TMBS[®]

TO-247AD (TO-3P)

PIN 2

CASE

2 x 30 A

100 V

350 A

0.657 V

150 °C

PIN 1 O

PIN 3 O-

PRIMARY CHARACTERISTICS

I_{F(AV)}

V_{RRM}

I_{FSM}

 V_F at $I_F = 30$ A

T_{.1} max.

Vishay General Semiconductor

Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.456$ V at $I_F = 10$ A

FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses



- High efficiency operation
- ROHS COMPLIANT
- Low thermal resistance
- Solder dip 260 °C, 40 s
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

TYPICAL APPLICATIONS

For use in high frequency inverters, switching power supplies, freewheeling diodes, OR-ing diode, dc-to-dc converters and reverse battery protection.

MECHANICAL DATA

Case: TO-247AD (TO-3P)

Epoxy meets UL 94V-0 flammability rating

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD22-B102

E3 suffix for consumer grade, meets JESD 201 class 1A whisker test

Polarity: As marked Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V60100P	UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	100	V	
Maximum average forward rectified current (Fig. 1)	per device per diode	I _{F(AV)}	60 30	А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	350	А	
Peak repetitive reverse current per diode at $t_p = 2 \ \mu s$, 1 kHz		I _{RRM}	1.0	A	
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T _J , T _{STG}	- 40 to + 150	°C	

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PARAMETER	TEST CO	NDITIONS	SYMBOL	TYP.	MAX.	UNIT
Breakdown voltage	I _R = 1.0 mA	T _J = 25 °C	V _{BR}	100 (minimum)	-	V
Instantaneous forward voltage per diode ⁽¹⁾	I _F = 10 A I _F = 15 A I _F = 30 A	T _J = 25 °C	• V _F	0.518 0.576 0.730	- - 0.79	V
	I _F = 10 A I _F = 15 A I _F = 30 A	T _J = 125 °C		0.456 0.531 0.657	- - 0.70	
Reverse current per diode ⁽²⁾	V _R = 80 V	T _J = 25 °C T _J = 125 °C	- I _R	34.6 9.5	-	μA mA
	V _R = 100 V	T _J = 25 °C T _J = 125 °C		82.0 19.2	800 45	μA mA

Notes:

(1) Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	V60100P	UNIT			
Typical thermal resistance per diode	$R_{ ext{ heta}JC}$	1.5	°C/W			

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V60100P-E3/45	6.12	45	30/tube	Tube	

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

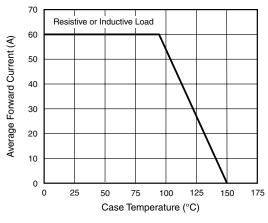


Figure 1. Forward Current Derating Curve

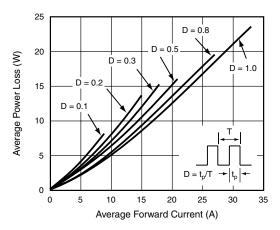


Figure 2. Forward Power Loss Characteristics Per Diode

For technical questions within your region, please contact one of the following: <u>PDD-Americas@vishay.com</u>, <u>PDD-Asia@vishay.com</u>, <u>PDD-Europe@vishay.com</u>



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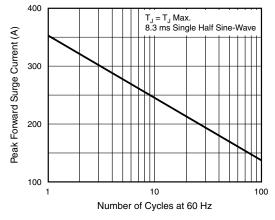


Figure 3. Maximum Non-Repetitive Peak Forward Surge Current Per Diode

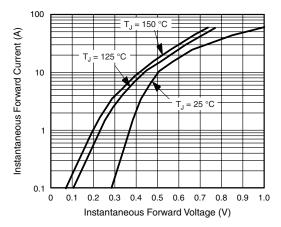


Figure 4. Typical Instantaneous Forward Characteristics Per Diode

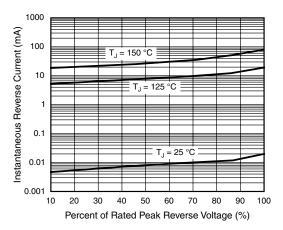


Figure 5. Typical Reverse Characteristics Per Diode

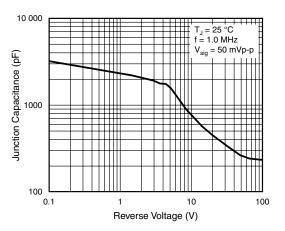


Figure 6. Typical Junction Capacitance Per Diode

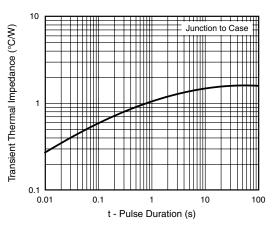
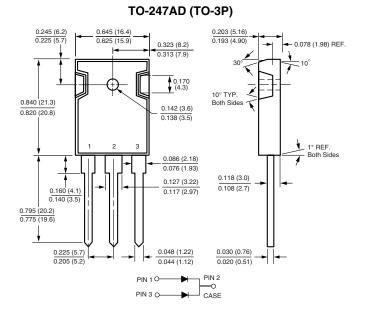


Figure 7. Typical Transient Thermal Impedance Per Diode



PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



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