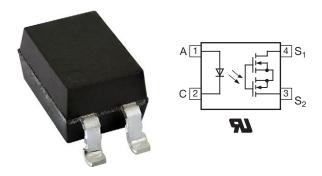
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1 Form A Solid-State Relay (Normally Open)



DESCRIPTION

The LH1546AD is a single channel solid state relay in a 4 pin SMD package. It is a SPST normally open switch (1 Form A) that replaces electromechanical relays in many applications. It is constructed using a GaAlAs LED for actuation control and MOSFET switches for the output. In addition, it employs current-limiting circuitry to provide overvoltage protection.

FEATURES

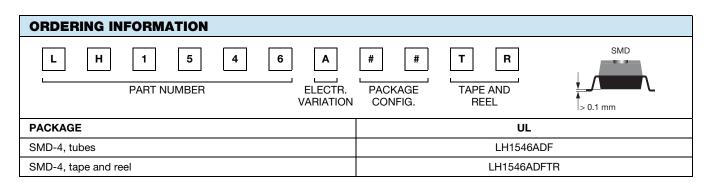
- Isolation test voltage 5300 V_{RMS}
- Typical R_{ON} 22 Ω
- Load voltage 350 V
- Load current 120 mA
- Clean bounce free switching
- Current limit protection
- Low power consumption
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- · General telecom switching
- Metering
- Security equipment
- Instrumentation
- Industrial controls
- Battery management systems
- Automatic test equipment

AGENCY APPROVALS

• UL1577, file no. E52744





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ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)					
PARAMETER	CONDITION	SYMBOL	VALUE	UNIT	
INPUT					
IRED continuous forward current		١ _F	50	mA	
IRED reverse voltage		V _R	5	V	
Input power dissipation		P _{diss}	80	mW	
OUTPUT					
DC or peak AC load voltage		VL	350	V	
Continuous DC load current at 25 °C, bidirectional		۱L	120	mA	
SSR output power dissipation		P _{diss}	550	mW	
SSR					
Ambient temperature range		T _{amb}	-40 to +85	°C	
Storage temperature range		T _{stg}	-40 to +150	°C	
Soldering temperature	t = 10 s max.	T _{sld}	260	°C	

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
IRED forward current, switch turn-on	I _L = 100 mA, t = 10 ms	I _{Fon}	-	0.25	2	mA
IRED forward current, switch turn-off	$V_L = \pm 350 \text{ V}, \text{ I}_L < 1 \ \mu\text{A}$	I _{Foff}	0.05	0.15	-	mA
IRED forward voltage	I _F = 10 mA	V _F	-	1.36	1.45	V
IRED reverse current	$V_R = 5 V$	I _R	-	-	10	μA
OUTPUT						
On-resistance, AC/DC: pin 3 (\pm) to 4 (\pm)	$I_{\rm F} = 5$ mA, $I_{\rm L} = 50$ mA	R _{ON}	-	22	35	Ω
Off-resistance	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	R _{OFF}	0.5	5000	-	GΩ
	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	Ι _Ο	-	< 1	200	nA
Off-state leakage current	$I_F = 0 \text{ mA}, V_L = \pm 350 \text{ V}$	Ι _Ο	-	6	500	nA
	$I_F = 0 \text{ mA}, V_L = 1 \text{ V}, 1 \text{ MHz}$	Co	-	39	-	pF
Output capacitance pin 3 to 4	$I_F = 0$ mA, $V_L = 50$ V, 1 MHz	C _O	-	6	-	pF
Current limit AC/DC	$I_F = 5 \text{ mA}, t = 5 \text{ ms}, V_L = \pm 6 \text{ V}$	l _{limit}	170	300	450	mA
TRANSFER						
Capacitance (input to output)	V _{IO} = 1 V	C _{IO}	-	1	-	pF

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

SWITCHING CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	I _F = 5 mA, I _L = 50 mA	t _{on}	-	0.13	3	ms
Turn-off time	$I_{\rm F} = 5$ mA, $I_{\rm L} = 50$ mA	t _{off}	-	0.05	3	ms

Rev. 1.8, 05-Jul-2018

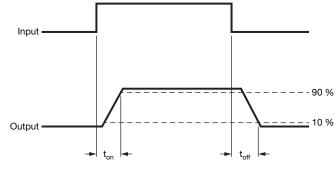
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Document Number: 83836

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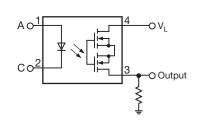
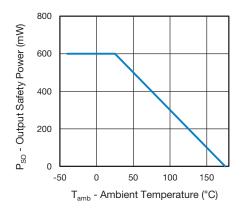


Fig. 1 - Timing Schematic

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		40 / 100 / 21	
Pollution degree	According to DIN VDE 0109		2	
Comparative tracking index	Insulation group IIIa	CTI	175	
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V _{ISO}	5300	V _{RMS}
Maximum transient isolation voltage	According to DIN EN 60747-5-5	VIOTM	8000	V _{peak}
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	VIORM	890	V _{peak}
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹²	Ω
	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω
Output safety power		P _{SO}	600	mW
Input safety current		I _{SI}	240	mA
Safety temperature		Τ _S	175	°C
Creepage distance			≥ 8	mm
Clearance distance			≥ 8	mm
Insulation thickness		DTI	≥ 0.4	mm
Input to output test voltage, method B	$V_{IORM} x 1.875 = V_{PR}$, 100 % production test with t _M = 1 s, partial discharge < 5 pC	V _{PR}	1669	V _{peak}
Input to output test voltage, method A	$V_{IORM} x 1.6 = V_{PR}$, 100 % sample test with $t_M = 10$ s, partial discharge < 5 pC	V _{PR}	1424	V _{peak}

Note

As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with • the safety ratings shall be ensured by means of protective circuits.





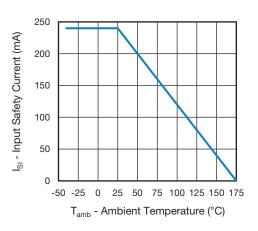


Fig. 3 - Safety Input Current vs. Ambient Temperature

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TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

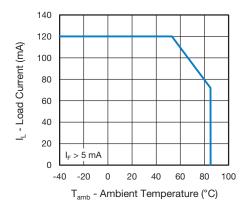


Fig. 4 - Maximum Load Current vs. Ambient Temperature

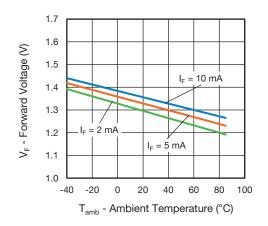


Fig. 5 - Forward Voltage vs. Ambient Temperature

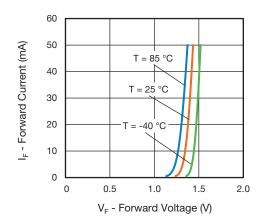


Fig. 6 - Forward Current vs. Forward Voltage

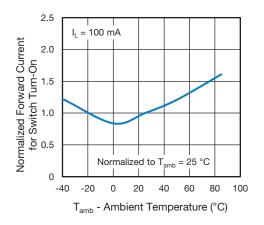


Fig. 7 - Normalized Forward Current for Switch Turn-On vs. Ambient Temperature

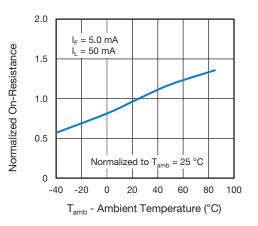


Fig. 8 - Normalized On-Resistance vs. Ambient Temperature

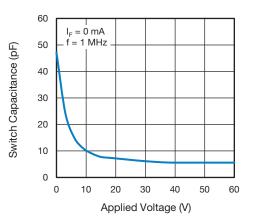


Fig. 9 - Switch Capacitance vs. Applied Voltage

4

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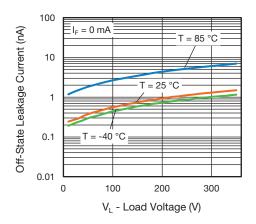


Fig. 10 - Off-State Leakage Current vs. Load Voltage

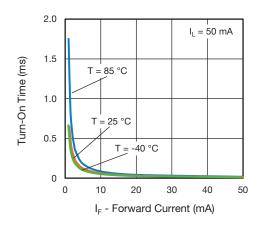


Fig. 11 - Turn-On Time vs. Forward Current

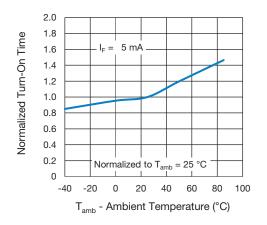


Fig. 12 - Normalized Turn-On Time vs. Ambient Temperature

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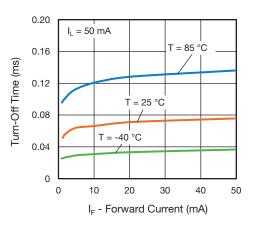


Fig. 13 - Turn-Off Time vs. Forward Current

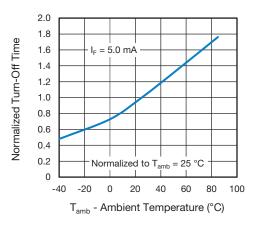


Fig. 14 - Normalized Turn-Off Time vs. Ambient Temperature

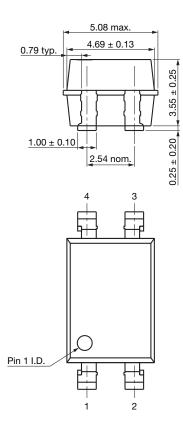
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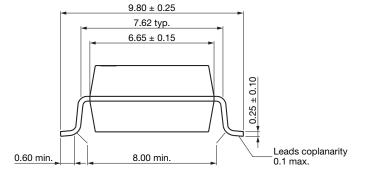


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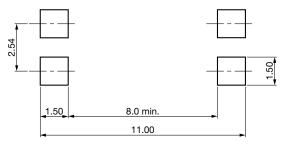
PACKAGE DIMENSIONS in millimeters

SMD-4

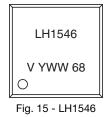




Recommended footprint



PACKAGE MARKING (example)



Note

• Tape and reel suffix (TR) is not part of the package marking



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PACKING INFORMATION (in millimeters)

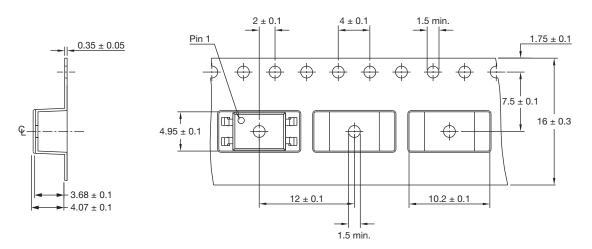
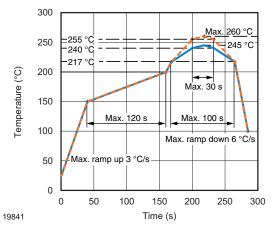


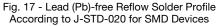
Fig. 16 - Tape and Reel Packing

TAPE AND REEL PACKING		
ТҮРЕ	UNITS/REEL	
SMD-4	1000	

TUBE PACKING			
ТҮРЕ	UNITS/TUBE	TUBES/BOX	UNITS/BOX
SMD-4	100	20	2000

SOLDER PROFILES





HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited Conditions: $T_{amb} < 30$ °C, RH < 60 % Moisture sensitivity level 1, according to J-STD-020



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Footprint and Schematic Information for LH1546

The footprint and schematic symbols for the following parts can be accessed using the associated links. They are available in Eagle, Altium, KiCad, OrCAD / Allegro, Pulsonix, and PADS.

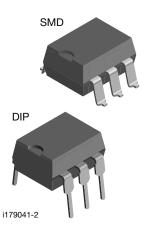
Note that the 3D models for these parts can be found on the Vishay product page.

PART NUMBER	FOOTPRINT / SCHEMATIC
LH1546AAB	www.snapeda.com/parts/LH1546AAB/Vishay/view-part
LH1546AABTR	www.snapeda.com/parts/LH1546AABTR/Vishay/view-part
LH1546AD	www.snapeda.com/parts/LH1546AD/Vishay/view-part
LH1546ADF	www.snapeda.com/parts/LH1546ADF/Vishay/view-part
LH1546ADFTR	www.snapeda.com/parts/LH1546ADFTR/Vishay/view-part
LH1546AEF	www.snapeda.com/parts/LH1546AEF/Vishay/view-part
LH1546AEFT2	www.snapeda.com/parts/LH1546AEFT2/Vishay/view-part
LH1546AEFTR	www.snapeda.com/parts/LH1546AEFTR/Vishay/view-part
LH1546AT	www.snapeda.com/parts/LH1546AT/Vishay/view-part

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