FREE

Vishay Semiconductors

High Performance Schottky Rectifier, 3.0 A



Cathode	Anode
0	0

SMC (DO-214AB)

PRIMARY CHARACTERISTICS				
I _{F(AV)}	3.0 A			
V _R	60 V			
V _F at I _F	0.52 V			
I _{RM}	20 mA at 125 °C			
T _J max.	150 °C			
E _{AS}	5.0 mJ			
Package	SMC (DO-214AB)			
Circuit configuration	Single			

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FEATURES

- · Low forward voltage drop
- Guard ring for enhanced ruggedness and long RoHS term reliability COMPLIANT HALOGEN
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-30BQ060-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	3.0	А			
V _{RRM}		60	V			
I _{FSM}	t _p = 5 μs sine	1200	А			
V _F	3.0 A _{pk} , T _J = 125 °C	0.52	V			
TJ	Range	-55 to +150	°C			

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-30BQ060-M3	UNITS	
Maximum DC reverse voltage	V _R	60	V	
Maximum working peak reverse voltage	V _{RWM}	80	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current		50 % duty cycle at $T_L = 123 \text{ °C}$, rectangular waveform	3.0	
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 113 °C, rectangular waveform		4.0	
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated	1200	А
non-repetitive surge current at T _C = 25 °C	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	130	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.0 A, L = 10 mH		5.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zer Frequency limited by T _J maxim	•	1.0	А

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS		UNITS
		3 A	т ос «О	0.58	V
Maximum forward voltage drop	V _{EM} ⁽¹⁾	6 A	- T _J = 25 °C	0.76	
Maximum forward voltage drop	VFM ⁽¹⁾	3 A	T _J = 125 °C	0.52	
		6 A		0.66	
Maximum reverse leakage ourrent	I _{RM} -	T _J = 25 °C		0.5	mA
Maximum reverse leakage current		T _J = 125 °C	V _R = Rated V _R	20	
Maximum junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to1 MHz), 25 °C		180	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		3.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}$ Pulse width = 300 $\mu s,$ duty cycle = 2 %

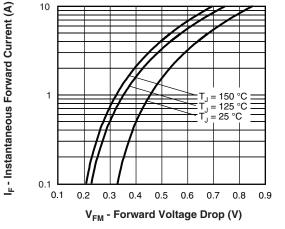
THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature range	T _J ⁽¹⁾		-55 to +150	°C	
Maximum storage temperature range	T _{Stg}		-55 10 +150	ч С	
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾		12		
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	46	°C/W	
Approvimeto weight			0.24	g	
Approximate weight			0.008	oz.	
Marking device		Case style SMC (DO-214AB) 3H		4	

Notes

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$ (1)

(2) Mounted 1" square PCB

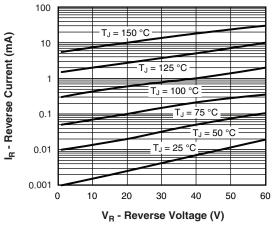
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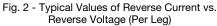


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Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)





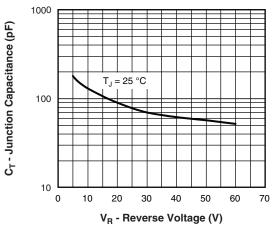


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

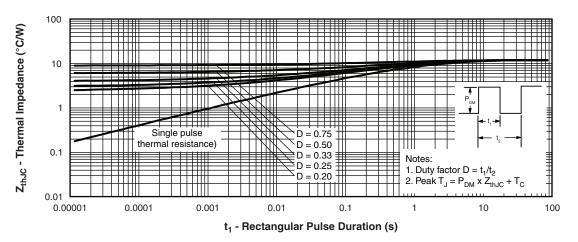


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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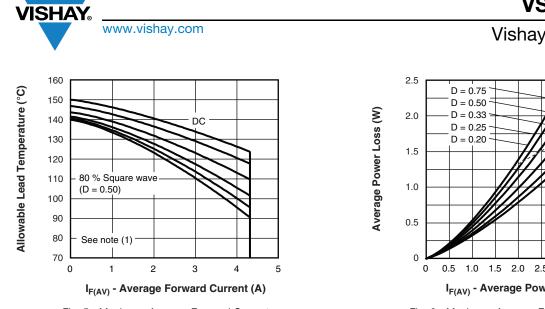


Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature

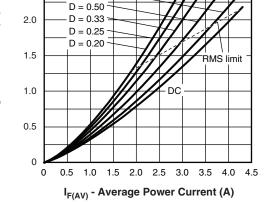


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

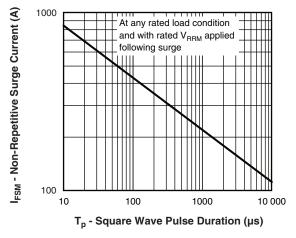


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$; $I_R at V_{R1} = 80 \%$ rated V_R

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ORDERING INFORMATION TABLE

vice code	VS-	30	в	Q	060	-M3
		2	3	4	5	6
	1 -	· Visł	nay Sem	niconduo	ctors pro	oduct
	2 -	Cur	rent rati	ng		
	3 -	- В =	SMC			
	4 -	Q =	Schottk	ky "Q" se	eries	
	5 -	· Volt	tage rati	ng (060	= 60 V))
	6 - Environmental digit:					
		-M3	= haloc	on_froo	RoHS.	complia

-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-30BQ060-M3/9AT	9AT	3500	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS		
Dimensions	www.vishay.com/doc?95402	
Part marking information	www.vishay.com/doc?95403	
Packaging information	www.vishay.com/doc?95404	

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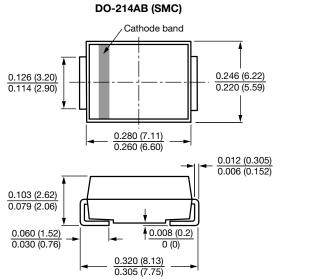


Outline Dimensions

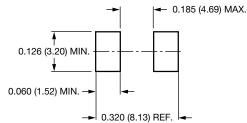
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SMC

DIMENSIONS in inches (millimeters)



Mounting Pad Layout



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