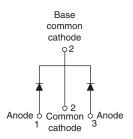


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High Performance Schottky Rectifier, 2 x 6 A

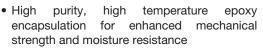




PRODUCT SUMMARY					
Package	TO-220AB				
I _{F(AV)}	2 x 6 A				
V_R	35 V, 40 V, 45 V				
V _F at I _F	0.53 V				
I _{RM} max.	7 mA at 125 °C				
T _J max.	175 °C				
Diode variation	Common cathode				
E _{AS}	8 mJ				

FEATURES

- 175 °C T_J operation
- Center tap TO-220 package
- · Low forward voltage drop
- · High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-12CTQ... center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UN				
I _{F(AV)}	Rectangular waveform	12	Α			
V_{RRM}	Range	35 to 45	V			
I _{FSM}	$t_p = 5 \mu s sine$	690	Α			
V _F	6 A _{pk} , T _J = 125 °C (per leg)	0.53	V			
T _J	Range	-55 to +175	°C			

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS- 12CTQ035PbF	VS- 12CTQ035-N3	VS- 12CTQ040PbF	VS- 12CTQ040-N3	VS- 12CTQ045PbF	VS- 12CTQ045-N3	UNITS
Maximum DC reverse voltage	V_{R}							
Maximum working peak reverse voltage	V _{RWM}	35	35	40	40	45	45	V

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward per leg		50 % duty cycle at T _C = 160 °C, rectangular waveform		6	- A	
current. See fig. 5 per device	I _{F(AV)}			12		
Maximum peak one cycle non-repetitive	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load	690	А	
surge current per leg. See fig. 7		10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	140		
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 ^{\circ}\text{C}$, $I_{AS} = 1.20 \text{A}$, $L = 11.10 \text{mH}$		8	mJ	
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.20	А	



VS-12CTQ...PbF Series, VS-12CTQ...-N3 Series

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
	V _{FM} ⁽¹⁾	6 A	T _J = 25 °C	0.60	V	
Maximum forward voltage drop per leg		12 A		0.73		
See fig. 1		6 A	T 405.00	0.53		
		12 A	T _J = 125 °C	0.64		
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.8	- mA	
See fig. 2		T _J = 125 °C		7.0		
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.35	V	
Forward slope resistance	r _t			18.23	mΩ	
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		400	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 8		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/ _I		V/µs		

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +175	°C	
Maximum thermal resistance, junction to case per leg		D	DC operation See fig. 4	3.50	°C/W	
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	1.75		
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50		
A no version at a consistat				2	g	
Approximate weight				0.07	OZ.	
Manustina taunus	minimum			6 (5)	kgf · cm	
Mounting torque maximum				12 (10)	(lbf \cdot in)	
				12CT	Q035	
Marking device			Case style TO-220AB	12CT	Q040	
				12CT	Q045	



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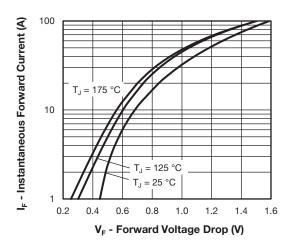


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

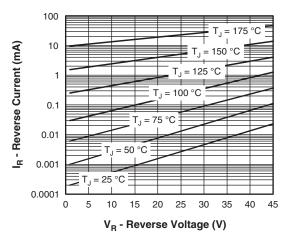


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

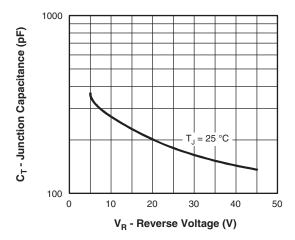


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

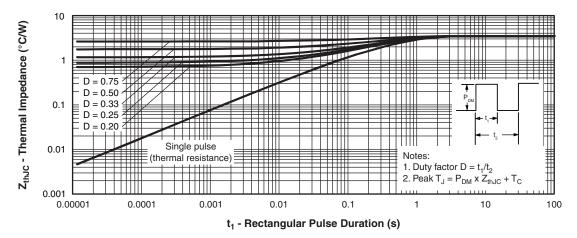


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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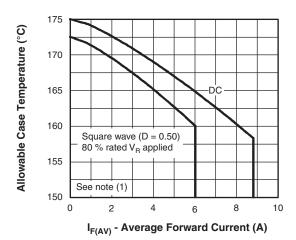


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

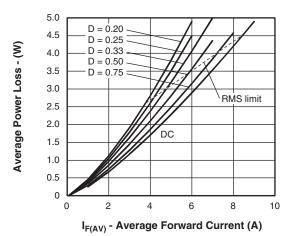


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

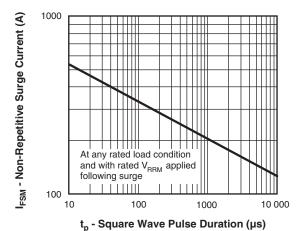


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

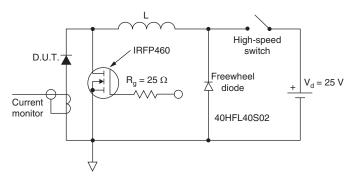


Fig. 8 - Unclamped Inductive Test Circuit

Note

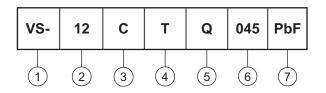
 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (12 = 12 A)

3 - Circuit configuration:

C = Common cathode

4 - Package:

T = TO-220

5 - Schottky "Q" series

035 = 35 V 040 = 40 V

6 - Voltage ratings

045 = 45 V

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-12CTQ035PbF	50	1000	Antistatic plastic tube			
VS-12CTQ035-N3	50	1000	Antistatic plastic tube			
VS-12CTQ040PbF	50	1000	Antistatic plastic tube			
VS-12CTQ040-N3	50	1000	Antistatic plastic tube			
VS-12CTQ045PbF	50	1000	Antistatic plastic tube			
VS-12CTQ045-N3	50	1000	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95222</u>				
Dout moulting information	TO-220AB PbF	www.vishay.com/doc?95225		
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028		
SPICE model		www.vishay.com/doc?95629		



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