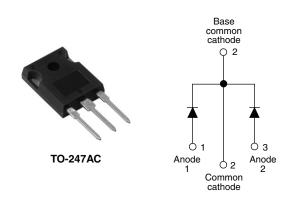
Vishay High Power Products

HEXFRED[®] Ultrafast Soft Recovery Diode, 2 x 6 A



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PRODUCT SUMMARY				
V _R	1200 V			
V _F at 6 A at 25 °C	3.0 V			
I _{F(AV)}	2 x 6 A			
t _{rr} (typical)	26 ns			
T _J (maximum)	150 °C			
Q _{rr} (typical)	116 nC			
dI _{(rec)M} /dt (typical) at 125 °C	100 A/µs			
I _{RRM} (typical)	4.4 A			

FEATURES

- Ultrafast recovery
- Ultrasoft recovery
- Very low I_{RRM}
- Very low Q_{rr}
- Specified at operating conditions
- Designed and qualified for industrial level

BENEFITS

- · Reduced RFI and EMI
- · Reduced power loss in diode and switching transistor
- Higher frequency operation
- Reduced snubbing
- Reduced parts count

DESCRIPTION

HFA12PA120C is a state of the art center tap ultrafast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. The HFA12PA120C has basic ratings of 1200 V and 6 A per leg continuous current. In addition to ultrafast recovery time, the HEXFRED® product line features extremely low values of peak recovery current (I_{RRM}) and does not exhibit any tendency to "snap-off" during the tb portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED HFA12PA120C is ideally suited for applications in power supplies and power conversion systems (such as inverters, converters, UPS systems, and power factor correction circuits), motor drives, and many other similar applications where high speed, high efficiency is needed.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Cathode to anode voltage	V _R		1200	V	
Maximum continuous forward currentper leg	l F	T _C = 100 °C	6		
per device			12	А	
Single pulse forward current	I _{FSM}		80	A	
Maximum repetitive forward current	I _{FRM}		24		
Movimum power dissinction	P _D	T _C = 25 °C	62.5	W	
Maximum power dissipation		T _C = 100 °C	25		
Operating junction and storage temperature range	T _J , T _{Stg}		- 55 to + 150	°C	

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ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA		-	-	
Maximum forward voltage		I _F = 6 A	-	2.7	3.0	v
	V _{FM}	I _F = 12 A	-	3.5	3.9	
		I _F = 6 A, T _J = 125 °C	-	2.4	2.8	
Maximum reverse		$V_{R} = V_{R}$ rated	-	0.26	5.0	
leakage current	IRM	T_J = 125 °C, V_R = 0.8 x V_R rated	-	110	500	μΑ
Junction capacitance	CT	V _R = 200 V	-	9.0	14	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body - 8.0 -		nH		

DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
	t _{rr}	I _F = 1.0 A, dI _F /dt = 200 A/μs, V _R = 30 V		-	26	-	
Reverse recovery time	t _{rr1}	T _J = 25 °C	I _F = 6 A dI _F /dt = 200 A/μs V _R = 200 V	-	53	80	ns
	t _{rr2}	T _J = 125 °C		-	87	130	
Peak recovery current	I _{RRM1}	T _J = 25 °C		-	4.4	8.0	A nC
	I _{RRM2}	T _J = 125 °C		-	5.0	9.0	
Reverse recovery charge	Q _{rr1}	T _J = 25 °C		-	116	320	
	Q _{rr2}	T _J = 125 °C		-	233	585	
Peak rate of fall of recovery current during $t_{\mbox{\scriptsize b}}$	dl _{(rec)M} /dt1	T _J = 25 °C		-	180	-	A/µs
	dl _{(rec)M} /dt2	T _J = 125 °C		-	100	-	πµs

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Lead temperature	T _{lead}	0.063" from case (1.6 mm) for 10 s	-	-	300	°C
Thermal resistance, junction to case	R _{thJC}		-	-	2.0	
Thermal resistance, junction to ambient	R _{thJA}	thJA Typical socket mount -		-	80	K/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.50	-	
Weight			-	2.0	-	g
weight			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)
Marking device		Case style TO-247AC (JEDEC)	HFA12PA120C		•	



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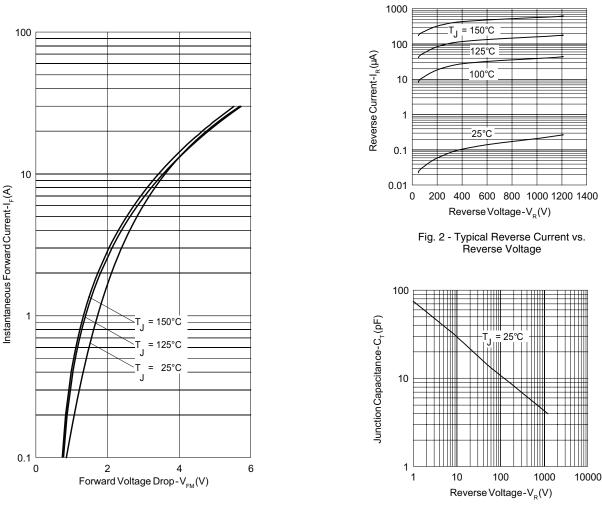
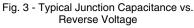
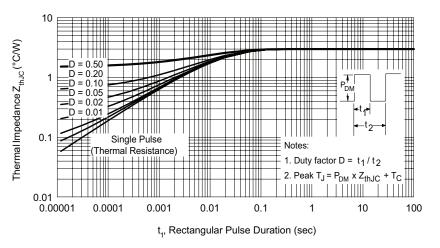
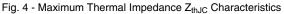


Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current







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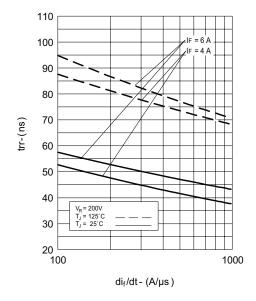
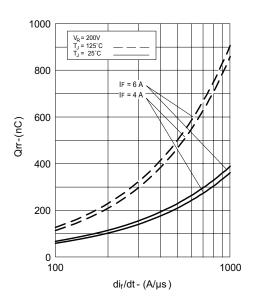


Fig. 5 - Typical Reverse Recovery Time vs. dI_F/dt



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Fig. 7 - Typical Stored Charge vs. dI_F/dt

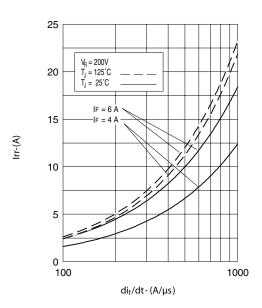
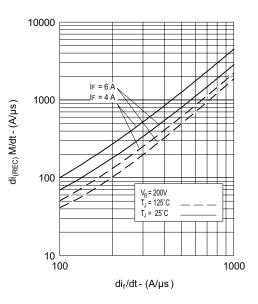
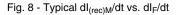


Fig. 6 - Typical Recovery Current vs. dl_F/dt









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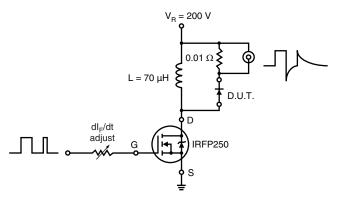


Fig. 9 - Reverse Recovery Parameter Test Circuit

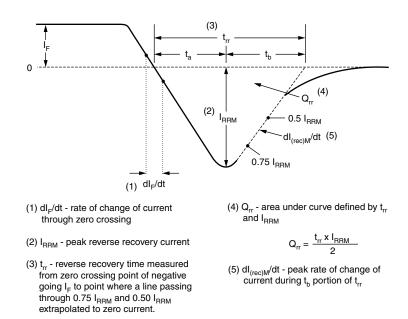


Fig. 10 - Reverse Recovery Waveform and Definitions

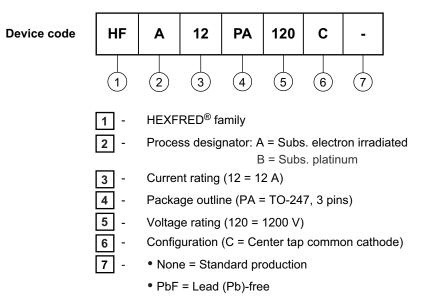
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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95223				
Part marking information	http://www.vishay.com/doc?95226			



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