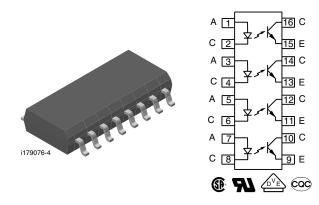
Vishay Semiconductors

SFH6916





DESCRIPTION

The SFH6916 has a GaAs infrared emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a 16 pin 50 mil lead pitch miniflat package. It features a high current transfer ratio, low coupling capacitance, and high isolation voltage.

The coupling devices are designed for signal transmission between two electrically separated circuits.

FEATURES

- SOP (small outline package)
- Isolation test voltage, 3750 V_{BMS} (1.0 s)
- High collector emitter voltage, V_{CEO} = 70 V
- Low saturation voltage
- Fast switching times
- Temperature stable
- Low coupling capacitance
- End stackable, 0.050" (1.27 mm) spacing
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

AGENCY APPROVALS

- UL1577, file no. E52744 system code U
- CSA 22.2 bulletin 5A, double protection
- DIN EN 60747-5-5 (VDE 0884)
- CQC GB4943.1-2011 (suitable for installation altitude below 2000 m)

ORDERING INFORMATION	
S F H	6 9 1 6 SOP-16
	<u>ب ر</u>
AGENCY CERTIFIED/PACKAGE	CTR (%)
UL, cUL	50 to 300
SOP-16, quad channel	SFH6916

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
Reverse voltage		V _R	6	V		
DC forward current		I _F	50	mA		
Surge forward current	$t_p \le 10 \ \mu s$	I _{FSM}	2.5	А		
Total power dissipation		P _{diss}	80	mW		
OUTPUT						
Collector emitter voltage		V _{CE}	70	V		
Emitter collector voltage		V _{EC}	7	V		
		Ι _C	50	mA		
Collector current	t _p = 1.0 ms	Ι _C	100	mA		
Total power dissipation per channel		P _{diss}	150	mW		



RoHS

COMPLIANT

HALOGEN

<u>GREEN</u>

(5-2008)



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	VALUE	UNIT				
COUPLER							
Isolation test voltage between emitter and detector	t = 1.0 s	V _{ISO}	3750	V _{RMS}			
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹²	Ω			
	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω			
Storage temperature range		T _{stg}	- 55 to + 125	°C			
Ambient temperature range		T _{amb}	- 55 to + 100	°C			
Junction temperature		Тj	100	°C			
Soldering temperature ⁽¹⁾	max. 10 s dip soldering distance to seating plane ≥ 1.5 mm		260	°C			
Total power dissipation		P _{tot}	700	mW			

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

⁽¹⁾ Refer to reflow profile for soldering conditions for surface mounted devices.

ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
Forward voltage	I _F = 5 mA	V _F		1.15	1.4	V	
Reverse current	V _R = 6 V	I _R		0.01	10	μA	
Capacitance	Co	C _O		14		pF	
Thermal resistance		R _{thja}		1000		K/W	
OUTPUT							
Collector emitter leakage current	$V_{CE} = 20 V$	I _{CEO}			100	nA	
Collector emitter capacitance	$V_{CE} = 5 V$, f = 1 MHz	C _{CE}		2.8		pF	
Thermal resistance		R _{thja}		500		K/W	
COUPLER							
Collector emitter saturation voltage	I _F = 20 mA, I _C = 1 mA	V _{CEsat}		0.1	0.4	V	
Coupling capacitance	f = 1 MHz	C _C		1		pF	

Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

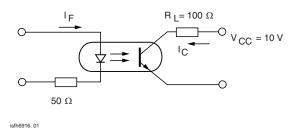
CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	PARAMETER TEST CONDITION SYMBOL MIN. TYP. MAX. UNIT						
Current transfer ratio	$I_{\rm F} = 5 {\rm mA}, V_{\rm CC} = 5 {\rm V}$	CTR	50		300	%	

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SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
NON-SATURATED	NON-SATURATED							
Rise time	I_{C} = 2 mA, V_{CC} = 10 V, R_{L} = 100 Ω	t _r		4		μs		
Fall time	I_{C} = 2 mA, V_{CC} = 10 V, R_{L} = 100 Ω	t _f		3		μs		
Turn-on time	I_C = 2 mA, V_{CC} = 10 V, R_L = 100 Ω	t _{on}		5		μs		
Turn-off time	I_{C} = 2 mA, V_{CC} = 10 V, R_{L} = 100 Ω	t _{off}		4		μs		
SATURATED								
Rise time	I_F = 16 mA, V_{CC} = 5 V, R_L = 1.9 $k\Omega$	tr		15		μs		
Fall time	I_F = 16 mA, V_{CC} = 5 V, R_L = 1.9 k Ω	t _f		0.5		μs		
Turn-on time	I_F = 16 mA, V_{CC} = 5 V, R_L = 1.9 $k\Omega$	t _{on}		1		μs		
Turn-off time	I_F = 16 mA, V_{CC} = 5 V, R_L = 1.9 $k\Omega$	t _{off}		30		μs		



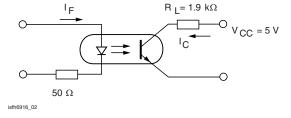


Fig. 1 - Switching Operation (without Saturation)

Fig. 2 - Switching Operation (with Saturation)

SAFETY AND INSULATION RATINGS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Climatic classification (according to IEC 68 part 1)				55/100/21				
Comparative tracking index		CTI	175		399			
Peak transient overvoltage		V _{IOTM}	6000			V		
Peak insulation voltage		V _{IORM}	707			V		
Safety rating - power output		P _{SO}			350	mW		
Safety rating - input current		I _{SI}			150	mA		
Safety rating - temperature		T _{SI}			175	°C		
Creepage distance			5			mm		
Clearance distance			5			mm		

Note

• As per IEC 60747-5-2, § 7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.



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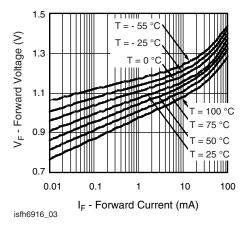
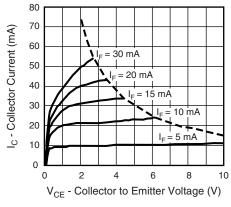
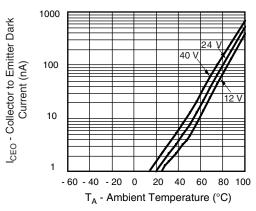


Fig. 3 - Diode Forward Voltage vs. Forward Current



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Fig. 4 - Collector Current vs. Collector Emitter Voltage



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Fig. 5 - Collector to Emitter Dark Current vs. Ambient Temperature

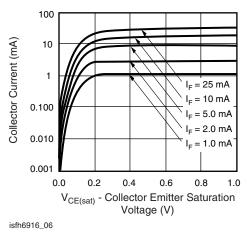


Fig. 6 - Collector Current vs. Collector Emitter Saturation Voltage

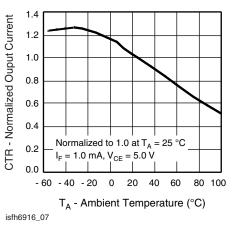


Fig. 7 - Normalized Output Current vs. Ambient Temperature

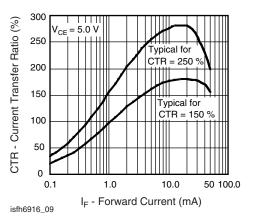
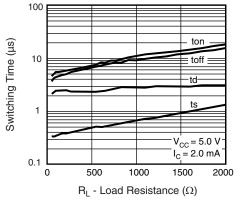


Fig. 8 - Current Transfer Ratio vs. Forward Current

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Fig. 9 - Switching Time vs. Load Resistance

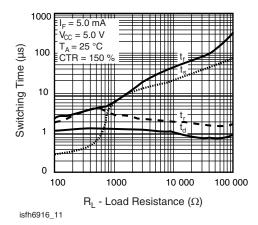


Fig. 10 - Switching Time vs. Load Resistance

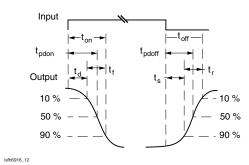
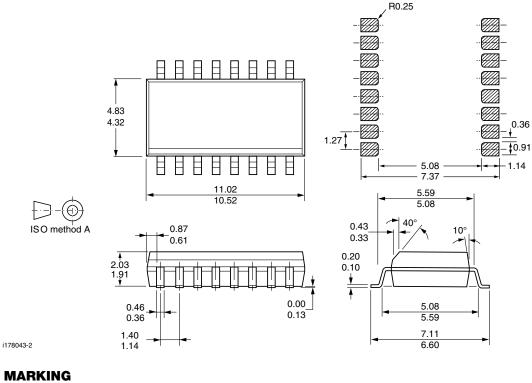


Fig. 11 - Switching Time Measurement



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PACKAGE DIMENSIONS in millimeters



PACKAGE MARKING





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