VS-E4PU6006LHN3

Vishay Semiconductors



Ultrafast Soft Recovery Diode, 60 A FRED Pt[®] Gen 4



PRIMARY CHARACTERISTICS							
I _{F(AV)}	60 A						
V _R	600 V						
V _F at I _F	1.29 V						
t _{rr} typ.	See Recovery table						
T _J max.	175 °C						
Package	TO-247AD 2L						
Circuit configuration	Single						

FEATURES

- Gen 4 FRED Pt[®] technology
- Low I_{RRM} and reverse recovery charge
- Very low forward voltage drop
- Polymide passivated chip for high reliability standard
- 175 °C operating junction temperature
- AEC-Q101 qualified, meets JESD 201 class 1A whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

Gen 4 Fred technology, state of the art, ultralow V_F, soft switching optimized for Discontinuous (Critical) Mode (DCM) and IGBT F/W diode.

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS				
Peak repetitive reverse voltage	V _{RRM}		600	V				
Average rectified forward current	I _{F(AV)}	T _C = 116 °C	60	٨				
Non-repetitive peak surge current	I _{FSM}	T_C = 25 °C, t_p = 8.3 ms half sine wave	450	A				
Operating junction and storage temperature	T _J , T _{Stg}		-55 to +175	°C				

ELECTRICAL SPECIFICATIONS (T_J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	600	-	-				
Forward voltage		I _F = 30 A	-	1.4	-				
		I _F = 60 A	-	1.46	1.7	v			
	V _F	I _F = 30 A, T _J = 125 °C	-	1.26	-				
		I _F = 60 A, T _J = 125 °C	-	1.33	-				
		I _F = 30 A, T _J = 150 °C	-	1.22	-]			
		I _F = 60 A, T _J = 150 °C	-	1.29	-				
	I _R	V _R = V _R rated	-	-	50				
Reverse leakage current		$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	-	500	μA			
Junction capacitance	CT	V _R = 600 V	-	30	-	pF			

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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST C	ONDITIONS	MIN.	TYP.	MAX.	UNITS			
Poverse recovery time	t _{rr}	$T_J = 25 \ ^{\circ}C$		-	74	-	ns			
Reverse recovery time		T _J = 125 °C	I _F = 60 A dI _F /dt = 1000 A/μs V _R = 400 V	-	105	-				
De als ve a come a summe ret	I _{RRM}	T _J = 25 °C		-	31	-	A			
Peak recovery current		T _J = 125 °C		-	50	-				
Reverse recovery charge	0	T _J = 25 °C	VR - 400 V	-	1530	-	nC			
	Q _{rr}	T _J = 125 °C		-	3520	-	nc			

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Thermal resistance, junction to case	R _{thJC}		-	-	0.6	°C/W				
Thermal resistance, case to heat sink	R _{thCS}		-	0.25	-					
147-1-1-1			-	6.0	-	g				
Weight			-	0.21	-	oz.				
Mounting torque			6.0		12	kgf · cm				
			(5)	-	(20)	(lbf \cdot in)				
Marking device		Case style TO-247AD 2L	E4PU6006LH							

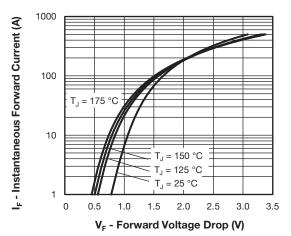


Fig. 1 - Typical Forward Voltage Drop Characteristics

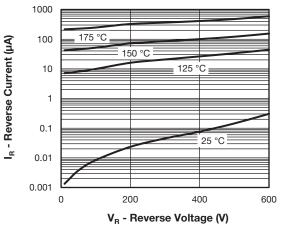
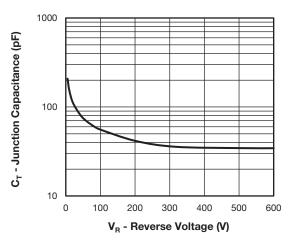


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

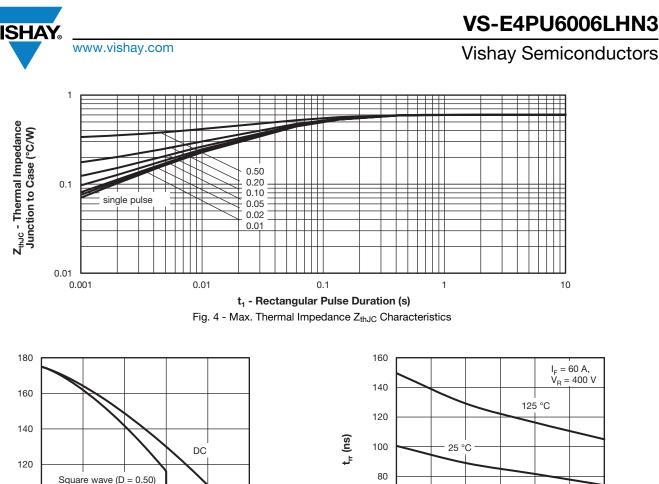




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100 80 0

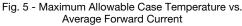
Allowable Case Temperature (°C)

I_{F(AV)} - Average Forward Current (A)

40

80 % rated V_B applied

20



60

80

100

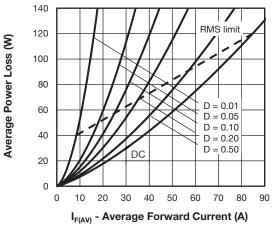


Fig. 6 - Forward Power Loss Characteristics

Note

Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$; (1)

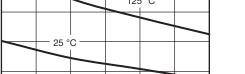
 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, x \, \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{Fig.5}) \\ \mathsf{P}_{\mathsf{d}\mathsf{R}\mathsf{EV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R}1} \, x \, \mathsf{I}_{\mathsf{R}} \ (1 - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

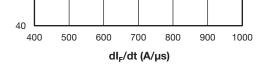
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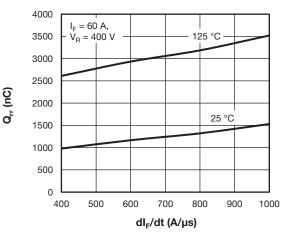
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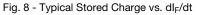




60

Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt





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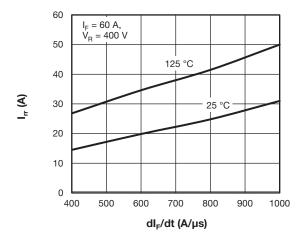


Fig. 9 - Typical Reverse Current vs. dl_F/dt

ORDERING INFORMATION TABLE

Device code	VS-	Е	4	Р	U	60	06	L	Н	N3
	1	2	3	4	5	6	7	8	9	10
	1 ·	- Vishay Semiconductors product								
	2 ·	- Circuit configuration:								
		E = single diode								
	3	- FRE	ED Gen	4						
	4	• P=	TO-247	' packag	e					
	5	- Pro	cess typ	be:						
		U =	ultrafas	t recove	ery					
	6	- Cur	rent rati	ng (60 =	= 60 A)					
	7.	- Volt	age rati	ng (06 =	= 600 V)					
	8	• L=	L = long lead							
	9 ·	• H=	AEC-Q	101 qua	lified					
	10 -									
		N3 :	= haloge	en-free,	RoHS-c	compliar	nt, and t	otally le	ad (Pb)	-tree

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-E4PU6006LHN3	25	500	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS						
Dimensions TO-247AD 2L www.vishay.com/doc?95536						
Part marking information	TO-247AD 2L	www.vishay.com/doc?95648				

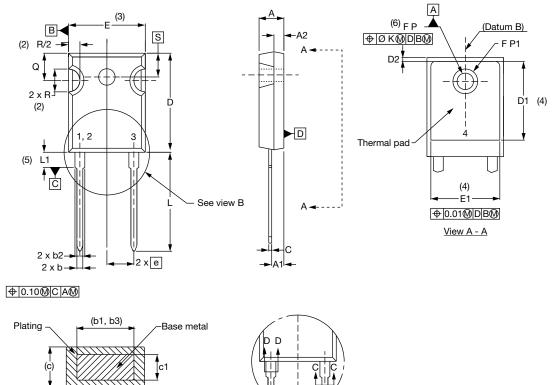
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TO-247AD 2L

DIMENSIONS in millimeters and inches



Section C - C, D - D

MILLIMET

ľ

MIN.

4.65

2.21

1.50 0.99

0.99

1.65

1.65

0.38 0.38

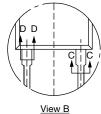
19.71

13.08

0.51

(b, b2)

(4)



TERS	INC	HES	NOTEO	NOTES SYMBOL				MILLIN	MILLIMETERS		INCHES		
MAX.	MIN.	MAX.	NUTES			MIN.	MAX.	MIN.	MAX.	NOTES			
5.31	0.183	0.209			E	15.29	15.87	0.602	0.625	3			
2.59	0.087	0.102			E1	13.46	-	0.53	-				
2.49	0.059	0.098			е	5.46 BSC 0.215 BSC		BSC					
1.40	0.039	0.055			ØK	0.254		0.010					
1.35	0.039	0.053			L	19.81	20.32	0.780	0.800				
2.39	0.065	0.094			L1	3.71	4.29	0.146	0.169				
2.34	0.065	0.092			ØР	3.56	3.66	0.14	0.144				
0.89	0.015	0.035			Ø P1	-	6.98	-	0.275				
0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224				
20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216				
-	0.515	-	4		S	S 5.51 BSC		0.217	BSC				

Notes

SYMBOL

A A1

A2

b

b1 b2

b3

С

c1 D

D1

D2

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

1.35

0.020

0.053

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

(5) Lead finish uncontrolled in L1

(6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4

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