

Vishay Siliconix

# Dual P-Channel 12-V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)		
	0.014 at V <sub>GS</sub> = - 4.5 V	- 9.8		
- 12	0.017 at V <sub>GS</sub> = - 2.5 V	- 8.9		
	0.022 at V <sub>GS</sub> = - 1.8 V	- 7.8		

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 • Definition
- TrenchFET<sup>®</sup> Power MOSFET ٠

P-Channel MOSFET

Compliant to RoHS Directive 2002/95/EC ٠

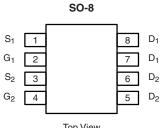
#### **APPLICATIONS**

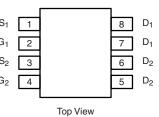
Load Switching



FREE

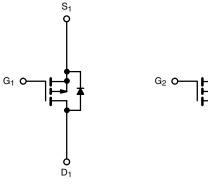
Available





Ordering Information: Si4933DY-T1-E3 (Lead (Pb)-free)

Si4933DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

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 $D_2$ 

 $S_2$ 

ABSOLUTE MAXIMUM RATINGS $T_{\mu}$	$_{\Lambda}$ = 25 °C, unle	ss otherwise n	oted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 12		V
Gate-Source Voltage		V <sub>GS</sub>	± 8		
Continuous Drain Current /T 150 °C/a	T <sub>A</sub> = 25 °C	– I <sub>D</sub>	- 9.8	- 7.4	^
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		- 7.8	- 5.9	
Pulsed Drain Current		I <sub>DM</sub>	- 30		A
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	- 1.7	- 0.9	
	T <sub>A</sub> = 25 °C	– P <sub>D</sub>	2.0	1.1	w
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		1.3	0.7	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 10 s	R <sub>thJA</sub>	45	62.5	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		85	110	°C/W
Maximum Junction-to-Foot (Drain)	Steady State		26	35	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

## Vishay Siliconix



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				•			
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -500 \ \mu A$	- 0.40		- 1.0	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -12 V, V_{GS} = 0 V$		- 1			
		$V_{DS}$ = - 12 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C	T <sub>J</sub> = 55 °C - 5			μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 V, V_{GS} = -4.5 V$	- 30			Α	
		$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -9.8 \text{ A}$		0.0115	0.014	·	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS}$ = - 2.5 V, $I_{D}$ = - 8.9 A		0.014	0.017		
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 5.0 A		0.018	0.022		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = -10$ V, $I_{D} = -9.8$ A		40		S	
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{S} = -1.7 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.7	- 1.2	V	
Dynamic <sup>b</sup>				•			
Total Gate Charge	Qg			46	70	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 6 V, V_{GS} = -4.5 V, I_D = -9.8 A$		6.0			
Gate-Drain Charge	Q <sub>gd</sub>			13			
Turn-On Delay Time t <sub>d(t</sub>				35	55		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 6 V, $R_L$ = 6 $\Omega$	47	47	70	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$t_{d(off)}$ I <sub>D</sub> $\cong$ - 1 A, V <sub>GEN</sub> = - 4.5 V, R <sub>g</sub> = 6 $\Omega$		320	480		
Fall Time	t <sub>f</sub>			260	390		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.7 A, dl/dt = 100 A/μs		210	315		

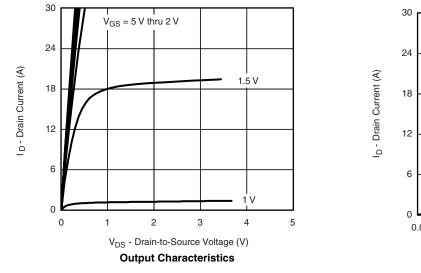
Notes:

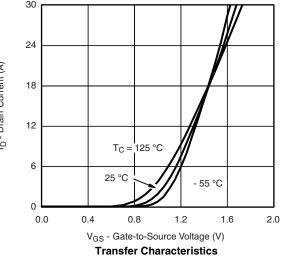
a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



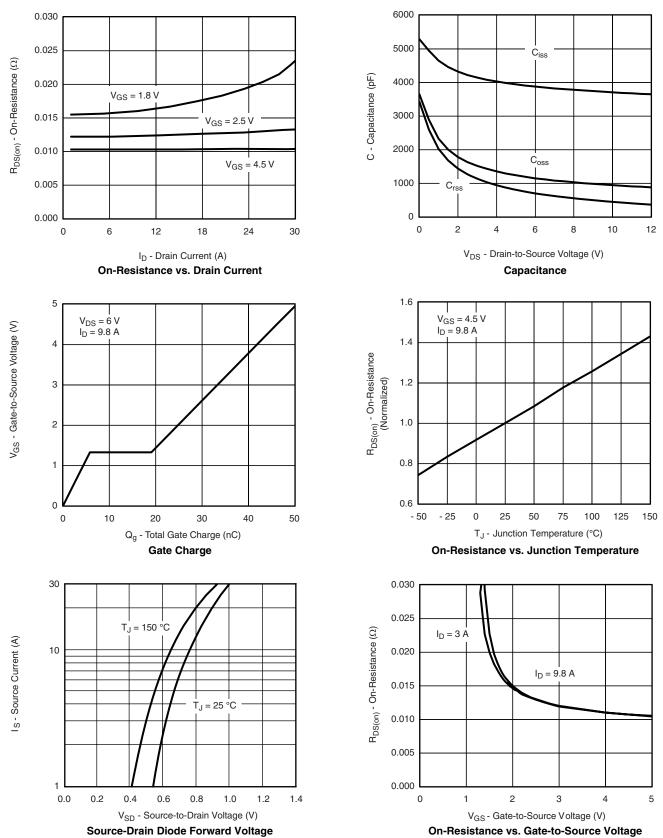




# Si4933DY

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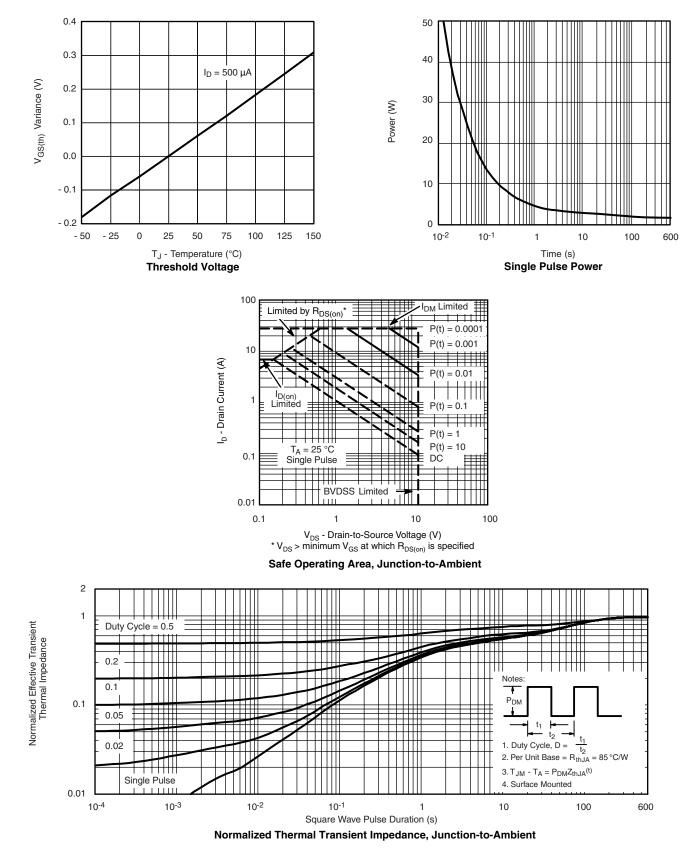
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



### **Vishay Siliconix**



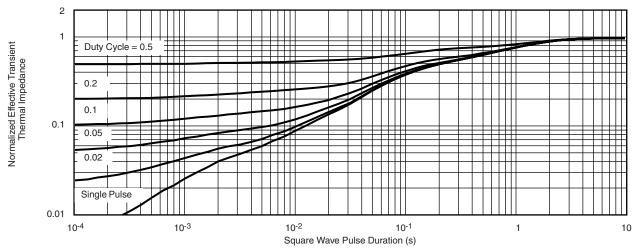
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Si4933DY Vishay Siliconix

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg271980">www.vishay.com/ppg271980</a>.



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