Vishay Siliconix

Si1013CX

RoHS

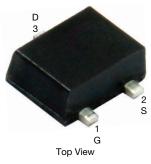
COMPLIANT

HALOGEN



PRODUCT SUMMARY						
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)	Q <sub>g</sub> (TYP.) (nC)			
	0.760 at V <sub>GS</sub> = -4.5 V	-0.45				
-20	1.040 at V <sub>GS</sub> = -2.5 V	-0.40	1			
	1.500 at V <sub>GS</sub> = -1.8 V	-0.32				



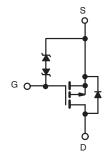


## FEATURES

- TrenchFET<sup>®</sup> power MOSFET
- 100 % Rg tested
- Typical ESD protection: 1000 V (HBM)
- Fast switching speed
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **APPLICATIONS**

- Load / power switch for portable devices
- Drivers: relays, solenoids, displays
- Battery operated systems



P-Channel MOSFET

### Marking Code: 6

### Ordering Information:

Si1013CX-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_A = 25 \degree C$ , unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage		V <sub>DS</sub>	-20	V		
Gate-Source Voltage		V <sub>GS</sub>	± 8			
	T <sub>A</sub> = 25 °C		-0.45 <sup>b, c</sup>			
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 70 °C	I <sub>D</sub>	-0.36 <sup>b, c</sup>			
Pulsed Drain Current (t = 300 μs)		I <sub>DM</sub>	-1.5	— A		
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	-0.16 <sup>b, c</sup>			
Maximum Dawar Dissinction	T <sub>A</sub> = 25 °C	P	0.19 <sup>b, c</sup>	10/		
Maximum Power Dissipation	T <sub>A</sub> = 70 °C	P <sub>D</sub>	0.12 <sup>b, c</sup>			
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C		

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum Junction-to-Ambient <sup>a, b</sup>	t ≤ 5 s	R <sub>thJA</sub>	440	530	°C/W	
Maximum Junction-to-Ambient 4, 2	Steady State		540	650		

#### Notes

a. Maximum under steady state conditions is 650 °C/W.

b. Surface mounted on 1" x 1" FR4 board.

c. t = 5 s.

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static			•	•	•		
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0, I_D = -250 \ \mu A$	-20	-	-	V	
V <sub>DS</sub> Temperature Coefficient	ΔV <sub>DS</sub> /T <sub>J</sub>		-	-12	-	mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = -250 μA	-	1.8	-	mv/°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = -250 \ \mu A$	-0.4	-	-1	V	
Cata Source Leekage		$V_{DS}=0~V,~V_{GS}=\pm~8~V$	-	-	± 30		
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS}=0~V,~V_{GS}=\pm~4.5~V$	-	-	± 1		
Zara Cata Valtaga Drain Currant	I	$V_{DS} = -20 \text{ V},  V_{GS} = 0 \text{ V}$	-	-	-1		
Zero Gate Voltage Drain Current	IDSS	$V_{DS}$ = -20 V, $V_{GS}$ = 0 V, $T_{J}$ = 85 °C	-	-	-10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = \ge 5 \text{ V},  V_{GS} = -4.5 \text{ V}$	-1.5	-	-	А	
		$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -0.4 \text{ A}$	-	0.630	0.630 0.760		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS}$ = -2.5 V, I <sub>D</sub> = -0.2 A	-	0.865	1.040	Ω	
		$V_{GS} = -1.8 \text{ V}, \text{ I}_{D} = -0.1 \text{ A}$	-	1.200	1.500	1	
Forward Transconductance	<b>g</b> fs	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = 0.4 \text{ A}$	-	1	-	S	
Dynamic <sup>b</sup>			•				
Input Capacitance	C <sub>iss</sub>		-	45	-	pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS}=-10 \text{ V},  V_{GS}=0 \text{ V},  f=1  \text{MHz}$	-	15	-		
Reverse Transfer Capacitance	C <sub>rss</sub>		-	10	-		
	Q <sub>g</sub> -	$V_{DS}$ = -10 V, $V_{GS}$ = -4.5 V, $I_{D}$ = -0.4 A	-	- 1.65 2.50			
Total Gate Charge			-	1	2	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = -0 V, $V_{GS}$ = -2.5 V, $I_D$ = -0.4	-	0.2	-		
Gate-Drain Charge	Q <sub>gd</sub>		-	0.26	-		
Gate Resistance	Rg	f = 1 MHz	2.4	12	24	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>		-	9	18		
Rise Time	t <sub>r</sub>	$V_{DD} = -10 \text{ V}, \text{ R}_{L} = 33.3 \Omega$	-	10	20	- ns	
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D\cong$ -0.3 A, $V_{GEN}$ = -4.5 V, $R_g$ = 1 $\Omega$	-	10	20		
Fall Time	t <sub>f</sub>		-	8	16		
Turn-On Delay Time	t <sub>d(on)</sub>		-	1	2		
Rise Time	t <sub>r</sub>	$V_{DD}$ = -10 V, $R_L$ = 33.3 $\Omega$	-	8	16		
Turn-Off DelayTime	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong$ -0.3 A, $\text{V}_\text{GEN}$ = -8 V, $\text{R}_\text{g}$ = 1 $\Omega$	-	9	18		
Fall Time	t <sub>f</sub>		-	5	10		
Drain-Source Body Diode Characteris	tics						
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>		-	-	-1.5	А	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = -0.3 A	-	-0.8	-1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>		-	16	24	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			8	16	nC	
verse Recovery Fall Time $t_a$ $I_F = -0.3 \text{ A, } dI/dt = 100 \text{ A/} \mu \text{s}$		-	11	-			
Reverse Recovery Rise Time	t <sub>b</sub>		-	5	1	ns	

#### Notes

a. Pulse test; pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%.$ 

b. Guaranteed by design, not subject to production testing.

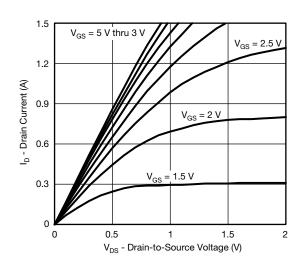
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

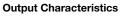
2

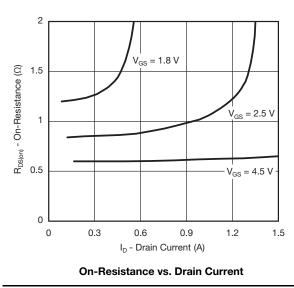
Gate Current vs. Gate-Source Voltage

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# TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

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0.010

0.008

0.006

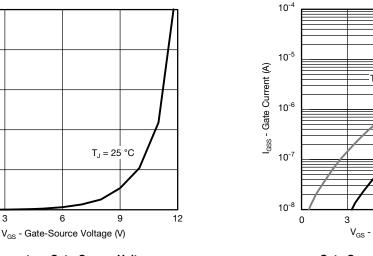
0.004

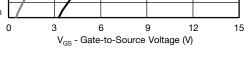
0.002

0.000

0

I<sub>GSS</sub> - Gate Current (mA)

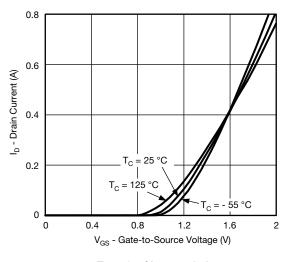




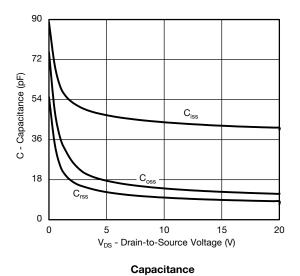
25 °C

150 °C

Gate Current vs. Gate-Source Voltage



**Transfer Characteristics** 



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 $V_{GS} =$ 2.5 V

1.5

1.3

1.1

0.9

0.7

- 50

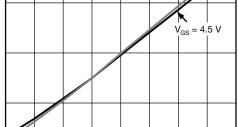
- 25

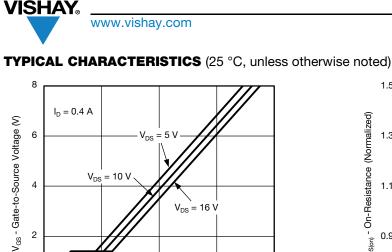
0

25

R<sub>DS(on)</sub> - On-Resistance (Normalized)

 $I_{\rm D} = 0.35 \, {\rm A}$ 





2

0

0

0.45

Gate Charge

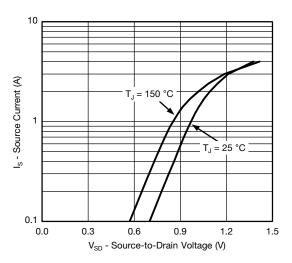
0.9

Q<sub>a</sub> - Total Gate Charge (nC)

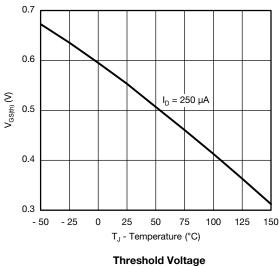
 $V_{DS} = 16 V$ 

1.35

1.8



Source-Drain Diode Forward Voltage



T<sub>J</sub> - Junction Temperature (°C) **On-Resistance vs. Junction Temperature** 

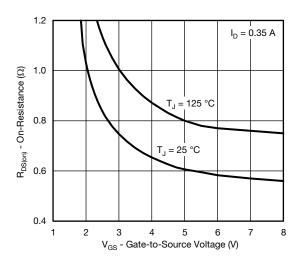
50

75

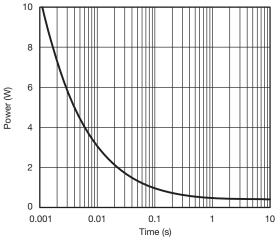
100

125

150



**On-Resistance vs. Gate-to-Source Voltage** 



Single Pulse Power, Junction-to-Ambient

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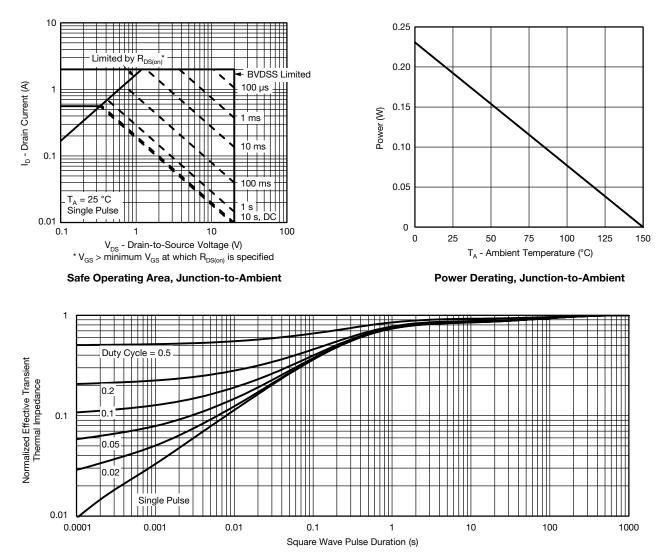
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# TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

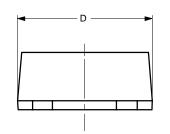


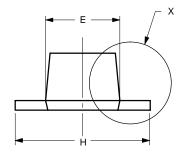
Normalized Thermal Transient Impedance, Junction-to-Ambient

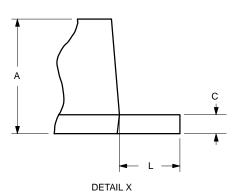
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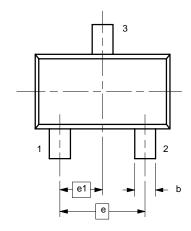


SC89-3









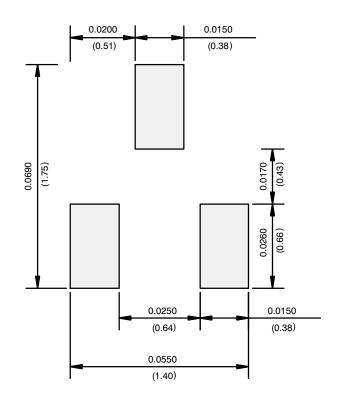
	MILLIM	IETERS	INCHES		
Dim	Min	Max	Min	Max	
Α	0.60	0.80	0.024	0.031	
b	0.23	0.33	0.009	0.013	
С	0.10	0.20	0.004	0.008	
D	1.50	1.70	0.059	0.067	
E	0.75	0.95	0.030	0.037	
е	1.00 BSC		0.040 BSC		
<b>e</b> <sub>1</sub>	0.50 BSC		0.020 BSC		
Н	1.50	1.70	0.059	0.067	
L	0.30	0.50	0.012	0.020	
ECN: S-03946—Rev. B, 09-Jul-01 DWG: 5869					

# **Application Note 826**

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**RECOMMENDED MINIMUM PADS FOR SC-89: 3-Lead** 



Recommended Minimum Pads Dimensions in Inches/(mm)

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