VS-VSKCU300/06PbF

Vishay Semiconductors



HEXFRED[®] Ultrafast Diodes, 300 A (INT-A-PAK Power Modules)



INT-A-PAK

PRIMARY CHARACTERISTICS				
V _R	600 V			
V _F (typical)	1.23			
t _{rr} (typical)	130 ns			
I _{F(AV)} at T _C	300 A at 48 °C			
Package	INT-A-PAK			
Circuit configuration	Two diodes common cathode			

FEATURES

- · Electrically insulated by DBC ceramic
- 3500 V_{BMS} isolating voltage
- Standard JEDEC[®] package
- · Simplified mechanical designs, rapid assembly
- High surge capability
- Large creepage distances
- UL approved file E78996
- Case style INT-A-PAK
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Cathode to anode voltage	V _R		600	V	
Continuous forward current per leg		T _C = 25 °C	435		
	IF	T _C = 100 °C	230	А	
Single pulse forward current	I _{FSM}	Limited by junction temperature	TBD		
Maximum power dissipation per leg	P _D	T _C = 25 °C	781	w	
		T _C = 100 °C	313		
Operating junction and storage temperature range	T _J , T _{Stg}		-40 to +150	°C	
RMS insulation voltage	V _{INS}	50 Hz, circuit to base, all terminals shorted, t = 1 s	3500	V	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	I _R = 500 μA	600	-	-	
Forward voltage drop per leg	V _{FM}	I _F = 150 A	-	1.23	1.53	
		I _F = 300 A	-	1.43	1.96	V
		I _F = 150 A, T _J = 125 °C	-	1.11	1.29	
		I _F = 300 A, T _J = 125 °C	-	1.39	1.73	
Maximum reverse leakage current	I _{RM}	$T_J = 150 \ ^{\circ}C, V_R = 600 \ V$	-	-	50	mA

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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	+	T _J = 25 °C		-	130	165	ns
neverse recovery time	t _{rr}	T _J = 125 °C		-	195	260	115
Peak recovery current		T _J = 25 °C	I _F = 50 A dI/dt = 200 A/μs	-	11	18	A
Feak recovery current	Irr	T _J = 125 °C		-	20	30	
Reverse recovery charge	Q _{rr}	T _J = 25 °C	V _R = 400 V (per leg)	-	670	1485	nC
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	1800	3900	10
Peak rate of recovery current	dl _{(rec)M} /dt	T _J = 125 °C		-	-	400	A/µs
Softness factor per leg s		I_F = 50 A, T_J = 25 °C, dI/dt = 400 A/µs, V_R = 200 V		-	0.2	-	
		I _F = 50 A, T _J = 125 °C, d	$1/dt = 400 \text{ A}/\mu \text{s}, \text{ V}_{\text{R}} = 200 \text{ V}$	-	0.22	-	

THERMAL AND MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction operating and storage temperature range			-40 to +150 °C	
Maximum thermal resistance, junction to case per leg	R _{thJC}	DC operation	0.16	K/W
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	0.05	r./ vv
Mounting to heatsink torque ± 10 % busbar	-	A mounting compound is recommended and the torque should be rechecked after a period of 3 hours to allow the spread of the compound.	4 to 6	Nm
Approvimeto weight			200	g
Approximate weight			7.1	oz.
Case style			INT-A-	PAK

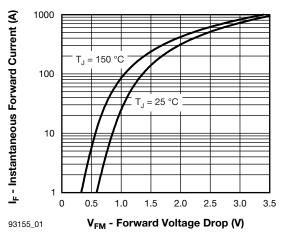
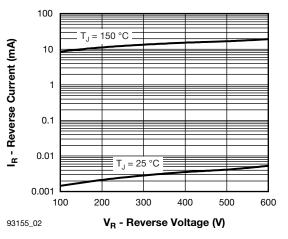
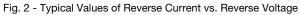


Fig. 1 - Maximum Forward Voltage Drop Characteristics





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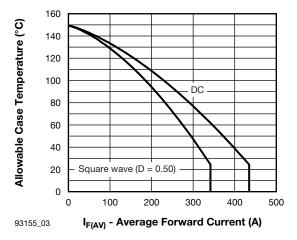


Fig. 3 - Maximum Allowable Case Temperature vs. Average Forward Current

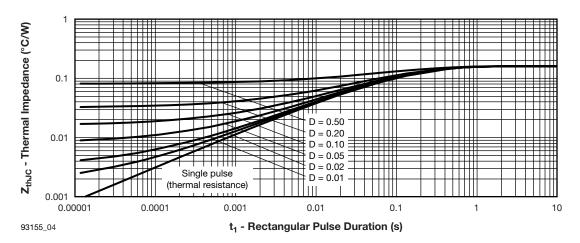
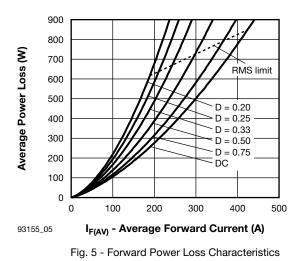


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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 $(\mathbf{F}_{R} = 400 \text{ V}_{R} =$

Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt (Per Leg)

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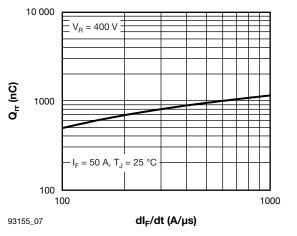
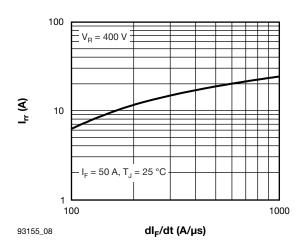
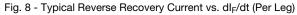
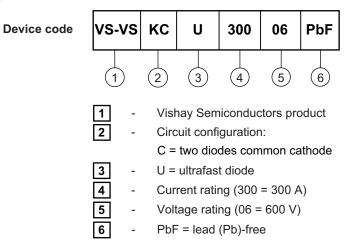


Fig. 7 - Typical Reverse Recovery Charge vs. dl_F/dt (Per Leg)

ORDERING INFORMATION TABLE







CIRCUIT CONFIGURATION				
CIRCUIT	CIRCUIT DRAWING			
Two diodes common cathode				

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95254					
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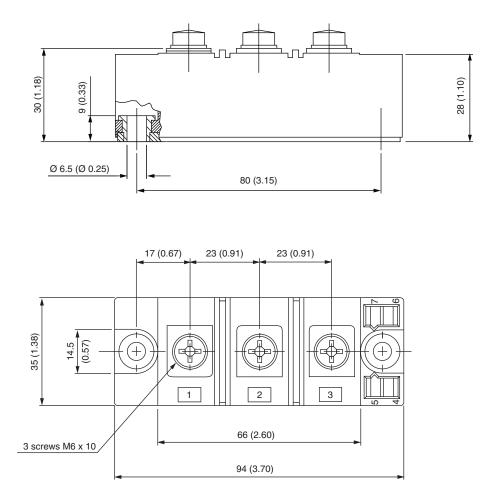


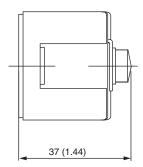
Outline Dimensions

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INT-A-PAK DBC

DIMENSIONS in millimeters (inches)







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