



N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
20	0.070 at V _{GS} = 4.5 V	3.7		
	0.080 at V _{GS} = 2.5 V	3.4		
	0.100 at V _{GS} = 1.8 V	3.0		

FEATURES

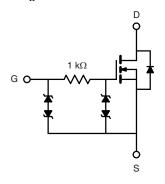
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFETs: 1.8 V Rated
- ESD Protected: 2000 V
- Thermally Enhanced SC-70 Package
- Compliant to RoHS Directive 2002/95/EC

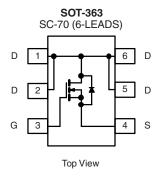


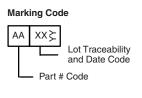


APPLICATIONS

Load Switching







Ordering Information: Si1410EDH-T1-E3 (Lead (Pb)-free)

Si1410EDH-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T	_A = 25 °C, unle	ss otherwise r	noted				
Parameter		Symbol	5 s	Steady State	Unit		
Drain-Source Voltage		V _{DS}	20		V		
Gate-Source Voltage		V _{GS}	± 12		V		
Continuous Drain Current /T 150 °C\8	T _A = 25 °C	- I _D	3.7	2.9	A		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 85 °C		2.6	2.0			
Pulsed Drain Current		I _{DM}	8		А		
Continuous Diode Current (Diode Conduction) ^a		I _S	1.4 0.9		I _S 1.4		
Maximum Power Dissipation ^a	T _A = 25 °C	- P _D	1.56	1.0	W		
Maximum Fower Dissipation	T _A = 85 °C		0.81	0.52			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C		

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	t ≤ 5 s	R _{thJA}	60	80	
Waximum Junction-to-Ambient	Steady State		100	125	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	34	45	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

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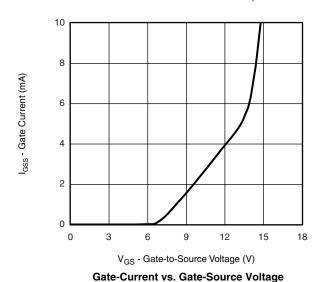
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.45			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$			± 1	μΑ	
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 10	mA	
Zero Gate Voltage Drain Current	1.	V _{DS} = 16 V, V _{GS} = 0 V			1		
	I _{DSS}	V _{DS} = 16 V, V _{GS} = 0 V, T _J = 85 °C			5	μΑ	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 4.5 V	4			Α	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 3.7 \text{ A}$		0.055	0.070		
		$V_{GS} = 2.5 \text{ V}, I_D = 3.4 \text{ A}$		0.065	0.080	Ω	
		V _{GS} = 1.8 V, I _D = 1.7 A		0.080	0.100		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 3.7 A		10		S	
Diode Forward Voltage ^a	V _{SD}	I _S = 1.4 A, V _{GS} = 0 V		0.75	1.1	V	
Dynamic ^b							
Total Gate Charge	Qg			5.6	8	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 3.7 \text{ A}$		0.75			
Gate-Drain Charge	Q _{gd}			1.10			
Turn-On Delay Time	t _{d(on)}			0.15	0.25		
Rise Time	t _r	$V_{DD} = 10 \text{ V}, R_{L} = 10 \Omega$		0.4	0.6	110	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 1 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 6 \Omega$		1.9	2.8	μs	
Fall Time	t _f			1.2	1.8		

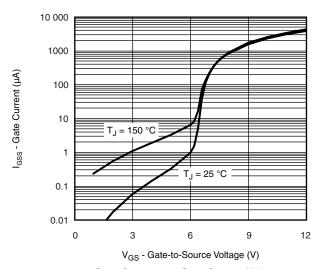
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

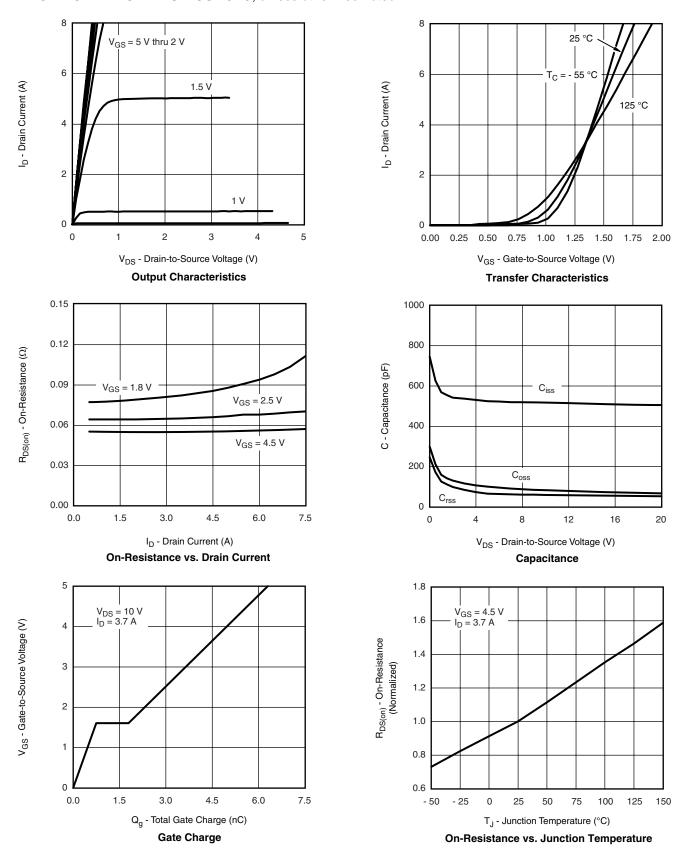




Gate-Current vs. Gate-Source Voltage



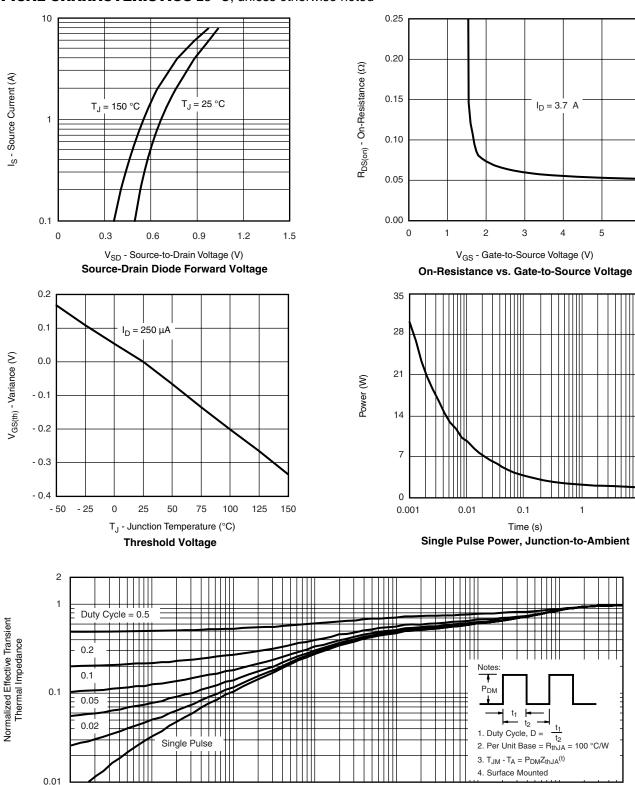
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



10⁻⁴

10⁻³

10⁻²

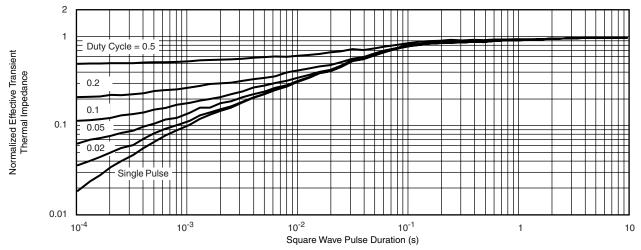
600

100

10⁻¹



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71409.

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