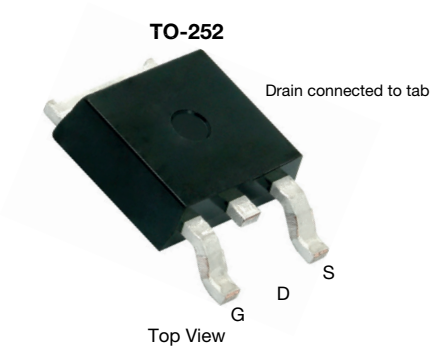
N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (Ω) MAX.	I_D (A) [c]	Q_g (TYP.)
100	0.0089 at V_{GS} = 10 V	50	33 nC
	0.0093 at V_{GS} = 7.5 V	50	



Ordering Information:
SUD70090E-GE3 (lead (Pb)-free and halogen-free)

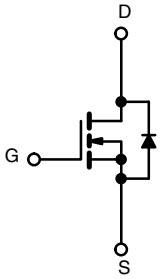
FEATURES

- ThunderFET® power MOSFET
- Maximum 175 °C junction temperature
- Q_{gd} / Q_{gs} ratio < 1 optimizes switching characteristics
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

RoHS COMPLIANT
HALOGEN FREE

APPLICATIONS

- DC/DC converter
- Power tools
- Motor drive switch
- DC/AC inverter
- Battery management



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current (T_J = 150 °C)	T_C = 25 °C	I_D	50 [c]	A
	T_C = 70 °C		50 [c]	
Pulsed Drain Current (t = 100 µs)		I_{DM}	120	
Avalanche Current		I_{AS}	40	
Single Avalanche Energy [a]	L = 0.1 mH	E_{AS}	80	mJ
Maximum Power Dissipation [a]	T_C = 25 °C	P_D	125	W
	T_C = 70 °C [b]		87.5	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) [b]	R_{thJA}	40	°C/W
Junction-to-Case (Drain)	R_{thJC}	1.2	

Notes

a. Duty cycle ≤ 1 %.
b. When mounted on 1" square PCB (FR4 material).
c. Package limited.

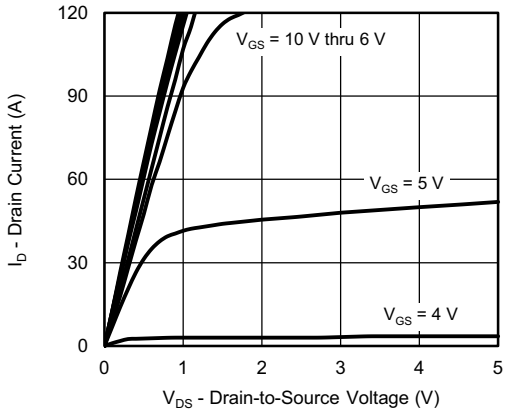
SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA	100	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	2	-	4	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V	-	-	± 250	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 100 V, V_{GS} = 0 V	-	-	1	µA
		V_{DS} = 100 V, V_{GS} = 0 V, T_J = 125 °C	-	-	150	
		V_{DS} = 100 V, V_{GS} = 0 V, T_J = 175 °C	-	-	5	mA
On-State Drain Current [a]	$I_{D(on)}$	V_{DS} ≥ 10 V, V_{GS} = 10 V	50	-	-	A
Drain-Source On-State Resistance [a]	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 20 A	-	0.0074	0.0089	Ω
		V_{GS} = 7.5 V, I_D = 15 A	-	0.0077	0.0093	
Forward Transconductance [a]	g_{fs}	V_{DS} = 15 V, I_D = 10 A	-	38	-	S
Dynamic [b]						
Input Capacitance	C_{iss}	V_{GS} = 0 V, V_{DS} = 50 V, f = 1 MHz	-	1950	-	pF
Output Capacitance	C_{oss}		-	845	-	
Reverse Transfer Capacitance	C_{rss}		-	54	-	
Total Gate Charge [c]	Q_g	V_{DS} = 50 V, V_{GS} = 10 V, I_D = 20 A	-	33	50	nC
Gate-Source Charge [c]	Q_{gs}		-	8.8	-	
Gate-Drain Charge [c]	Q_{gd}		-	7.5	-	
Gate Resistance	R_g	f = 1 MHz	0.7	3.5	7	Ω
Turn-On Delay Time [c]	$t_{d(on)}$	V_{DD} = 50 V, R_L = 5 Ω I_D ≅ 10 A, V_{GEN} = 10 V, R_g = 1 Ω	-	15	30	ns
Rise Time [c]	t_r		-	27	54	
Turn-Off Delay Time [c]	$t_{d(off)}$		-	36	72	
Fall Time [c]	t_f		-	45	90	
Drain-Source Body Diode Ratings and Characteristics [b] (T_C = 25 °C)						
Pulsed Current (t = 100 µs)	I_{SM}		-	-	120	A
Forward Voltage [a]	V_{SD}	I_F = 10 A, V_{GS} = 0 V	-	0.8	1.5	V
Reverse Recovery Time	t_{rr}	I_F = -10 A, dI/dt = 100 A/µs	-	77	116	ns
Peak Reverse Recovery Charge	$I_{RM(REC)}$		-	4.2	6.3	A
Reverse Recovery Charge	Q_{rr}		-	145	365	nC

Notes

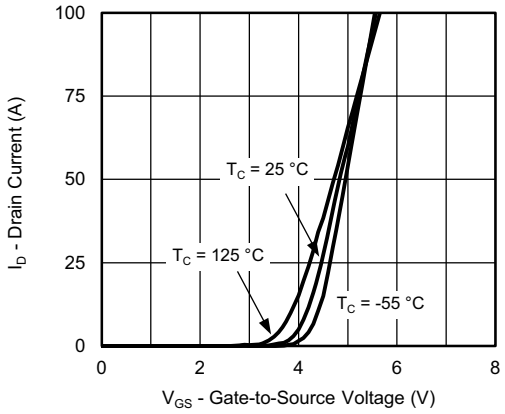
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

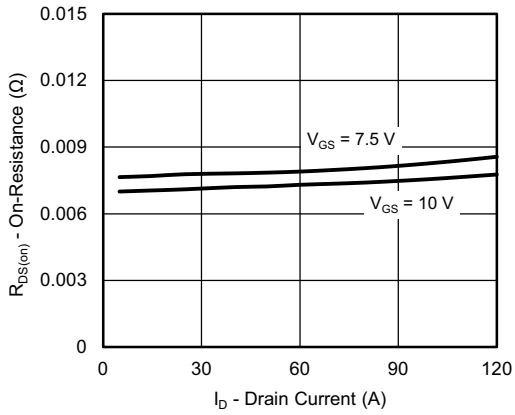
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



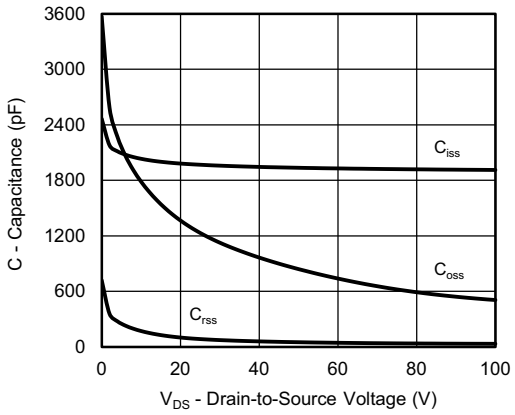
Output Characteristics



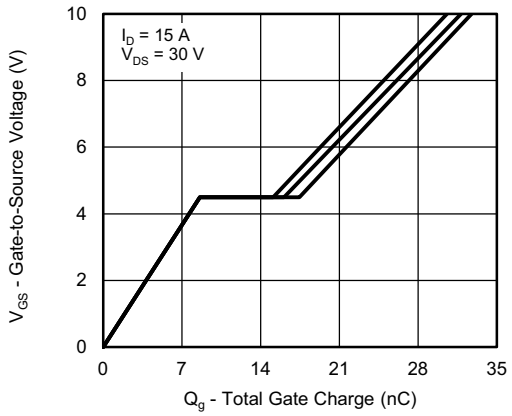
Transfer Characteristics



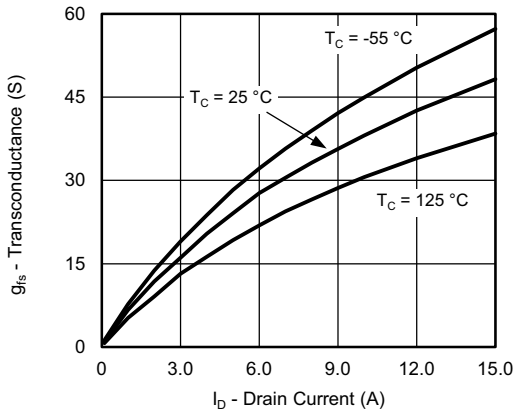
On-Resistance vs. Drain Current



Capacitance

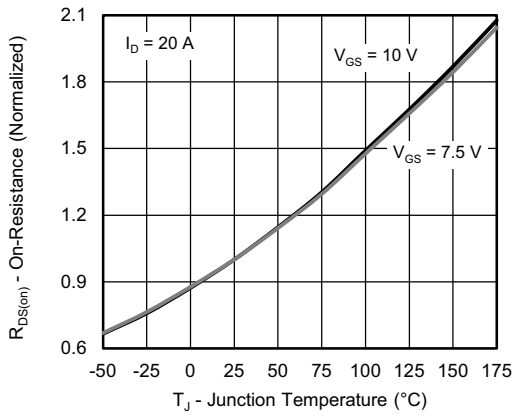


Gate Charge

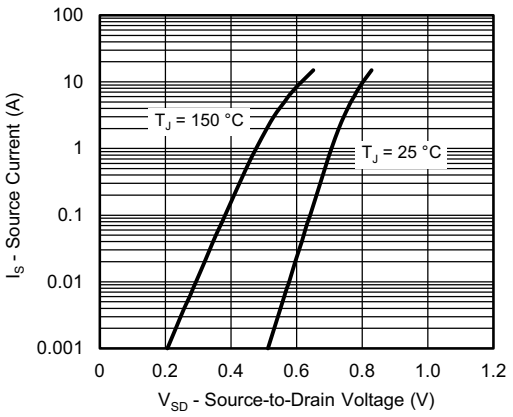


Transconductance

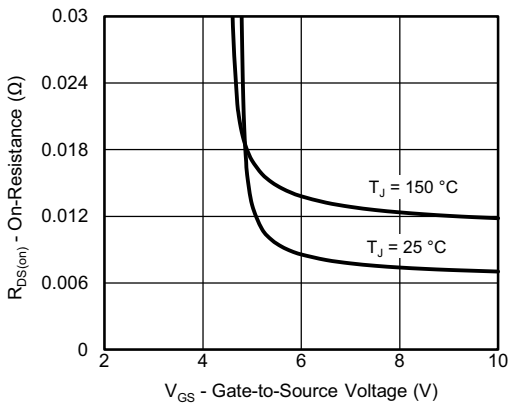
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



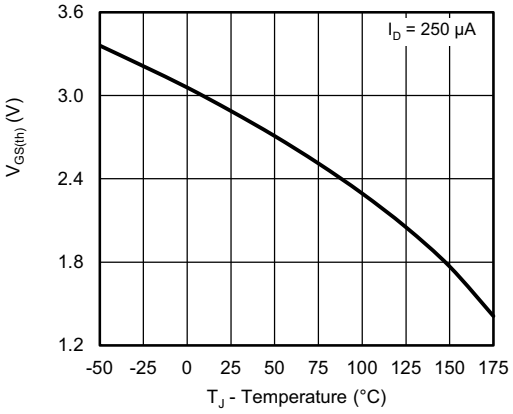
On-Resistance vs. Junction Temperature



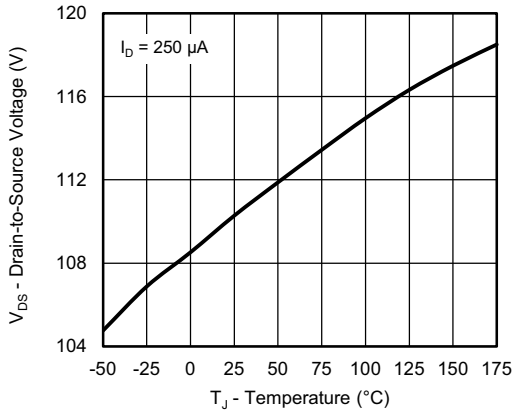
Source Drain Diode Forward Voltage



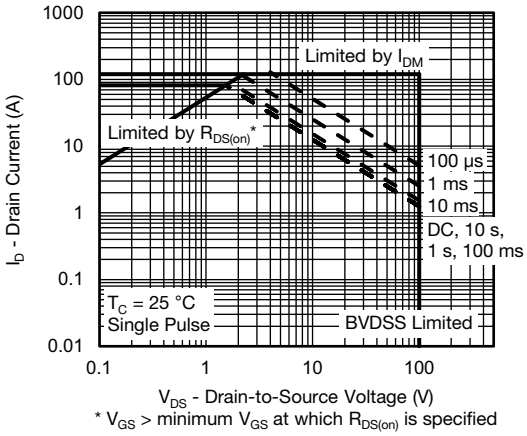
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

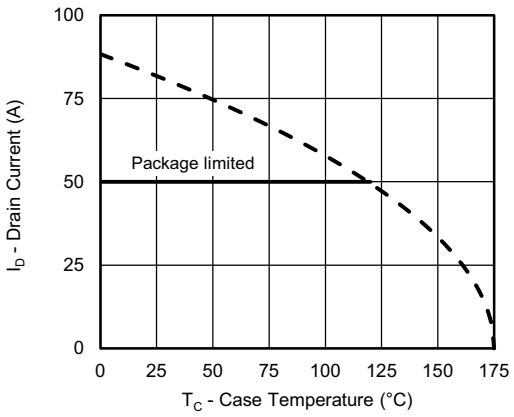


Drain Source Voltage vs. Junction Temperature

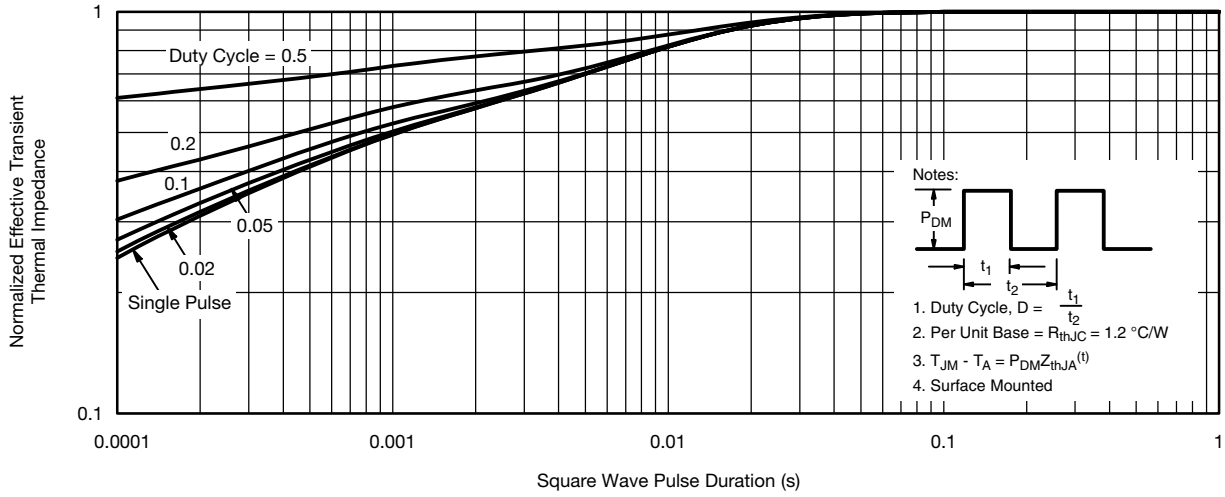


Safe Operating Area

THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Current De-Rating



Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction to Case (25 °C)

  are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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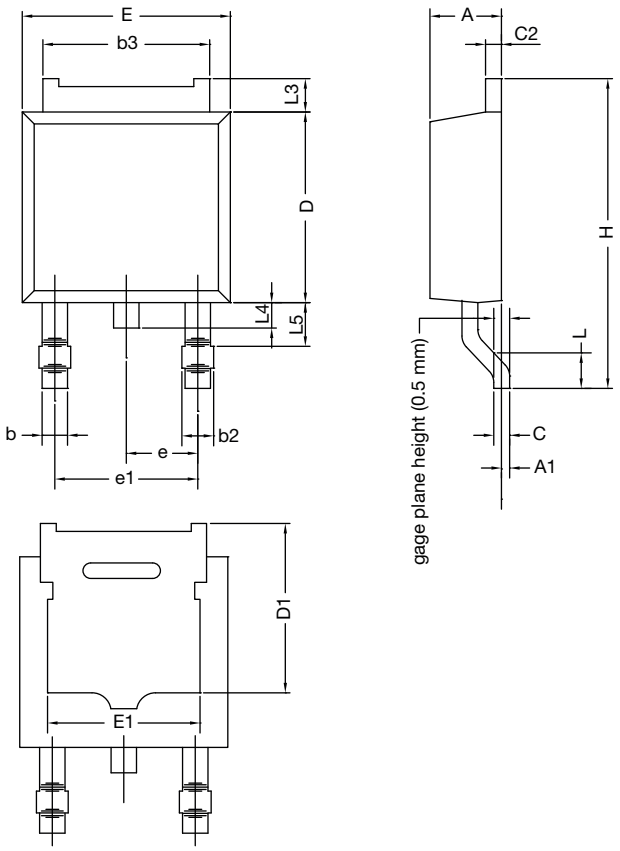
TO-252AA Case Outline

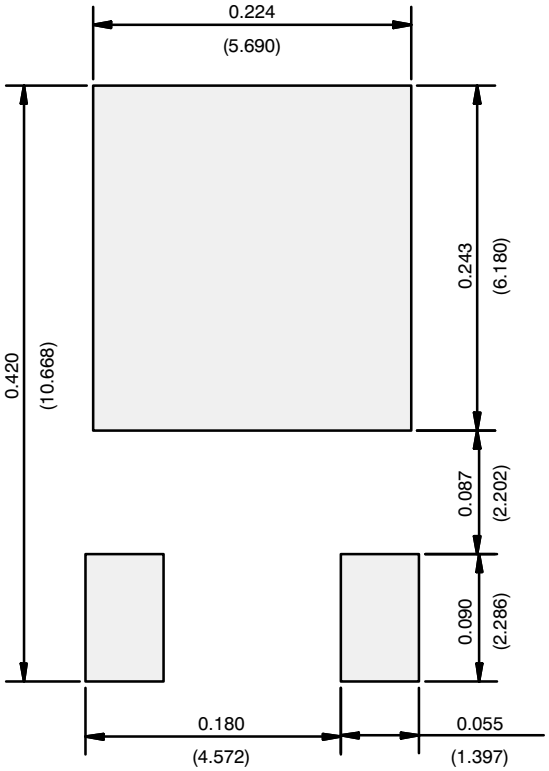


	MILLIMETERS		INCHES	
DIM.	MIN.	MAX.	MIN.	MAX.
A	2.18	2.38	0.086	0.094
A1	-	0.127	-	0.005
b	0.64	0.88	0.025	0.035
b2	0.76	1.14	0.030	0.045
b3	4.95	5.46	0.195	0.215
C	0.46	0.61	0.018	0.024
C2	0.46	0.89	0.018	0.035
D	5.97	6.22	0.235	0.245
D1	4.10	-	0.161	-
E	6.35	6.73	0.250	0.265
E1	4.32	-	0.170	-
H	9.40	10.41	0.370	0.410
e	2.28 BSC		0.090 BSC	
e1	4.56 BSC		0.180 BSC	
L	1.40	1.78	0.055	0.070
L3	0.89	1.27	0.035	0.050
L4	-	1.02	-	0.040
L5	1.01	1.52	0.040	0.060
ECN: T16-0236-Rev. P, 16-May-16 DWG: 5347				

Notes

- Dimension L3 is for reference only.

RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads
Dimensions in Inches/(mm)

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