VS-32CTQ025-M3, VS-32CTQ030-M3

Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 15 A



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 15 A			
V_{R}	25 V, 30 V			
V _F at I _F	0.40 V			
I _{RM} typ.	97 mA at 125 °C			
T _J max.	150 °C			
E _{AS}	13 mJ			
Package	3L TO-220AB			
Circuit configuration	Common cathode			

FEATURES

- 150 °C T_J operation
- Low forward voltage drop
- High frequency operation



- High purity, high temperature epoxy FREE encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-32CTQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	30	Α	
V _{RRM}		25/30	V	
I _{FSM}	t _p = 5 μs sine	900	Α	
V _F	15 A _{pk} , T _J = 125 °C	0.40	V	
TJ	Range	-55 to +150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-32CTQ025-M3	VS-32CTQ030-M3	UNITS
Maximum DC reverse voltage	V_R	25	30	V
Maximum working peak reverse voltage	V_{RWM}	25	30	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current, see fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 115 °C, rectangular waveform		30	
Maximum peak one cycle non-repetitive		5 μs sine or 3 μs rect. pulse	Following any rated load	900	Α
surge current, see fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	250	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 1.20 \text{A}, L = 11.10 \text{mH}$		13	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical 3		Α	



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
), (1)	15 A	T _J = 25 °C	0.49	V
Maximum forward voltage drop		30 A		0.58	
See fig. 1	V _{FM} ⁽¹⁾	15 A	T _J = 125 °C	0.40	
		30 A		0.53	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	1.75	- mA
		T _J = 125 °C		145	
Typical reverse leakage current	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = Rated V _R	97	mA
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.233	V
Forward slope resistance	r _t			9.09	mΩ
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		1300	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

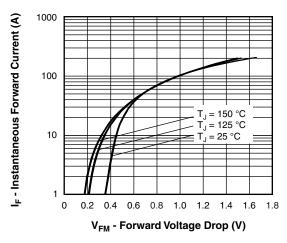
Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55 to 150	°C
Maximum thermal resistance, junction to case per leg	R _{thJC}	DC operation See fig. 4	3.25	°C/W
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.50	C/VV
Approximate weight			2	g
Approximate weight			0.07	oz.
Mounting torque minimum			6 (5)	kgf ⋅ cm
Mounting torque maximum			12 (10)	(lbf · in)
Maddan dada		Consistua 21 TO 200AD	32CTQ025	
Marking device		Case style 3L TO-220AB	32CTQ030	

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T, = 150 °C 100 I_R - Reverse Current (mA) = 125 °C 10 = 100 °C $T_1 = 75 \, ^{\circ}C$ 0.1 0.01 = 25 °C 0.001 10 15 25 0 30 V_R - Reverse Voltage (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

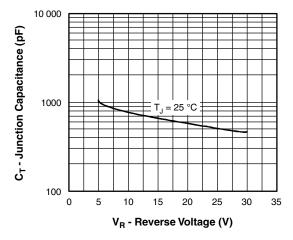


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

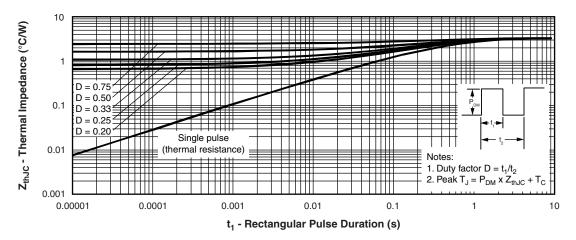


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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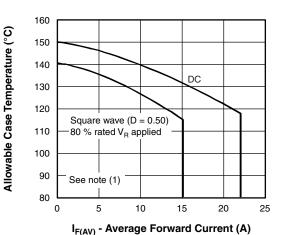


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

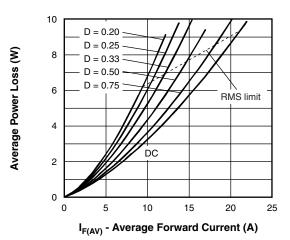


Fig. 6 - Forward Power Loss Characteristics

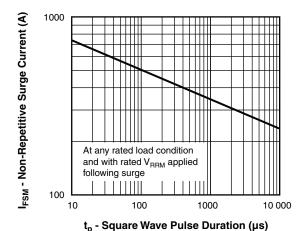


Fig. 7 - Maximum Non-Repetitive Surge Current

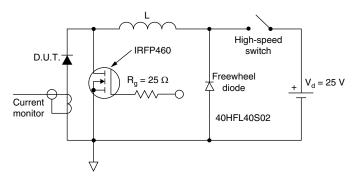


Fig. 8 - Unclamped Inductive Test Circuit

Note

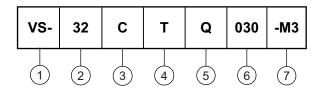
Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80 \%$ rated V_R

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (30 A)

- Circuit configuration:

C = common cathode

4 - Package:

T = TO-220

5 - Schottky "Q" series

6 - Voltage ratings - 025 = 25 V 030 = 30 V

7 - Environmental digit

-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-32CTQ025-M3	50	1000	Antistatic plastic tube		
VS-32CTQ030-M3	50	1000	Antistatic plastic tube		

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?96154				
Part marking information	www.vishay.com/doc?95028			



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