HALOGEN

FREE



Vishay General Semiconductor

Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.41 \text{ V}$ at $I_F = 5 \text{ A}$



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 30 A			
V_{RRM}	120 V			
I _{FSM}	300 A			
V_F at $I_F = 30 A$	0.71 V			
T _J max.	150 °C			
Package	TO-220AB			
Diode variation	Common cathode			

FEATURES

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- · Low thermal resistance
- Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

MECHANICAL DATA

Case: TO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V60120C	UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	120	V	
Maximum average forward rectified current (fig. 1)	per device	1	60	^	
	per diode	I _F (AV)	30	Α	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	300	А	
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +150	°C	



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	- V _F ⁽¹⁾	0.48	-		
	I _F = 15 A			0.66	-		
	I _F = 30 A			0.88	0.95	V	
	I _F = 5 A	T _A = 125 °C		0.41	-	v	
	I _F = 15 A			0.58	-		
	I _F = 30 A			0.71	0.75		
Reverse current at rated V _R per diode	V _R = 90 V	T _A = 25 °C	I _R ⁽²⁾	14	-	μΑ	
		T _A = 125 °C		11	-	mA	
	Vp = 120 V	T _A = 25 °C		40	500	μΑ	
		T _A = 125 °C		15	45	mA	

Notes

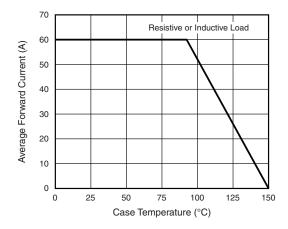
(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL V60120C		UNIT	
Typical thermal resistance per diode	$R_{ heta JC}$	1.2	°C/W	

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-220AB	V60120C-M3/4W	1.89	4W	50/tube	Tube	

RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)





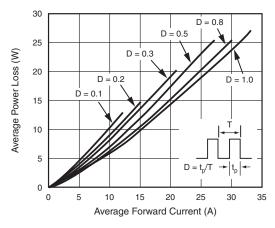


Fig. 2 - Forward Power Loss Characteristics Per Diode



50

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350 T_J = T_J max. 8.3 ms Single Half Sine-Wave 250 250 D 200 D 150 D 150 D 50 D 50

Fig. 3 - Maximum Non-Repetitive Peak Forward Surge Current Per Diode

10

Number of Cycles at 60 Hz

100

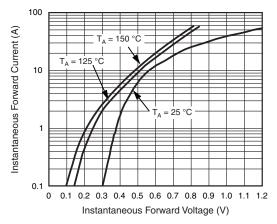


Fig. 4 - Typical Instantaneous Forward Characteristics Per Diode

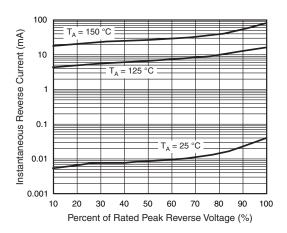


Fig. 5 - Typical Reverse Characteristics Per Diode

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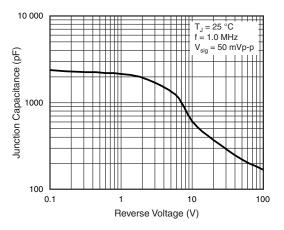


Fig. 6 - Typical Junction Capacitance Per Diode

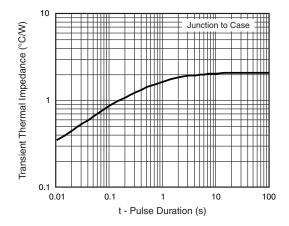
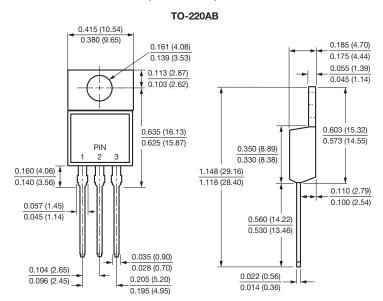


Fig. 7 - Typical Transient Thermal Impedance Per Diode



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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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