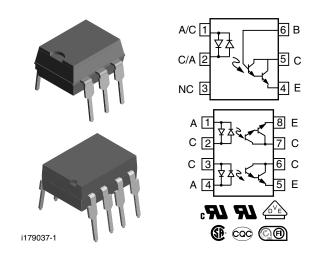


Optocoupler, Photodarlington Output, AC Input, High Gain (Single, Dual Channel)



LINKS TO ADDITIONAL RESOURCES



DESCRIPTION

The IL755, ILD755 are bidirectional input optically coupled isolators. They consist of two gallium arsenide infrared emitting diodes coupled to a silicon NPN photodarlington per channel.

The IL755 is single channel Darlington optocoupler. The

ILD755 has two isolated channels in a single DIP package.

FEATURES

- · AC or polarity insensitive inputs
- Built-in reverse polarity input protection
- Industry standard DIP package





RoHS COMPLIANT

APPLICATIONS

 Designed for applications requiring detection or monitoring of AC signals

AGENCY APPROVALS

- <u>UL</u> / <u>cUL</u> 1577
- DIN EN 60747-5-5 (VDE 0884-5) for:
 - IL755
 - ILD755
- CSA
- CQC GB8898 / CQC GB4943.1
- BSI

ORDERING INFORMATIO	N					
I L x 7 5 5 - # X 0 # # T PART NUMBER CTR PACKAGE OPTION TAPE AND REEL 7.62 mm > 0.7 mm						
		CTR	R (%)			
AGENCY CERTIFIED / PACKAGE	SINGLE CHANNEL, 6 PIN		DUAL CHANNEL, 8 PIN			
7.0.0.0	± 2 mA	± 1 mA	± 2 mA	± 1 mA		
UL, cUL, CSA, CQC	≥ 750	≥ 1000	≥ 750	≥ 1000		
DIP-#	IL755-1	IL755-2	ILD755-1	ILD755-2		
SMD-#, option 7	IL755-1X007	IL755-2X007T	-	ILD755-2X007T		
VDE, UL, cUL, CSA, CQC	≥ 750	≥ 1000	≥ 750	≥ 1000		
DIP-#	IL755-1X001	-	-	-		
SMD-#, option 7	-	-	ILD755-1X017	-		

Note

· Additional options may be possible, please contact sales office



PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
INPUT					
Forward continuous current			I _F	60	mA
Power dissipation			P _{diss}	100	mW
Derate linearly from 25°C				1.33	mW/°C
ОИТРИТ					
Collector emitter breakdown voltage			BV _{CEO}	60	V
Collector base breakdown voltage			BV _{CBO}	60	V
		IL755-1		200	mW
Dawer discipation		IL755-2	<u> </u>	200	mW
Power dissipation		ILD755-1	P _{diss}	150	mW
		ILD755-2	1	150	mW
		IL755-1		2.6	mW/°C
Derate linearly from 25°C		IL755-2	1	2.6	mW/°C
Derate linearly from 25 C		ILD755-1		2.0	mW/°C
		ILD755-2	1	2.0	mW/°C
COUPLER					
		IL755-1		250	mW
Total navyay discination		IL755-2	<u> </u>	250	mW
Total power dissipation		ILD755-1	P _{tot}	400	mW
		ILD755-2		400	mW
		IL755-1		3.0	mW/°C
Derate linearly from 25 °C		IL755-2		3.0	mW/°C
Defate infeatiy from 25 C		ILD755-1		3.0	mW/°C
		ILD755-2		3.0	mW/°C
Storage temperature			T _{stg}	-55 to +150	°C
Operating temperature			T _{amb}	-55 to +100	°C
Lead soldering time at 260 °C				10	S

Note

• Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT								
Forward voltage	$I_F = \pm 10 \text{ mA}$	V_{F}		1.2	1.5	V		
OUTPUT								
Collector emitter breakdown voltage	$I_{C} = 1.0 \text{ mA}$	BV _{CEO}	60	75		V		
Collector base breakdown voltage	$I_C = 10 \mu A$	BV _{CBO}	60	90		V		
Collector emitter leakage current	$V_{CE} = 10 \text{ V}, I_F = 0 \text{ A}$	I _{CEO}		10	100	nA		
COUPLER								
Collector emitter saturation voltage	$I_C = 10 \text{ mA}, I_F = \pm 10 \text{ mA}$	V _{CEsat}			1	V		

Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements



CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Current transfer ratio	$I_F = \pm 2 \text{ mA}, V_{CE} = 5.0 \text{ V}$	IL755-1	CTR	750	-	-	%	
	$I_F = \pm 2 \text{ mA}, V_{CE} = 5.0 \text{ V}$	ILD755-1	CTR	750	-	-	%	
Current transfer fatto	$I_F = \pm 1 \text{ mA}, V_{CE} = 5.0 \text{ V}$	IL755-2	CTR	1000	-	-	%	
	$I_F = \pm 1 \text{ mA}, V_{CE} = 5.0 \text{ V}$	ILD755-2	CTR	1000	-	-	%	

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Rise time	time	IL755-1	t _r	-	50	-	μs
nise time	$V_{CC} = 10 \text{ V}, I_F = \pm 2 \text{ mA}, R_L = 100 \Omega$	ILD755-1	t _r	-	50	-	μs
Fall time	V 10 V L . 2 m A D 100 O	IL755-1	t _f	-	50	-	μs
raii tiille	$V_{CC} = 10 \text{ V}, I_F = \pm 2 \text{ mA}, R_L = 100 \Omega$	ILD755-1	t _f	-	50	-	μs
Rise time	$V_{CC} = 10 \text{ V}, I_{F} = \pm 1 \text{ mA}, R_{I} = 100 \Omega$	IL755-2	t _r	-	70	-	μs
Rise time	$V_{CC} = 10 \text{ V}, \text{ if } = \pm 1 \text{ IIIA}, \text{ AL} = 100 22$	ILD755-2	t _r	-	70	-	μs
Fall time	V -10 V I - + 1 mA B - 100 O	IL755-2	t _f	-	70	-	μs
	$V_{CC} = 10 \text{ V}, I_F = \pm 1 \text{ mA}, R_L = 100 \Omega$	ILD755-2	t _f	-	70	-	μs

SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Climatic classification	According to IEC 68 part 1		55 / 100 / 21				
Comparative tracking index		CTI	175				
Maximum rated withstanding isolation voltage	t = 1 min	V _{ISO}	4420	V_{RMS}			
Maximum transient isolation voltage		V _{IOTM}	10 000	V _{peak}			
Maximum repetitive peak isolation voltage		V_{IORM}	890	V_{peak}			
Isolation resistance	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω			
Isolation resistance	$V_{IO} = 500 \text{ V}, T_{amb} = 100 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹¹	Ω			
Output safety power		P _{SO}	400	mW			
Input safety current		I _{SI}	275	mA			
Safety temperature		T _S	175	°C			
Creepage distance			≥ 7	mm			
Clearance distance			≥ 7	mm			
Insulation thickness		DTI	≥ 0.4	mm			

Note

• As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits

TYPICAL CHARACTERSITICS (T_{amb} = 25 °C, unless otherwise specified)

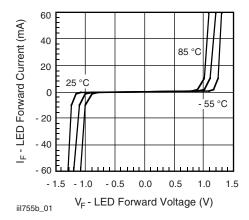


Fig. 1 - LED Forward Current vs. Forward Voltage

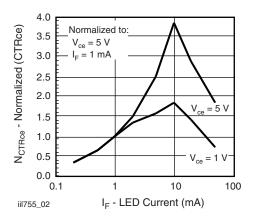


Fig. 2 - Normalized Non-Saturated and Saturated CTR_{CE} vs. LED Current

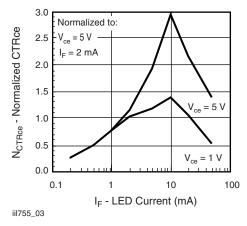


Fig. 3 - Normalized Non-Saturated and Saturated CTR $_{\rm CE}$ vs. LED Current

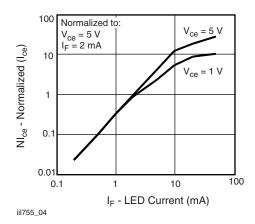


Fig. 4 - Normalized Non-Saturated and Saturated I_{CE} vs. LED Current

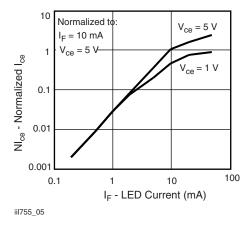


Fig. 5 - Normalized Non-Saturated and Saturated Collector-Emitter Current vs. LED Current

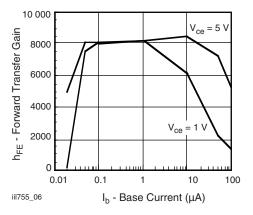


Fig. 6 - Non-Saturated and Saturated h_{FE} vs. Base Current

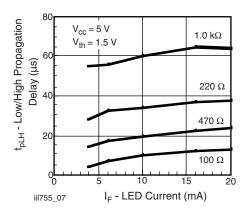


Fig. 7 - Low to High Propagation Delay vs. Collector Load Resistance and LED Current

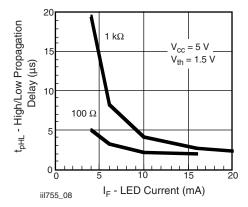


Fig. 8 - High to Low Propagation Delay vs. Collector Load Resistance and LED Current

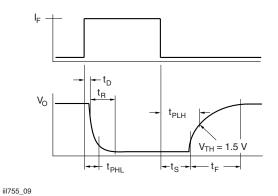


Fig. 9 - Switching Waveform

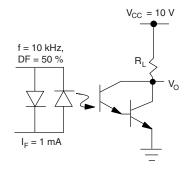
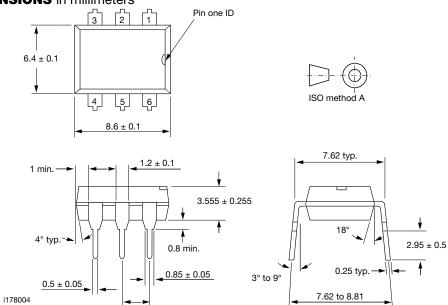


Fig. 10 - Test Circuit, Saturated and Non-Saturated Operation

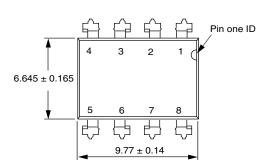
iil755_10



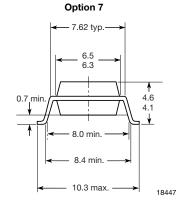
PACKAGE DIMENSIONS in millimeters

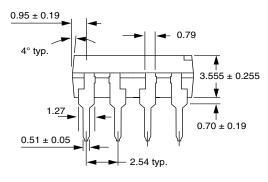


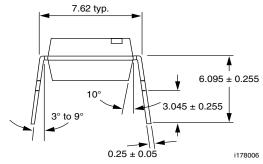
2.54 typ.



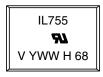








PACKAGE MARKING (example)



Notes

- The VDE logo is only marked on option 1 parts
- Tape and reel suffix (T) is not part of the package marking



Vishay

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