

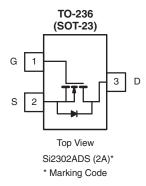
# N-Channel 2.5-V (G-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)				
20	$0.060 \text{ at V}_{GS} = 4.5 \text{ V}$	2.4				
	$0.115 \text{ at V}_{GS} = 2.5 \text{ V}$	2.0				

### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si2302ADS-T1-E3 (Lead (Pb)-free)

Si2302ADS-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted)						
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		$V_{DS}$	20		V	
Gate-Source Voltage		$V_{GS}$	± 8			
Outlines Durin Outline /T 450 00\8	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	2.4	2.1	А	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		1.9	1.7		
Pulsed Drain Current <sup>a</sup>		I <sub>DM</sub>	10		A	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	0.94	0.6		
Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	P <sub>D</sub>	0.9	0.7	W	
rower dissipation	T <sub>A</sub> = 70 °C		0.57	0.46		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 t	o 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 5 s	D	115	140	°C/W
	Steady State	R <sub>thJA</sub>	140	175	

a. Surface mounted on FR4 board.

For SPICE model information via the Worldwide Web: www.vishay.com/www/product/spice.htm



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V, } I_{D} = 10  \mu\text{A}$				V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 50 \mu A$	0.65	0.95	1.2	V	
Gate Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	1	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V			0.1	μА	
	IDSS	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			2.0		
On-State Drain Current <sup>a</sup>	1	$V_{DS} \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	6			A	
On-State Drain Current	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 2.5 \text{ V}$	4				
Drain-Source On-Resistance <sup>a</sup>	В	$V_{GS} = 4.5 \text{ V}, I_D = 3.6 \text{ A}$	0.045 0.00		0.060 <sup>b</sup>		
	R <sub>DS(on)</sub>	$V_{GS} = 2.5 \text{ V}, I_D = 3.1 \text{ A}$		0.070	0.115	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 5 \text{ V}, I_{D} = 3.6 \text{ A}$		8		S	
Diode Forward Voltage	V <sub>SD</sub>	$I_S = 0.94 \text{ A}, V_{GS} = 0 \text{ V}$		0.76	1.2	V	
Dynamic							
Total Gate Charge	$Q_g$			4.0	10		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 3.6 \text{ A}$		0.65		nC	
Gate-Drain Charge	$Q_{gd}$			1.5			
Input Capacitance	C <sub>iss</sub>			300			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		120		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			80			
Gate Resistance	R <sub>q</sub>	f = 1 MHz	0.5	1	2	Ω	
Switching	, ,		ı				
Turn-On Delay Time	t <sub>d(on)</sub>			7	15		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 2.8 $\Omega$		55	80	ns	
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D\cong 3.6$ A, $V_{GEN}=4.5$ V, $R_g=6$ $\Omega$		16	60		
Fall Time	t <sub>f</sub>	•		10	25		

### Notes:

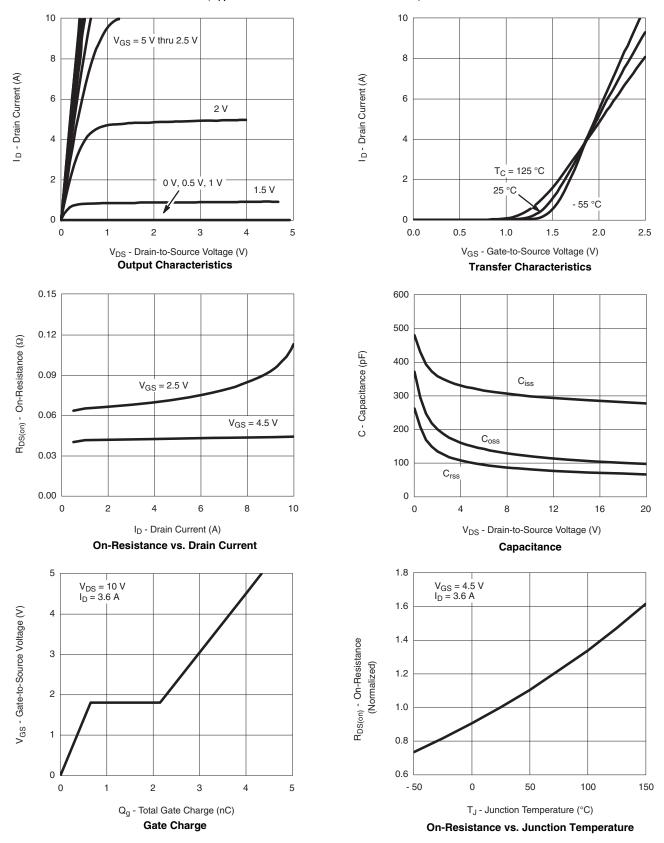
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; PW  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.

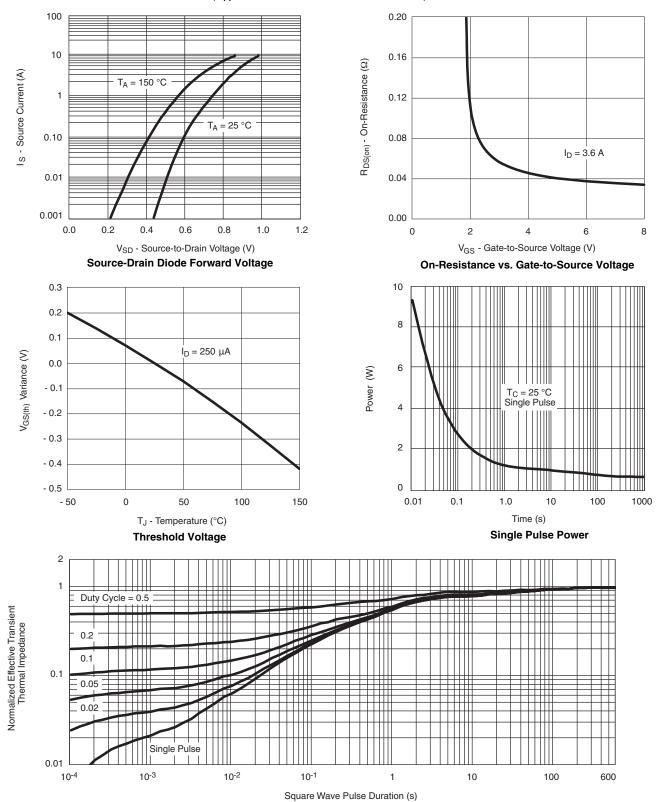
b. Effective for production 10/04.



### **TYPICAL CHARACTERISTICS** ( $T_A = 25$ °C, unless otherwise noted)



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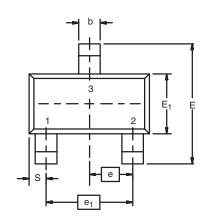


Normalized Thermal Transient Impedance, Junction-to-Ambient

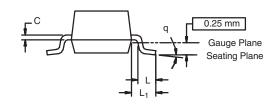
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### SOT-23 (TO-236): 3-LEAD







Dim	MILLI	METERS	INCHES			
	Min	Max	Min	Max		
Α	0.89	1.12	0.035	0.044		
A <sub>1</sub>	0.01	0.10	0.0004	0.004		
A <sub>2</sub>	0.88	1.02	0.0346	0.040		
b	0.35	0.50	0.014	0.020		
С	0.085	0.18	0.003	0.007		
D	2.80	3.04	0.110	0.120		
E	2.10	2.64	0.083	0.104		
E <sub>1</sub>	1.20	1.40	0.047	0.055		
е	0.95 BSC		0.037	0.0374 Ref		
e <sub>1</sub>	1.90 BSC		0.074	0.0748 Ref		
L	0.40	0.60	0.016	0.024		
L <sub>1</sub>	0.64 Ref		0.025	5 Ref		
S	0.50 Ref		0.020	) Ref		
q	3°	8°	3°	8°		
FCN: S-03946-Rev K 09-	lul-01	•				

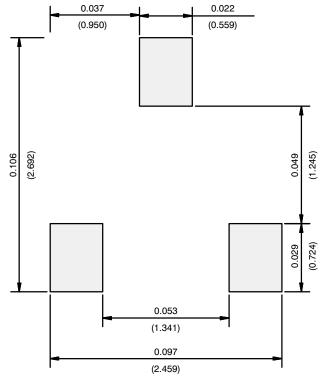
ECN: S-03946-Rev. K, 09-Jul-01

DWG: 5479

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### **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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