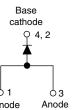
Vishay Semiconductors

High Performance Schottky Rectifier, 3.5 A



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D-PAK (

(TO-252AA)	

PRODUCT SUMMARY								
Package	TO-252AA (D-PAK)							
I _{F(AV)}	3.5 A							
V _R	60 V							
V _F at I _F	See Electrical table							
I _{RM}	30 mA at 125 °C							
T _J max.	150 °C							
Diode variation	Single die							
E _{AS}	6 mJ							

FEATURES

- Popular D-PAK outline
- · Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-30WQ06FNPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL CHARACTERISTICS VALUES								
I _{F(AV)}	Rectangular waveform	3.5	А					
V _{RRM}		60	V					
I _{FSM}	t _p = 5 μs sine	490	А					
V _F	3 A _{pk} , T _J = 125 °C	0.53	V					
TJ		-40 to +150	°C					

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-30WQ06FNPbF	UNITS						
Maximum DC reverse voltage	V _R	60	V						
Maximum working peak reverse voltage	V _{RWM}	00	v						

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST COND	DITIONS	VALUES	UNITS				
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_{C} = 133 °	3.5						
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	490	A				
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse		70					
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 1 \text{ A}, L = 12 \text{ m}$	6.0	mJ					
Repetitive avalanche current	I _{AR}	Current decaying linearly to ze Frequency limited by T _J maxir	1.0	А					







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ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS					
Maximum forward voltage drop See fig. 1		3 A	T.I = 25 °C	0.61	V				
	V _{FM} ⁽¹⁾	6 A	1j=25 0	0.76					
	VFM ("	3 A	T.I = 125 °C	0.53					
		6 A	$I_{\rm J} = 125$ C	0.65					
Maximum reverse leakage current	I _{BM} ⁽¹⁾	T _J = 25 °C		2	mA				
See fig. 2	IRM \''	T _J = 125 °C	$V_R = Rated V_R$	30					
Threshold voltage	V _{F(TO)}	T T movimum	•	0.38	V				
Forward slope resistance	r _t		$T_J = T_J$ maximum		mΩ				
Typical junction capacitance	CT	$V_{R} = 5 V_{DC}$ (test signal range	145	pF					
Typical series inductance	L _S	Measured lead to lead 5 mm	5.0	nH					
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs					

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

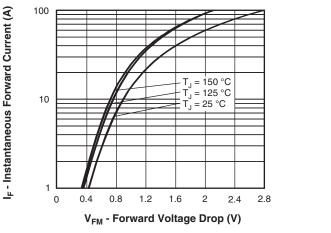
THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS						
Maximum junction and storage temperature range	T_{J} ⁽¹⁾ , T_{Stg}		-40 to +150	°C						
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	4.7	°C/W						
Approximate weight			0.3	g						
Approximate weight			0.01	oz.						
Marking device		Case style D-PAK (similar to TO-252AA)	30WQ(06FN						

Note

 $^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

VS-30WQ06FNPbF

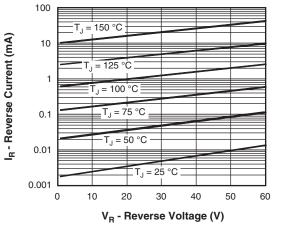
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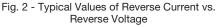


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Fig. 1 - Maximum Forward Voltage Drop Characteristics





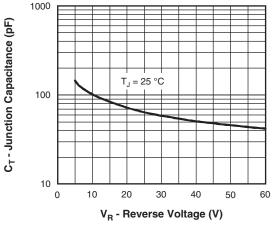


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

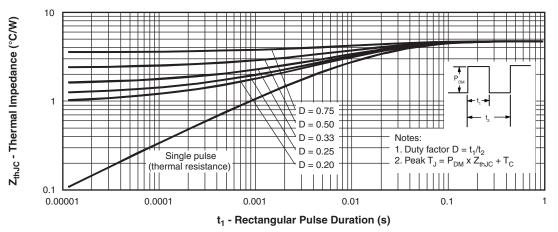
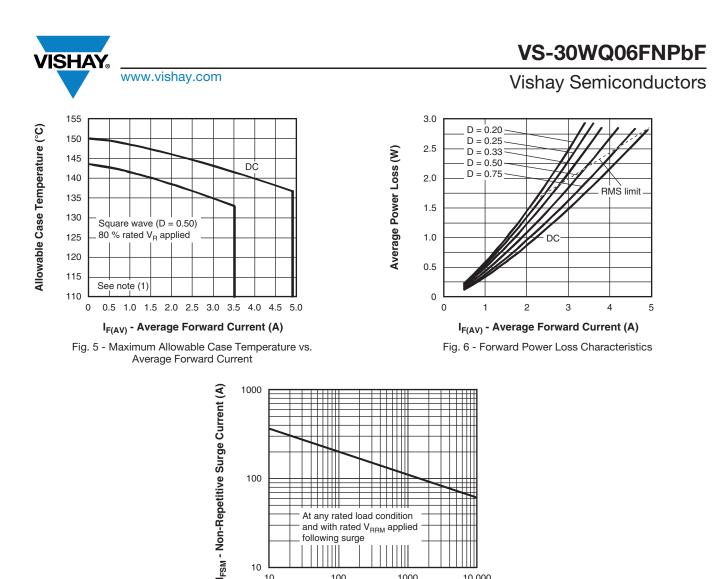


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



and with rated $V_{\rm RRM}$ applied

t_p - Square Wave Pulse Duration (μs) Fig. 7 - Maximum Non-Repetitive Surge Current

1000

10 000

following surge

100

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $\begin{array}{l} \mathsf{Pd} = \mathsf{forward power loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} x \, \mathsf{V}_{\mathsf{FM}} \, \mathsf{at} \, (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \, (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse power loss} = \mathsf{V}_{\mathsf{R1}} \, x \, \mathsf{I}_{\mathsf{R}} \, (\mathsf{1} - \mathsf{D}); \, \mathsf{I}_{\mathsf{R}} \, \mathsf{at} \, \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \, \, \% \, \, \mathsf{rated} \, \mathsf{V}_{\mathsf{R}} \end{array}$

10 10

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ORDERING INFORMATION TABLE

Device code	VS-	30	w	Q	06	FN	TRL	PbF		
	1	2	3	4	5	6	7	8		
	1 - 2 -		Vishay Semiconductors product Current rating (3.5 A)							
	3 -	Pac	Package identifier:							
	4 -	••	W = D-PAK Schottky "Q" series							
	5 - 6 -									
	7 -		• None = tube (50 pieces)							
	 TR = tape and reel TRL = tape and reel (left oriented) 									
	8 -		• TRR = tape and reel (right oriented) PbF = lead (Pb)-free							

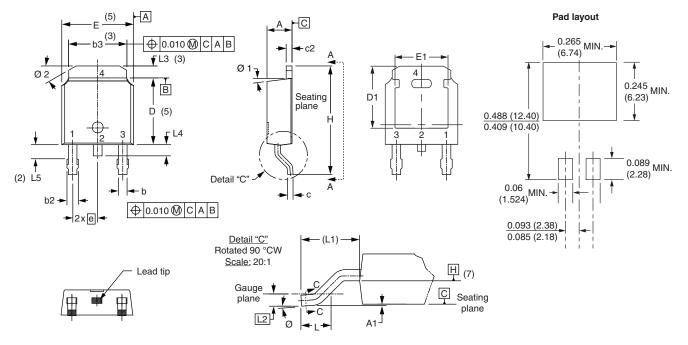
LINKS TO RELATED DOCUMENTS							
Dimensions	www.vishay.com/doc?95016						
Part marking information	www.vishay.com/doc?95059						
Packaging information	www.vishay.com/doc?95033						
SPICE model	www.vishay.com/doc?95687						





D-PAK (TO-252AA)

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	HES	NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMIDUL	MIN.	MAX.	MIN.	MAX.	NOTES		STINDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC	
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410	
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070	
b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.	
b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020	BSC	
С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050	3
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040	
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060	2
D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°	
E	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°	
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°	

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

(2) Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Section C - C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip

(5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁶⁾ Dimension b1 and c1 applied to base metal only

⁽⁷⁾ Datum A and B to be determined at datum plane H

⁽⁸⁾ Outline conforms to JEDEC outline TO-252AA

1



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