# SB2D, SB2G, SB2J, SB2K, SB2M

Vishay General Semiconductor

# **Surface-Mount Glass Passivated Rectifier**



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SMB (DO-214AA)

Cathode O Anode

## **ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub>	2.0 A					
V <sub>RRM</sub>	200 V, 400 V, 600 V, 800 V, 1000 V					
I <sub>FSM</sub>	55 A					
I <sub>R</sub>	1.0 µA					
$V_F$ at $I_F$ = 2.0 A	0.86 V					
T <sub>J</sub> max.	150 °C					
Package	SMB (DO-214AA)					
Circuit configuration	Single					

#### FEATURES

- Low profile package
- Ideal for automated placement
- Glass passivated chip junction
- Low forward voltage drop
- Low leakage current
- High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

## **TYPICAL APPLICATIONS**

For use in general purpose rectification of power supplies, inverters, converters and freewheeling diodes for consumer and telecommunication.

#### **MECHANICAL DATA**

Case: SMB (DO-214AA)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS compliant, and commercial grade

**Terminals:** natte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: color band denotes the cathode end

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	SYMBOL	SB2D	SB2G	SB2J	SB2K	SB2M	UNIT
Device marking code		B2D	B2G	B2J	B2K	B2M	
Maximum repetitive peak reverse voltage		200	400	600	800	1000	V
Maximum DC forward current (fig. 1)	I <sub>F</sub> <sup>(1)</sup>	2.0				А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	55			А		
Operating and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150 °C				°C	

Note

<sup>(1)</sup> Mounted on 8 mm x 8 mm pad areas, 1 oz. FR4 PCB



FREE





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ELECTRICAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT	
Instantaneous forward voltage	I <sub>F</sub> = 1.0 A	T₄ = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.90	-	V	
	I <sub>F</sub> = 2.0 A	$I_A = 25 C$		0.96	1.15		
	I <sub>F</sub> = 1.0 A	T <sub>A</sub> = 125 °C		0.78	-		
	I <sub>F</sub> = 2.0 A	T <sub>A</sub> = 125 C		0.86	1.05		
Reverse current	Rated V <sub>B</sub>	T <sub>A</sub> = 25 °C	- I <sub>R</sub> <sup>(2)</sup>	0.15	1.0	μA	
	naleu v <sub>R</sub>	T <sub>A</sub> = 125 °C		36	125		
Typical reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = I <sub>rr</sub> = 0.25 A	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1.0 A, I <sub>rr</sub> = 0.25 A		2.0		μs	
Typical junction capacitance	Rated V <sub>B</sub> = 4.	Rated $V_{B} = 4.0 V, 1 MHz$		16		pF	

Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width,  $\leq$  40 ms

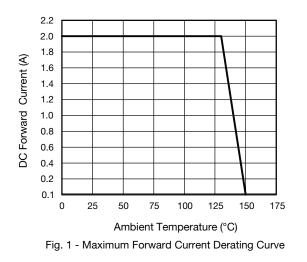
<b>THERMAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	SB2D	SB2G	SB2J	SB2K	SB2M	UNIT
Typical thermal resistance	R <sub>0JA</sub> <sup>(1)</sup>	70					°C/W
Typical thermal resistance	R <sub>eJM</sub> <sup>(1)</sup>	10					

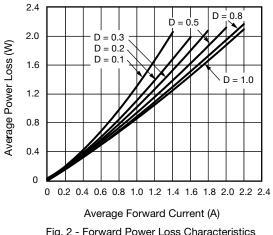
#### Note

<sup>(1)</sup> Units mounted on PCB with 8.0 mm x 8.0 mm copper pad areas, 1 oz. FR4 PCB; R<sub>0JA</sub> - junction-to-ambient R<sub>0JM</sub> - junction-to-mount

ORDERING INFORMATION (Example)								
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE				
SB2J-M3/52T	0.096	52T	750	7" diameter plastic tape and reel				
SB2J-M3/5BT	0.096	5BT	3200	13" diameter plastic tape and reel				

## **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)







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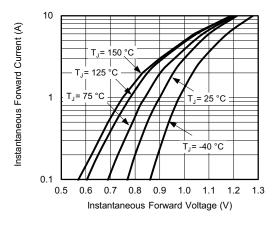


Fig. 3 - Typical Instantaneous Forward Characteristics

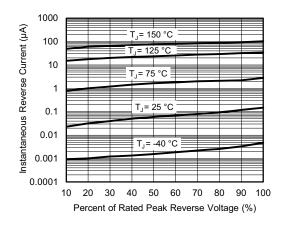
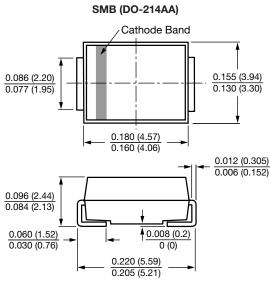


Fig. 4 - Typical Reverse Characteristics

## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



## 100 T<sub>J</sub> = 25 °C f = 1.0 MHz Junction Capacitance (pF) $V_{STG} = 50 \text{ mV}_{p-p}$ 10 1 0.1 1 10 100 Reverse Voltage (V)

Fig. 5 - Typical Junction Capacitance

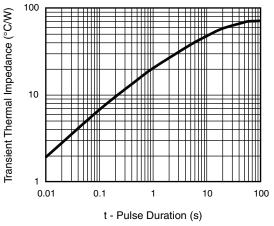
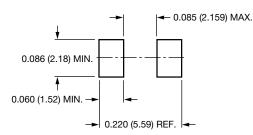


Fig. 6 - Typical Transient Thermal Impedance

#### Mounting Pad Layout



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