VCAN24A2-HT5

Vishay Semiconductors

Bidirectional Symmetrical (BiSy) Low Capacitance, Dual-Line ESD Protection Diode in DFN1110-3A



DFN1110-3A

MARKING (example only)



www.vishay.com

Dot = pin marking X = date code

Y = type code (see table below)

LINKS TO ADDITIONAL RESOURCES



Application Notes

FEATURES

- For CAN FD Bus applications
- Small DFN1110-3A
- 2-line ESD protection
- Working range ±24 V
- Low leakage current I_R < 0.05 μA
- Low load capacitance $C_D < 6 \text{ pF}$ (at $V_R = 5 \text{ V}$)
- ESD immunity acc. IEC 61000-4-2 ± 30 kV contact discharge ± 30 kV air discharge
- ESD capability according to AEC-Q101: human body model: class H3B: > 8 kV
- e3 pins side wall plated with tin (Sn)
- AOI capable
- AEC-Q101 qualified available
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

AUTOMOTIVE GRADE Available					
Pb-free					
(e3)					
ROHS COMPLIANT					
HALOGEN					
<u>GREEN</u> (5-2008)					

ORDERING INFORMATION							
	ENVIR	ONMENTAL AND QUALITY C	ODE	PACKAGING CODE			
PART NUMBER (EXAMPLE)	AEC-Q101 QUALIFIED	RoHS-COMPLIANT + LEAD (Pb)-FREE TERMINATIONS	TIN PLATED	10K PER 7" REEL (8 mm TAPE)	ORDERING CODE (EXAMPLE)		
	QUALIFIED	GREEN	FLATED	10K = MOQ			
VCAN24A2-HT5	-	G	3	-08	VCAN24A2-HT5-G3-08		
VCAN24A2-HT5	Н	G	3	-08	VCAN24A2-HT5HG3-08		

PACKAGE DATA						
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VCAN24A2-HT5	DFN1110-3A	А	1.43 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	T_A = 25 °C, acc. IEC 61000-4-5; t_p = 8/20 $\mu s;$ single shot	I _{PPM}	2.5	А		
Peak pulse power	T_A = 25 °C; pin 1 or 2 to pin 3; acc. IEC 61000-4-5; t_p = 8/20 $\mu s;$ single shot	P _{PP}	100	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses, $T_A = 25 \text{ °C}$	V _{ESD}	± 30	kV		
	Air discharge acc. IEC 61000-4-2; 10 pulses, $T_A = 25 \text{ °C}$	V ESD	± 30	kV		
Operating temperature	Junction temperature	TJ	-55 to +150	°C		
Storage temperature		T _{STG}	-55 to +150	°C		

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For technical questions, contact: ESDprotection@vishay.com

Document Number: 86309

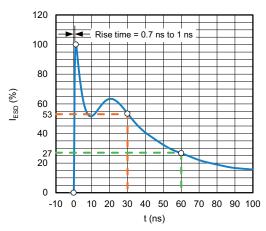
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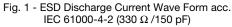


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ELECTRICAL CHARACTERISTICS (pin 1 to 3, 3 to 1, 2 to 3, or 3 to 2) (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITIONS/REMARKS		MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N _{channel}	-	-	2	lines		
Reverse stand-off voltage	Max. reverse working voltage	V _{RWM}	-	-	24	V		
Reverse voltage	At I _R = 0.05 μA	V _R	24	-	-	V		
Reverse current	At V _{RWM} = 24 V	I _R	-	-	0.05	μA		
Reverse breakdown voltage	At I _R = 1 mA	V _{BR}	26.5	28	29.5	V		
Reverse clamping voltage	At I _{PP} 1 A; t _p = 8/20 μs	V _C	-	-	35	V		
	At I _{PP} = I _{PPM} = 2.5 A; t _p = 8/20 μs	V _C	-	36	41	V		
Capacitance	At $V_R = 5 V$, $f = 1 MHz$	CD	-	5	6	pF		
	Diode capacitance matching at $V_R = 5 V$, $C_{D13} vs. C_{D23}$	dC _D	-	-	0.12	pF		





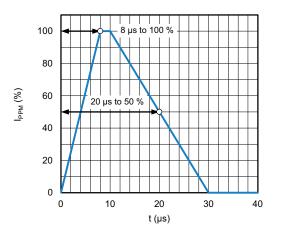


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

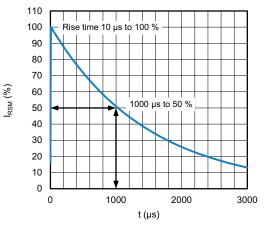


Fig. 3 - 10/1000 µs Peak Pulse Current Wave Form

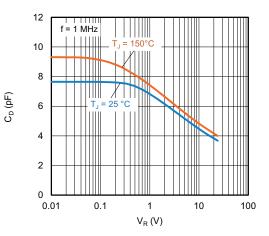


Fig. 4 - Typical Capacitance vs. Reverse Voltage

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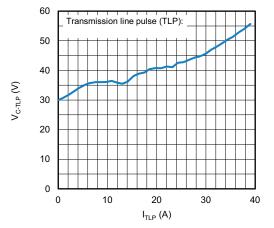


Fig. 5 - Typical Clamping Voltage vs. Peak Pulse Current

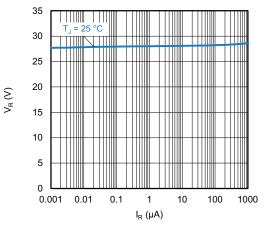


Fig. 6 - Typical Reverse Voltage vs. Reverse Current

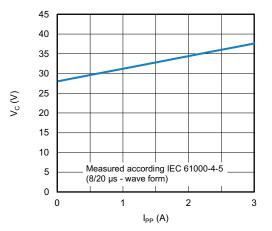


Fig. 7 - Typical Peak Clamping Voltage vs. Peak Pulse Current

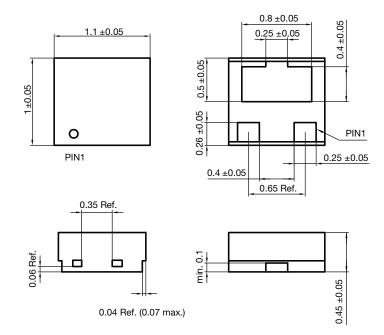
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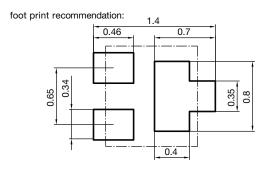
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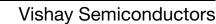


PACKAGE DIMENSIONS in millimeters (inches)



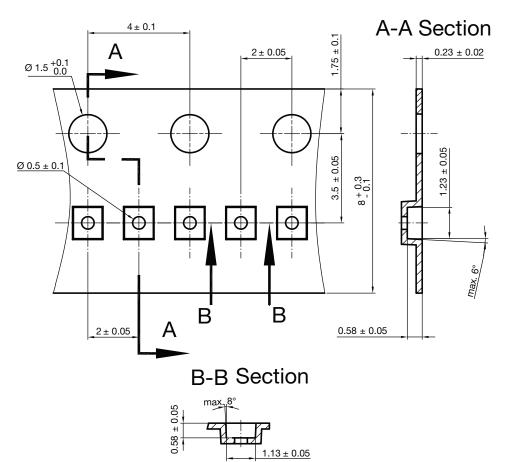


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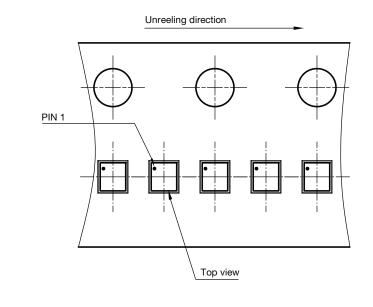
CARRIER TAPE DFN1110-3A



Document no: S8-V-3906.04-065 (4) Package name: DFN1110-3A Created date: 28.10.2019

surface resistance: 10^5 - $10^{11}\frac{OHMS}{SQ}$ Cummulative tolerances of 10 sprocket holes is \pm 0.2 mm

ORIENTATION IN CARRIER TAPE DFN1110-3A



Document no: S8-V-3906.04-066 (4) Package name: DFN1110-3A Created date: 28.10.2019

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