

### Vishay Semiconductors

COMPLIANT

HALOGEN

FREE

## Hyperfast Rectifier, 8 A FRED Pt®



#### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	8 A			
$V_{R}$	600 V			
V <sub>F</sub> at I <sub>F</sub>	1.3 V			
t <sub>rr</sub> (typ.)	16 ns			
T <sub>J</sub> max.	175 °C			
Package	TO-220 FullPAK 2L			
Circuit configuration	Single			

#### **FEATURES**

- · Hyperfast soft recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Fully isolated package (V<sub>INS</sub> = 2500 V<sub>RMS</sub>)
- True 2 pin package
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION / APPLICATIONS**

Hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

#### **MECHANICAL DATA**

Case: TO-220 FullPAK 2L

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per

J-STD-002

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		600	V
Average rectified forward current in DC	I <sub>F(AV)</sub>	T <sub>C</sub> = 114 °C	8	
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	80	Α
Repetitive peak surge current Square wave 20 kHz duty cycle (50 %)	I <sub>FRM</sub>	T <sub>C</sub> = 96 °C	16	
Operating junction and storage temperatures	T <sub>.I</sub> , T <sub>Sta</sub>		-65 to +175	°C

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS MIN. TYP.		MAX.	UNITS	
Breakdown voltage, blocking voltage	$V_{BR}, V_{R}$	I <sub>R</sub> = 100 μA	600	-	-	
Forward voltage	V	I <sub>F</sub> = 8 A	-	2.0	2.65	V
Forward voitage	V <sub>F</sub>	I <sub>F</sub> = 8 A, T <sub>J</sub> = 150 °C	-	1.3	1.85	]
Deverage legicage comment	$V_R = V_R$ rated	-	0.02	12		
Reverse leakage current	I <sub>R</sub>	$T_J = 150 ^{\circ}\text{C},  V_R = V_R  \text{rated}$	-	15	100	μA
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	6	-	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8	-	nH





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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	16	23	
Reverse recovery time		$I_F = 8 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	20	28	
neverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		=.	21	-	ns
		T <sub>J</sub> = 125 °C	$I_F=8~A,$ $dI_F/dt=200~A/\mu s,$ $V_R=390~V$	=.	39	-	
Peak recovery current		T <sub>J</sub> = 25 °C		-	3	-	Α
reak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C		=.	5 -	-	
Devenue receivem charge	0	T <sub>J</sub> = 25 °C		=.	36	-	nC
Reverse recovery charge Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	108	-	110	
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 125 °C	$I_F = 8 A,$ $dI_F/dt = 600 A/\mu s,$ $V_R = 390 V$	=.	30	-	ns
Peak recovery current	I <sub>RRM</sub>			-	13	-	Α
Reverse recovery charge	Q <sub>rr</sub>			-	205	-	nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C
Thermal resistance, junction-to-case	R <sub>thJC</sub>		-	4.6	5.5	
Thermal resistance, junction-to-ambient	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W
Typical thermal resistance, case-to-heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2	-	g
vveignt			-	0.07	-	OZ.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220 FullPAK 2L	ETH0806FP			



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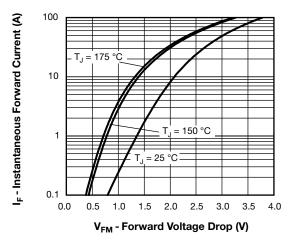


Fig. 1 - Typical Forward Voltage Drop Characteristics

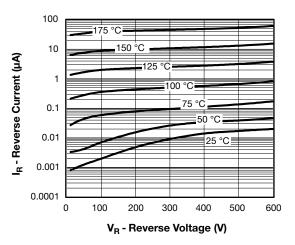


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

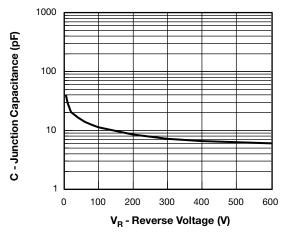


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

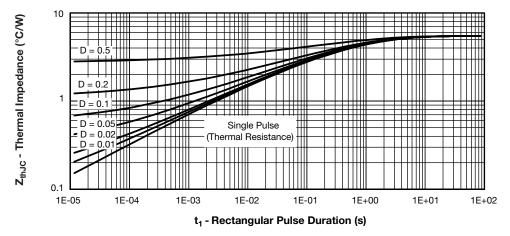


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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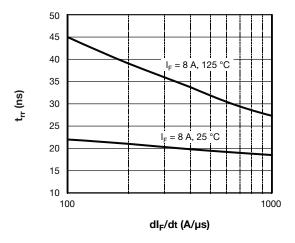


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

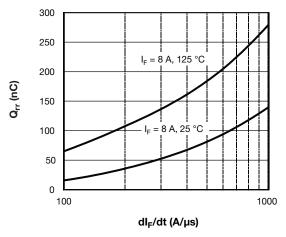


Fig. 6 - Forward Power Loss Characteristics

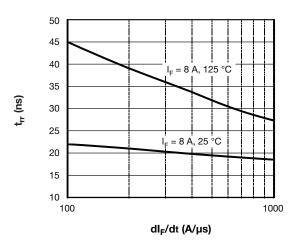


Fig. 7 - Typical Reverse Recovery vs. dl<sub>F</sub>/dt

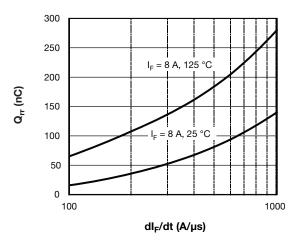
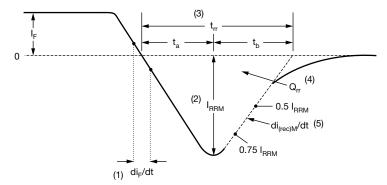


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3)  $t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $I_F$  to point where a line passing through 0.75  $I_{RBM}$  and 0.50  $I_{RBM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{\rm rr}$  area under curve defined by  $\mathbf{t}_{\rm rr}$  and  $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

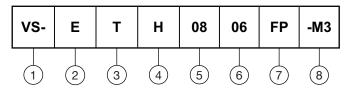
Fig. 9 - Reverse Recovery Waveform and Definitions



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### **ORDERING INFORMATION TABLE**

**Device code** 



Vishay Semiconductors product

2 - Circuit configuration:

E = single

**3** - T = TO-220

4 - H = hyperfast recovery time

5 - Current code: 08 = 8 A

Voltage code: 06 = 600 V

7 - FP = TO-220 FullPAK 2L

8 - Environmental digit:

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-ETH0806FP-M3	50	1000	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96157			
Part marking information	www.vishay.com/doc?95392			
SPICE model	www.vishay.com/doc?96865			



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