



P-Channel 1.8-V (G-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
- 20	0.010 at V _{GS} = - 4.5 V	- 8.8		
	0.013 at V _{GS} = - 2.5 V	- 7.6		
	0.016 at V _{GS} = - 1.8 V	- 6.8		

FEATURES

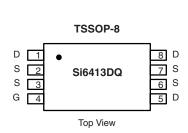
- · Halogen-free
- TrenchFET® Power MOSFET



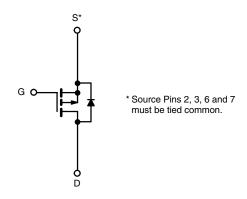
RoHS

APPLICATIONS

- Load Switch
- PA Switch
- Charger Switch



Ordering Information: Si6413DQ-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unles	ss otherwise r	noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 20		٧
Gate-Source Voltage		V _{GS}	± 8		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	- I _D	- 8.8	- 7.2	
	T _A = 70 °C		- 7.0	- 5.7	
Pulsed Drain Current (10 µs Pulse Width)		I _{DM}	- 30		Α
Continuous Source Current (Diode Conduction) ^a		I _S	- 1.35 - 0.95		95
Maximum Power Dissipation ^a	T _A = 25 °C	- P _D	1.5	1.05	W
	T _A = 70 °C		1.0	0.67	
Operating Junction and Storage Temperature Range		T _J , T _{stq}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Manianum lungtion to Ambient	t ≤ 10 s	R _{thJA}	60	83		
Maximum Junction-to-Ambient ^a	Steady State		100	120	°C/W	
Maximum Junction-to-Foot	Steady State		35	45		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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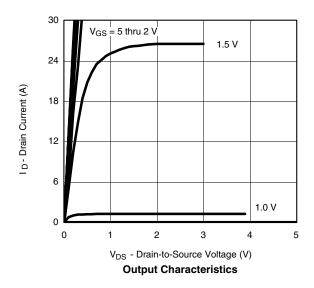
Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit	
Static			1	•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -400 \mu A$	- 0.40		- 0.8	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 16 V, V _{GS} = 0 V		-1		0	
		$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			- 10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 20			Α	
Drain-Source On-State Resistance ^a		V _{GS} = - 4.5 V, I _D = - 8.8 A		0.008	0.010	Ω	
	R _{DS(on)}	V _{GS} = - 2.5 V, I _D = - 7.6 A		0.010	0.013		
		V _{GS} = - 1.8 V, I _D = - 6.8 A		0.013	0.016		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 8.8 A		45		S	
Diode Forward Voltage ^a	V_{SD}	I _S = - 1.3 A, V _{GS} = 0 V		- 0.58	- 1.1	V	
Dynamic ^b							
Total Gate Charge	Q_g			69	105		
Gate-Source Charge	Q_{gs}	V _{DS} = - 10 V, V _{GS} = - 5 V, I _D = - 8.8 A	9.5		nC		
Gate-Drain Charge	Q_{gd}			15.5		1	
Turn-On Delay Time	t _{d(on)}			55	85		
Rise Time	t _r	V_{DD} = - 10 V, R_L = 10 Ω		120	200	ns	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 1 A, V_GEN = - 4.5 V, R_G = 6 Ω		305	470		
Fall Time	t _f			160	250		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.3 A, di/dt = 100 A/μs		90	150		

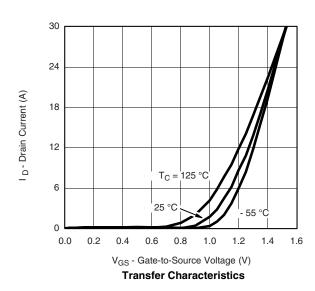
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

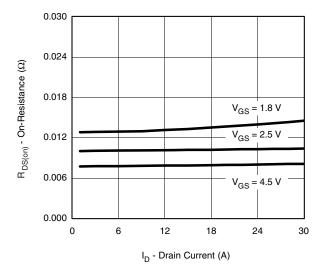




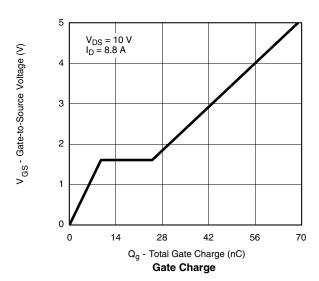


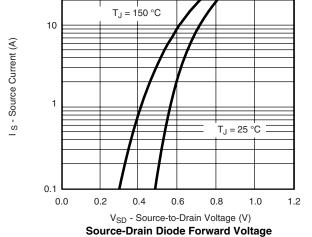


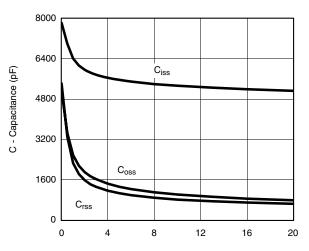
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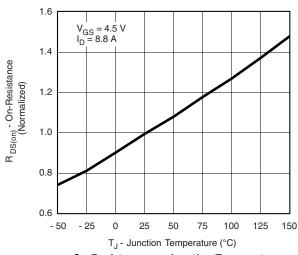
On-Resistance vs. Drain Current



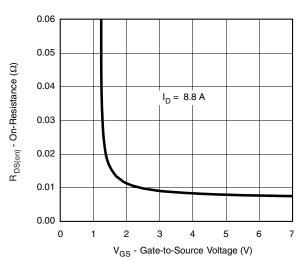




V_{DS} - Drain-to-Source Voltage (V) **Capacitance**



On-Resistance vs. Junction Temperature



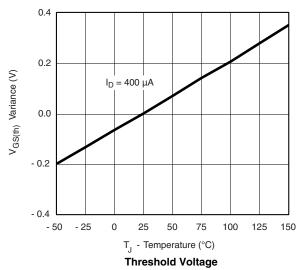
On-Resistance vs. Gate-to-Source Voltage

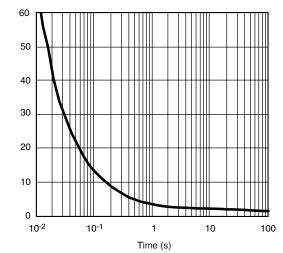
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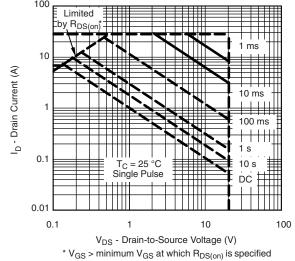
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



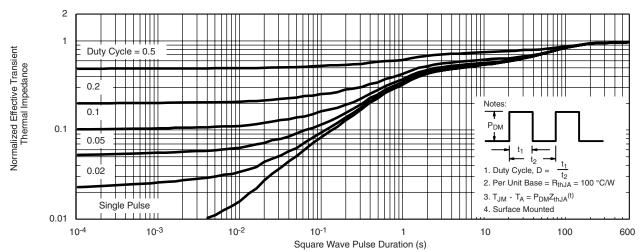


Single Pulse Power, Junction-to-Ambient



Power (W)

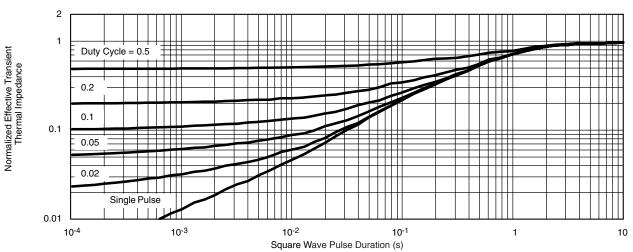
Safe Operating Area, Junction-to-Case



Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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