

Vishay General Semiconductor

Surface-Mount TMBS[®] (Trench MOS Barrier Schottky) Rectifier



Cathode O Anode

LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	3.0 A			
V _{RRM}	100 V			
I _{FSM}	60 A			
E _{AS}	24 mJ			
V_F at $I_F = 3.0$ A	0.62 V			
T _J max.	150 °C			
Package	SMA (DO-214AC)			
Circuit configurations	Single			

FEATURES

- Low profile package
- · Ideal for automated placement
- Trench MOS Schottky technology
- · Low power losses, high efficiency
- Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: SMA (DO-214AC) Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 2 whisker test

Polarity: color band denotes the cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	VSSA310S	UNIT	
Device marking code		V3B		
Maximum repetitive peak reverse voltage	V _{RRM}	100	V	
Maximum DC forward current	I _F ⁽¹⁾	3.0	Α	
	I _F ⁽²⁾	1.7		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	60	A	
Non-repetitive avalanche energy at $T_J = 25 \text{ °C}$, L = 60 mH	E _{AS}	24	mJ	
Peak repetitive reverse current at $t_p = 2 \ \mu s$, 1 kHz, T _J = 38 °C ± 2 °C	I _{RRM}	1.0	A	
Operating junction and storage temperature range	T _J , T _{STG}	-40 to +150	°C	

Notes

⁽¹⁾ Mounted on 10 mm x 10 mm pad areas, 1 oz. FR4 PCB

⁽²⁾ Free air, mounted on recommended copper pad area



VSSA310S



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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CON	TEST CONDITIONS		TYP.	MAX.	UNIT
Breakdown voltage	I _R = 1.0 mA	T _A = 25 °C	V _{BR}	100 (minimum)	-	V
Instantaneous forward voltage	I _E = 3.0 A	T _A = 25 °C	V _F ⁽¹⁾	0.71	0.80	V
	$I_{\rm F} = 3.0 \rm{A}$	T _A = 125 °C		0.62	0.70	
Reverse current	$\lambda = 70 \lambda$	T _A = 25 °C	I _R (2)	1.0	-	μA
	V _R = 70 V	T _A = 125 °C		0.95	-	mA
	V 100 V	T _A = 25 °C		3.5	150	μA
	V _R = 100 V	T _A = 125 °C		2.2	15	mA
Typical junction capacitance	4.0 V, 1 MHz		CJ	175	-	pF

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)				
PARAMETER	SYMBOL	VSSA310S	UNIT	
Typical thermal resistance	R _{0JA} ⁽¹⁾	135	°C/W	
	R _{0JM} ⁽²⁾	25		

Notes

 $^{(1)}$ Free air, mounted on recommended PCB 1 oz. pad area. Thermal resistance $R_{\theta JA}$ - junction to ambient

 $^{(2)}$ Units mounted on PCB with 10 mm x 10 mm copper pad areas. $R_{\theta JM}$ - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
VSSA310S-E3/61T	0.064	61T	1800	7" diameter plastic tape and reel	
VSSA310S-E3/5AT	0.064	5AT	7500	13" diameter plastic tape and reel	



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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

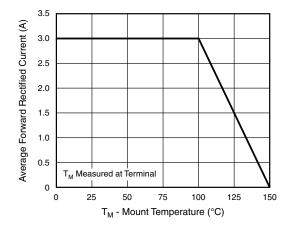


Fig. 1 - Maximum Forward Current Derating Curve

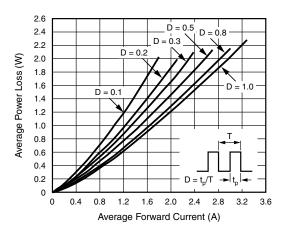


Fig. 2 - Forward Power Loss Characteristics

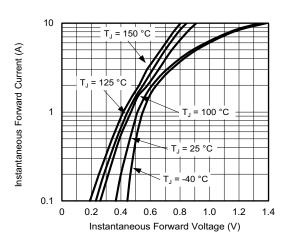


Fig. 3 - Typical Instantaneous Forward Characteristics

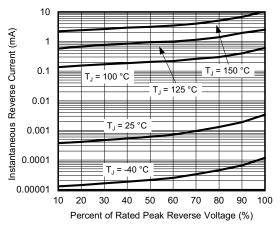


Fig. 4 - Typical Reverse Characteristics

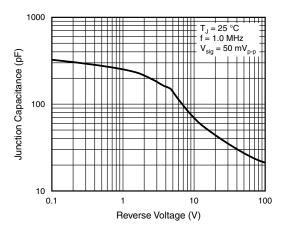


Fig. 5 - Typical Junction Capacitance

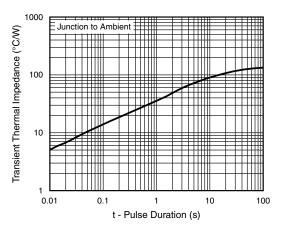


Fig. 6 - Typical Transient Thermal Impedance

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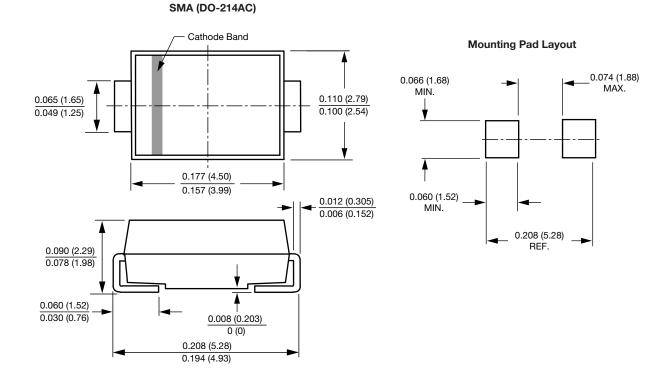
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

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